

A Brookes-Style Denotational Semantics for Release/Acquire Concurrency

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We present a compositional denotational semantics for a functional language with first-class parallel composition and shared-memory operations whose operational semantics follows the Release/Acquire weak memory model (RA). The semantics is formulated in Moggi's monadic approach, and is based on Brookes-style traces. To do so we adapt Brookes's traces to Kang et al.'s view-based machine for RA, and supplement Brookes's mumble and stutter closure operations with additional operations, specific to RA. The latter provides a more nuanced understanding of traces that uncouples them from operational interrupted executions. We show that our denotational semantics is adequate and use it to validate various program transformations of interest. This is the first work to put weak memory models on the same footing as many other programming effects in Moggi's standard monadic approach.

CCS Concepts: • **Theory of computation** → **Denotational semantics; Parallel computing models; Functional constructs; Program analysis.**

Additional Key Words and Phrases: Weak memory models, Release/Acquire, Shared state, Shared memory, Concurrency, Denotational semantics, Monads, Program refinement, Program equivalence, Compiler optimizations

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1 Introduction

Denotational semantics defines the meaning of programs *compositionally*, where the meaning of a program term is a function of the meanings assigned to its immediate syntactic constituents. This key feature makes denotational semantics instrumental in understanding the meaning a piece of code independently of the context under which the code will run. This style of semantics contrasts with standard operational semantics, which only executes closed/whole programs. A basic requirement of such a denotation function $\llbracket - \rrbracket$ is for it to be *adequate* w.r.t. a given operational semantics: plugging program terms M and N with equal denotations—i.e. $\llbracket M \rrbracket = \llbracket N \rrbracket$ —into some program context $\Xi[-]$ that closes over their variables, results in observationally indistinguishable closed programs in the given operational semantics. Moreover, assuming that denotations have a defined order (\leq), a “directed” version of adequacy ensures that $\llbracket M \rrbracket \leq \llbracket N \rrbracket$ implies that all behaviors exhibited by $\Xi[M]$ under the operational semantics are also exhibited by $\Xi[N]$.

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For shared-memory concurrent programming, Brookes’s seminal work [13] defined a denotational semantics, where the denotation $\llbracket M \rrbracket$ is a set of totally ordered traces of M closed under certain operations, called stutter and mumble. Traces consist of sequences of memory snapshots that M guarantees to provide while relying on its environment to make other memory snapshots. Brookes [12] used the insights behind this semantics to develop a model for separation logic, and Turon and Wand [50] used them to design a separation logic for refinement. Additionally, Xu et al. [52] used traces as a foundation for the Rely/Guarantee approach for verification of concurrent programs, and Liang et al., Liang et al. [36, 37] used a trace-based program logic for refinement.

A *memory model* decides what outcomes an execution of a program can have. Brookes [13] established the adequacy of the trace-based denotational semantics w.r.t. the operational semantics of the strongest model, known as *sequential consistency* (SC), where every memory access happens instantaneously and immediately affects all concurrent threads. However, SC is too strong to model real-world shared memory, whether it be of modern hardware, such as x86-TSO [42, 47] and ARM, or of programming languages such as C/C++ and Java [4, 39]. These runtimes follow *weak memory models* that allow performant implementations, but admit more behaviors than SC.

Do weak memory models admit adequate Brookes-style denotational semantics? This question has been answered affirmatively once, by Jagadeesan et al. [25], who closely followed Brookes to define denotational semantics for x86-TSO. Other weak memory models, in particular, models of *programming languages*, and *non-multi-copy-atomic* models, where writes can be observed by different threads in different orders, were so far out of reach of Brookes’s totally ordered traces, only captured by much more sophisticated models based on *partial orders* [15, 19, 24, 26, 29, 43].

In this paper we target the Release/Acquire memory model (RA, for short). This model, obtained by restricting the C/C++11 memory model to Release/Acquire atomics, is a well-studied fundamental memory model weaker than x86-TSO, which, roughly speaking, ensures “causal consistency” together with “per-location-SC” and “RMW (read-modify-write) atomicity” [30, 31]. These assurances make RA sufficiently strong for implementing common synchronization idioms. RA allows more performant implementations than SC, since, in particular, it allows the reordering of a write followed by a read from a different location, which is commonly performed by hardware, and it is non-multi-copy-atomic, thus allowing less centralized architectures like POWER [48].

Our first contribution is a Brookes-style denotational semantics for RA. As Brookes’s traces are totally ordered, this result may seem counterintuitive. The standard semantics for RA is a declarative (a.k.a. axiomatic) memory model, in the form of acyclicity consistency constraints over partially ordered candidate execution graphs. Since these graphs are not totally ordered, one might expect that Brookes’s traces are insufficient. Nevertheless, our first key observation is that an *operational* presentation of RA as an interleaving semantics of a weak memory system lends itself to Brookes-style semantics. For that matter, we develop a notion of traces compatible with Kang et al.’s “view-based” machine [28], an operational semantics that is equivalent to RA’s declarative formulation. Our main technical result is the (directed) adequacy of the proposed Brookes-style semantics w.r.t. that operational semantics of RA.

A main challenge when developing a denotational semantics lies in making it sufficiently abstract. While *full* abstraction is often out of reach, as a yardstick, we want our semantics to be able to justify various compiler transformations/optimizations that are known to be sound under RA [51]. Indeed, an immediate practical application of a denotational semantics is the ability to provide *local* formal justifications of program transformations, such as those performed by optimizing compilers. In this setting, to show that an optimization $N \rightarrow M$ is valid amounts to showing that replacing N by M anywhere in a larger program does not introduce new behaviors, which follows from $\llbracket M \rrbracket \leq \llbracket N \rrbracket$ given a directionally adequate denotation function $\llbracket - \rrbracket$.

To support various compiler transformations, we close our denotations under certain operations, including analogs to Brookes’s stutter and mumble, but also several RA-specific operations, that allow us to relate programs which would naively correspond to rather different sets of traces. Given these closure operations, our semantics validates standard program transformations, including structural transformations, algebraic laws of parallel programming, and all known thread-local RA-valid compiler optimizations. Thus, the denotational semantics is instrumental in formally establishing validity of transformations under RA, which is a non-trivial task [19, 51].

Our second contribution is to connect the core semantics of parallel programming languages exhibiting weak behaviors to the more standard semantic account for programming languages with effects. Brookes presented his semantics for a simple imperative WHILE language, but Benton et al., Dvir et al. [6, 20] later recast it atop Moggi’s monad-based approach [40] which uses a functional, higher-order core language. In this approach the core language is modularly extended with effect constructs to denote program effects. In particular, we define parallel composition as a first-class operator. This is in contrast to most of the research of weak memory models that employ imperative languages and assume a single top-level parallel composition.

A denotational semantics given in this monadic style comes ready-made with a rich semantic toolkit for program denotation [7], transformations [5, 8–10, 23], reasoning [2, 38], etc. We challenge and reuse this diverse toolkit throughout the development. We follow a standard approach and develop specialized logical relations [45, 49] to establish the compositionality property of our proposed semantics; its soundness, which allows one to use the denotational semantics to show that certain outcomes are impossible under RA; and adequacy. This development puts weak memory models, which often require bespoke and highly specialized presentations, on a similar footing to many other programming effects.

Outline. In §2 we overview the Release/Acquire operational semantics and the trace-based denotational semantics that we use and extend in this paper. In §3 we summarize our contributions.

The rest of the paper goes into further detail. In §4 we present the programming language syntax and typing system, which in §5 we equip with an extended presentation of the RA operational semantics, along with observations that will support our definition of traces. In §6 we define our trace-based denotational semantics for RA, and in §7 we work up to and establish our main results. Finally, we conclude and discuss related work in §8.

Comparing to the conference version. The conference version of this paper [21] is covered here by §§1-3 and 8, roughly speaking. The rest of this paper extends the conference version. Here, definitions and theorems are formally specified and proved. This account also provides a more detailed discussion and more examples. By expanding in breadth and depth, we state (and prove) some results in a stronger form here, such as the denotational semantics supporting transformations involving arbitrary RMWs; and a tighter characterization of the commutativity of rewrite rules.

2 Preliminaries

We overview previous work and our slight variations on it to facilitate the abridged discussion of our contribution (§3). Particularly, we partially present the language and its operational semantics under the Sequential Consistency (SC) memory model (§2.1), Brookes’s denotational semantics for SC (§2.2), and Kang et al.’s operational presentation of the RA memory model (§2.3). See §4 for the full language, and §5 for a detailed account of the RA operational semantics.

2.1 Language and Operational Semantics

The programming language we use is an extension of a functional language with shared-state constructs. Program terms M and N can be composed sequentially explicitly as $M; N$ or implicitly

by left-to-right evaluation in the pairing construct $\langle M, N \rangle$. They can be composed in parallel as $M \parallel N$. We assume preemptive scheduling, thus imposing no restrictions on the interleaving execution steps between parallel threads. To introduce the memory-access constructs, we present the well-known *message passing* litmus test, adapted to the functional setting:

$$(x := 1 ; y := 1) \parallel \langle y?, x? \rangle \quad (\text{MP})$$

Here, x and y refer to distinct shared memory locations. Assignment $\ell := v$ stores the value v at location ℓ in memory, and dereference $\ell?$ loads a value from ℓ . The language also includes atomic read-modify-write (RMW) constructs. For example, assuming integer storable values, FAA (ℓ, v) (Fetch-And-Add) atomically adds v to the value stored in ℓ . In contrast, interleaving is permitted between the dereferencing, adding, and storing in $\ell := (\ell? + v)$. The underlying *memory model* dictates the behavior of the memory-access constructs more specifically.

In the functional setting, execution results in a returned value: $\ell := v$ returns the unit value $\langle \rangle$, i.e. the empty tuple; $\ell?$, and the RMW constructs such as FAA (ℓ, v), return the loaded value; $M ; N$ returns what N returns; and $\langle M, N \rangle$, as well as $M \parallel N$, return the pair consisting of the return value of M and the return value of N . We assume left-to-right execution of pairs, so in the (MP) example $\langle y?, x? \rangle$ steps to $\langle v, x? \rangle$ for a value v that can be loaded from y , and $\langle v, x? \rangle$ steps to $\langle v, w \rangle$ for a value w that can be loaded from x . In between, the left side of the parallel composition (\parallel) can take steps.

We can use intermediate results in subsequent computations via *let* binding: **let** $a = M$ **in** N binds the result of M to a in N . Thus, we execute M first, and substitute the resulting value V for a in N before executing $N[a \rightarrow V]$. Similarly, we deconstruct pairs by matching: **match** M **with** $\langle a, b \rangle$. N binds the components of the pair that M returns to a and b respectively in N . The first and second projections **fst** and **snd**, as well as the operation **swap** that swaps the pair constituents, are defined using **match** standardly.

Traditionally, weak memory models are contrasted by finding litmus test programs, such as (MP), with which one model supports a specific *observable behavior* that the other does not. Since different models feature quite different notions of internal state, and observing the memory directly is not considered feasible anyway, internal interactions are ignored. We do not consider infinite executions in this paper, so we conflate observable behaviors with *outcomes*: values that the program may evaluate to from given initial memory values. Litmus tests are traditionally designed with all initial memory values set to 0 in mind.

Remark (Imperative vs. Functional). *Presentations of litmus tests for weak-memory models are usually presented imperatively using local registers a, b . This is subsumed in the functional setting by systematically replacing registers with let-bindings.*

For example, we can apply this process to the imperative message passing litmus test:

Style	Imperative	Functional
Program	$x := 1 \parallel a := y$ $y := 1 \parallel b := x$	$x := 1 ; y := 1 \parallel$ let $a = y?$ in let $b = x?$ in $\langle a, b \rangle$
Outcome of interest	An execution that ends with $a = 1 \wedge b = 0$	An evaluations that returns $\langle \rangle, \langle 1, 0 \rangle$

Up to standard, memory-model agnostic equivalences, this is our functional presentation (MP).

In the strongest memory model of Sequential Consistency (SC), every value stored is immediately made available to every thread, and every dereference must load the latest stored value. Thus the underlying memory model uses maps from locations to values for the memory state that evolves during program execution. Given an initial state, the behavior of a program in SC depends

only on the choice of interleaving of steps. In (MP) the order of the two stores and the two loads ensures that $\langle \langle \rangle, \langle 0, 0 \rangle \rangle$, $\langle \langle \rangle, \langle 0, 1 \rangle \rangle$, and $\langle \langle \rangle, \langle 1, 1 \rangle \rangle$ are observable, but $\langle \langle \rangle, \langle 1, 0 \rangle \rangle$ is not.

Observable behavior as defined for whole programs is too crude to study program *terms* that can interact with the program context within which they run. Indeed, compare M_1 defined as $x := 1; y := 1; y?$ versus M_2 defined as $x := 1; y := x?; y?$. Under SC, the difference between them as whole programs is unobservable: starting from any initial state both return 1. Now consider them within the program context $- \parallel x := 2$. That is, compare $M_1 \parallel x := 2$ versus $M_2 \parallel x := 2$. In the first, M_1 still always returns 1; but in the second, M_2 can also return 2 by interleaving the store of 2 in x immediately after the store of 1 in x . Thus, if $\llbracket M \rrbracket$, i.e. M 's denotation, were to simply map initial states to possible results according to executions of M , we could not define $\llbracket M \parallel N \rrbracket$ in terms of $\llbracket M \rrbracket$ and $\llbracket N \rrbracket$ alone, because we would have $\llbracket M_1 \rrbracket = \llbracket M_2 \rrbracket$ but also $\llbracket M_1 \parallel x := 2 \rrbracket \neq \llbracket M_2 \parallel x := 2 \rrbracket$. We conclude that $\llbracket M \rrbracket$ must contain more information on M than an “input-output” relation; it must account for interference by the environment.

2.2 Brookes's Trace-based Semantics for Sequential Consistency

A prominent approach to define fully-compositional, denotational semantics for concurrent programs is due to Brookes [13], who defined a denotational semantics for SC by taking $\llbracket M \rrbracket$ to be a set of traces of M closed under certain rewrite rules as we detail below. Brookes established a (directional) adequacy theorem: if $\llbracket M \rrbracket \supseteq \llbracket N \rrbracket$ then the transformation $M \rightarrow N$ is valid under SC. The latter means that, when assuming SC-based operational semantics, M can be replaced by N within a program without introducing new observable behaviors for it. Thus, adequacy formally grounds the intuition that the denotational semantics soundly captures behavior of program terms.

As a particular practical benefit, formal and informal simulation arguments which are used to justify transformations in operational semantics can be replaced by cleaner and simpler proofs based on the denotational semantics. For example, a simple argument shows that $\llbracket x := v; x := w \rrbracket \supseteq \llbracket x := w \rrbracket$ holds in Brookes's semantics. Thanks to adequacy, this justifies Write-Write Elimination (WW-Elim) $x := v; x := w \rightarrow x := w$ in SC.

Traces in SC. In Brookes's semantics, a program term is denoted by the set of traces, each trace consisting of a sequence of transitions. Each transition is of the form $\langle \mu, \rho \rangle$, where μ and ρ are memories, i.e. maps from locations to values. A transition describes a program term's execution relying on a memory state snapshot μ in order to guarantee the memory state snapshot ρ .

For example, $\llbracket x := w \rrbracket$ includes all traces of the form $\langle \langle \rho, \rho[x := w] \rangle \rangle$, where $\rho[x := w]$ is equal to ρ except for mapping x to w . The definition is compositional: the traces in $\llbracket x := v; x := w \rrbracket$ are obtained from sequential compositions of traces from $\llbracket x := v \rrbracket$ with traces from $\llbracket x := w \rrbracket$, obtaining all traces of the form $\langle \langle \mu, \mu[x := v] \rangle \langle \rho, \rho[x := w] \rangle \rangle$. Such a trace relies on μ in order to guarantee $\mu[x := v]$, and then relies on ρ in order to guarantee $\rho[x := w]$. Allowing $\rho \neq \mu[x := v]$ reflects the possibility of environment interference between the two store instructions. Indeed, when denoting parallel composition $\llbracket M \parallel N \rrbracket$ we include all traces obtained by interleaving transitions from a trace from $\llbracket M \rrbracket$ with transitions from a trace from $\llbracket N \rrbracket$. By sequencing and interleaving, one subterm's guarantee can fulfill the requirement which another subterm relies on. They may also relegate reliances and guarantees to their mutual context.

In the functional setting, executions not only modify the state but also return values. In this setting, traces are pairs, which we write as $\langle \xi \rangle \cdot r$, where ξ is the sequence of transitions and r represents the final value that the program term guarantees to return [6]. For example, the semantics of dereference $\llbracket x? \rrbracket$ includes all traces of the form $\langle \langle \mu, \mu \rangle \rangle \cdot \mu(x)$. Indeed, the execution of $x?$ does not change the memory and returns the value loaded from x . In the semantics of assignment $\llbracket x := v \rrbracket$, instead of $\langle \langle \mu, \mu[x := v] \rangle \rangle$ we have $\langle \langle \mu, \mu[x := v] \rangle \rangle \cdot \langle \rangle$.

Rewrite rules in SC. Were denotations in Brookes’s semantics defined to *only* include the traces explicitly mentioned above, it would not be abstract enough to justify (WW-Elim), which eliminates redundant writes. Indeed, we only saw traces with two transitions in $\llbracket x := v ; x := w \rrbracket$, but in $\llbracket x := w \rrbracket$ we saw traces with one. The semantics would still be adequate, but it would lack abstraction. This is where Brookes’s second main idea comes into play, making the denotations more abstract by closing them under two operations that rewrite traces:

Stutter adds a transition of the form $\langle \mu, \mu \rangle$ anywhere in the trace. Intuitively, a program term can always guarantee what it relies on.

Mumble combines of subsequent transitions of the form $\langle \mu, \rho \rangle \langle \rho, \theta \rangle$ into a single transition $\langle \mu, \theta \rangle$ anywhere in the trace. Intuitively, a program term can always omit a guarantee to the environment, and rely on its own omitted guarantee instead of relying on the environment.

Denotations in Brookes’s semantics are defined to be sets of traces *closed* under rewrite rules: applying a rewrite to a trace in the set results in a trace that is also in the set. For example, $\llbracket x := w \rrbracket$ is the least closed set with all traces of the form $\langle \rho, \rho [x := w] \rangle \cdot \langle \rangle$, and $\llbracket x := v ; x := w \rrbracket$ is the least closed set with all sequential compositions of traces from $\llbracket x := v \rrbracket$ with traces from $\llbracket x := w \rrbracket$.

Closure under these rules makes traces in $\llbracket M \rrbracket$ correspond precisely to *interrupted executions* of M , which are executions of M in which the memory can arbitrarily change between steps of execution. Each transition $\langle \mu, \rho \rangle$ in a trace in $\llbracket M \rrbracket$ corresponds to multiple execution steps of M that transition μ into ρ , and each gap between transitions accounts for possible environment interruption. The rewrite rules maintain this correspondence: stutter corresponds to taking 0 steps, and mumble corresponds to taking $n + m$ steps instead of taking n steps and then m steps when the environment did not change the memory in between. Brookes’s adequacy proof is based on this precise correspondence. In particular, the single-pair traces in $\llbracket M \rrbracket$ correspond to the (uninterrupted) executions, the “input-output” relation, of M .

2.3 Overview of Release/Acquire Operational Semantics

Memory accesses in RA are more subtle in than in SC. To address this we adopt Kang et al.’s “view-based” machine [28], an operational presentation of RA proven to be equivalent to the original declarative formulation of RA [e.g. 31]. In this model, rather than the memory holding only the latest value written to every variable, the memory accumulates a set of memory update messages for each location. Each thread maintains its own *view* that captures which messages the thread can observe, and is used to constrain the messages that the thread may read and write. The messages in the memory carry views as well, which are inherited from the thread that wrote the message, and passed to any thread that reads the message. Thus views indirectly maintain a causal relationship between messages in memory throughout the evolution of the system.

More concretely, causality is enforced by timestamping messages, thus placing them on their location’s *timeline*. A view κ associates a timestamp κ_ℓ to each location ℓ , obscuring the portion of ℓ ’s timeline before κ_ℓ . The view *points to* a message at ℓ with timestamp κ_ℓ . Messages point to messages via the view they carry, and must point to themselves.

To capture the atomicity of RMWs, each message occupies a half-open segment $(q, t]$ on their location’s timeline, where t is the message’s timestamp. A message with segment $(q, t]$ *dovetails* with a message at the same location with timestamp q , if there is one. When an RMW writes it must “modify” the message from which it read by dovetailing with it.

We explain our notation for messages by example. Assuming of two location, x and y , we denote by $x:1@(.5, 1.7] \langle y@3.5 \rangle$ the message at location x that carries the value 1, occupies the segment $(.5, 1.7]$ on x ’s timeline, and carries the view κ such that $\kappa_x = 1.7$ and $\kappa_y = 3.5$ (every message points to itself). An example memory is depicted at the top of Figure 1.

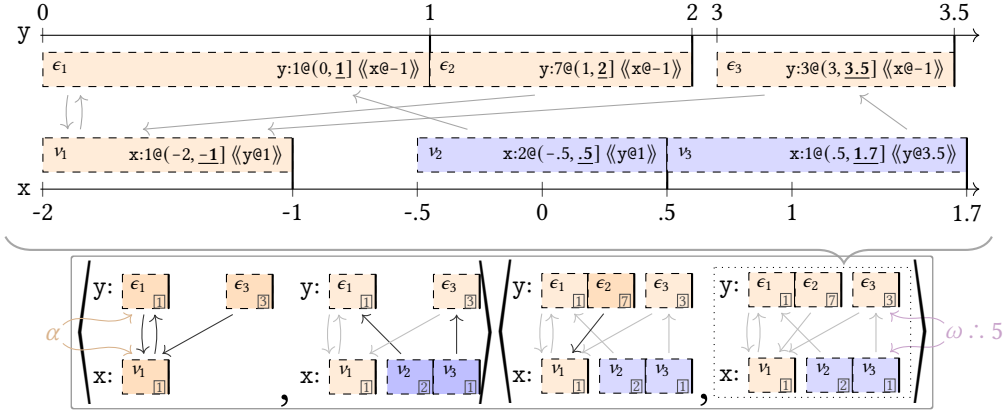


Fig. 1. Illustrations of a memory (top) and a trace (bottom), in the setting of two memory locations, x and y . **Top:** A memory holding six messages. The timelines are purposefully misaligned and not to scale to emphasize that timestamps for different locations are incomparable and that only the order between them is relevant. The graph structure that the views impose is illustrated by arrows pointing between messages. Messages that are not dovetailed are set apart, e.g. ν_3 dovetails with ν_2 , which does not dovetail with ν_1 . **Bottom:** A trace with two transitions: $\alpha \langle \langle \mu_1, \rho_1 \rangle \langle \mu_2, \rho_2 \rangle \rangle \omega \cdot : 5$. The memory illustrated on top is ρ_2 . Messages and edges that are not part of a previous memory are highlighted. The local messages are ν_2 and ν_3 , and the rest are environment messages.

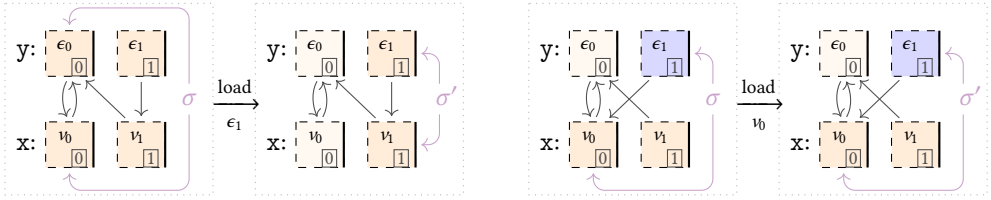


Fig. 2. Depictions of a step during an execution of a litmus test, with the view of the right thread changing from σ to σ' . The value each message carries is in its bottom-right corner. Views are illustrated implicitly in the graph structure that they impose. Obscured messages are faded. **Left:** As the right thread in (MP) loads 1 from y , it inherits the view of ϵ_1 , obscuring ν_0 . **Right:** The right thread in (SB) loading 0 from x . Storing ϵ_1 did not obscure ν_0 .

When a thread writes to a location ℓ , it must increase the timestamp its view associates with ℓ and use its new view as the message’s view. The message’s segment must not overlap with any other segment on ℓ ’s timeline. In particular, only one message can ever dovetail with a given message. A thread can only read from revealed messages, and when it reads, its view increases as needed to *dominate* the view of the loaded message, where a view ω dominates a view α , written $\alpha \leq \omega$, if $\alpha_\ell \leq \omega_\ell$ for every ℓ . Increasing the view in this way may obscure messages at the location of the read as well as other locations.

Revisiting the (MP) litmus test, starting with a memory with a single message holding 0 at each location, and with all views pointing to the timestamps of these message, suppose the right thread loaded 1 from y , as depicted on the left side of Figure 2. Such a message can only be available if the left thread stored it. Before storing 1 to y , the left thread stored 1 to x , obscuring the initial x message. The right thread inherits this limitation through the causal relationship, so it will not be able to load 0 from x . Therefore, RA forbids the outcome $\langle \langle \rangle, \langle 1, 0 \rangle \rangle$.

In contrast, consider the litmus test known as *store buffering*:

$$(x := 1; y?) \parallel (y := 1; x?) \quad (\text{SB})$$

By considering the possible interleavings, one can check that no execution in SC returns $\langle 0, 0 \rangle$. However, in RA some do. Indeed, even if the left thread stores to x before the right thread loads from x , the right thread's view allows it to load 0, as depicted on the right side of [Figure 2](#).

We can recover the SC behavior by interspersing fences between sequenced memory accesses, which we model with $\text{FAA}(z, 0)$ to a fresh location z . Thus, compare (SB) to the *store buffering with fences* litmus test:

$$(x := 1; \text{FAA}(z, 0); y?) \parallel (y := 1; \text{FAA}(z, 0); x?) \quad (\text{SB+F})$$

Both of the $\text{FAA}(z, 0)$ instructions store messages that must dovetail with the message that they load from, and in that also inherit its view. They cannot both dovetail with the same message because their segments cannot intersect. Thus, one of them—say, the one on the right—will have to dovetail with the other. In this scenario, the view of the message that the left thread stores at z points to the message it previously stored at x . When the right thread loads the message from z it inherits this view, obscuring the initial message to x . Therefore, when it later loads from x , it must load what the left thread stored. Thus, like in SC, no execution in RA returns $\langle 0, 0 \rangle$.

3 Contribution Summary

We begin by showcasing our notion of a trace, which we adapt to RA both in the structure of the trace itself, as well as in the rewrite rules we impose ([§3.1](#)). We then briefly explain the way in which our semantics is standard, and a few beneficial consequences of this fact ([§3.2](#)). Finally, we connect our denotational semantics to the operational semantics of RA in ([§3.3](#)), showing both adequacy and sufficient abstraction.

3.1 Traces for Release/Acquire

As in [Brookes's](#) SC-traces, our RA-traces include a sequence of transitions ξ , each transition a pair of RA memories; and a return value r . Intuitively, these play a similar role here, formally grounded in analogs to the stutter and mumble rewrite rules. Seeing that the operational semantics only adds messages and never modifies them, we require that every memory snapshot in the sequence ξ be contained in the subsequent one, whether it be within or across transitions. A message added within a transition is a *local message*; otherwise it is an *environment message*. We call the first memory in ξ 's first transition its *opening memory*, and the second memory in ξ 's last transition its *closing memory*. In addition, RA-traces include an initial view α , declaring which messages are relied upon to be revealed in ξ 's opening memory; and a final view ω , declaring which messages are guaranteed to be revealed in ξ 's closing memory. We write the trace as $\alpha \boxed{\xi} \omega \cdot r$. See bottom of [Figure 1](#) for an illustrated example.

RA specific rewrite-rules. We add several more bespoke RA-specific rewrite rules to close denotations under, making the denotational semantics more abstract. For example, (**WW-Elim**) is also valid under RA. The reasoning we have used to justify it under SC, by showing $\llbracket x := v; x := w \rrbracket \supseteq \llbracket x := w \rrbracket$ in [Brookes's](#) semantics, will only get us so far here. Replicating the process, the trace we end up with in $\llbracket x := v; x := w \rrbracket$ after rewriting with mumble has two local messages, whereas traces from $\llbracket x := w \rrbracket$ only have a single local message. Roughly speaking, the equality concerning SC memories $\mu[x := v][x := w] = \mu[x := w]$ does not transfer to RA where memory, by accumulating messages, is more concrete. We resolve this by adding the absorb rewrite rule, which replaces two dovetailed local messages with one that carries the second message's value. Thus, in the proof for RA we follow the mumble rewrite with an absorb rewrite.

Internalized operational invariants. We further increase abstraction in our denotations by pointing out and internalizing properties of the operational semantics. Without restrictions, traces may represent behaviors that include operationally unreachable states. Forbidding such redundant traces eliminates a source of differentiation between denotations, thus increasing their abstraction.

Specifically, consider the transformation $x? ; y? \rightarrow y?$, a consequence of the RA-valid Irrelevant Read Elimination (R-Elim) $x? ; \langle \rangle \rightarrow \langle \rangle$ and structural equivalences. Consider the state S that consists of the memory at the top of Figure 1 and the view that points to v_3 and ϵ_2 . The only step $x? ; y?$ can take from the state S is to load v_3 , inheriting the view that v_3 carries, which changes the thread's view to point to ϵ_3 . Only ϵ_3 is available in the following step, which means the term returns 3. In contrast, starting from S , the term $y?$ can load from ϵ_2 to return 7. This analysis does not invalidate the transformation because the state S is unreachable by an execution starting from an initial state, and should therefore be ignored when determining observable behaviors.

Just as we restrict our attention to reachable states when analyzing the operational semantics, we refine our denotational semantics by restricting our denotational domain to traces that possess analogous properties. This move allows us to justify (R-Elim): we have $\llbracket x? ; \langle \rangle \rrbracket \supseteq \llbracket \langle \rangle \rrbracket$.

3.2 Compositionality and the Monadic Presentation

One of the contributions of this work is to bridge research of weak-memory models with Moggi's monad-based approach [40] to denotational semantics. This approach also comes with practical benefits, such as a built-in semantic framework for the effect-free fragment of the language, to which effect constructs can be modularly added. Reasoning about the effect-free fragment stays valid through modular expansions with effects. For instance, if K is effect-free, then:

$$\llbracket \text{if } K \text{ then } M ; N \text{ else } M ; N' \rrbracket = \llbracket M ; \text{if } K \text{ then } N \text{ else } N' \rrbracket$$

So-called structural equivalences may otherwise require challenging ad-hoc proofs [e.g. 24, 26].

Higher order. An important aspect of a programming language is its facilitation of abstraction. Higher-order programming is a flexible instance of this, in which programmable functions can take functions as input and return functions as output. Moggi's approach supports this feature out-of-the-box in such a way that does not complicate the rest of the semantics, as the first-order fragment of the semantics need not change to include it.

Every value returned by an execution has a semantic presentation which we use as the return value in traces. The semantic and syntactic values are identified in the first-order fragment, but different syntactic functions may have the same semantics, so the identification does not extend to entire higher-order language.

A term is a *program* if it is *closed* (every variable occurrence is bound) and of *ground type* (all functions are applied to arguments). This definition is in line with the expectation that a program should return a concrete result that the end-user can consume. Thus, we only consider observable behaviors of programs. Transformations only need to be valid when applied within programs. Programs degenerate to closed terms in the first-order fragment.

To deal with the need to prove properties "pointwise" that abstractions bring about we use logical relations [45, 49]. Moggi's toolkit provides a standard way to define these, thereby lifting properties to their higher-order counterparts.

Compositionality. In its most basic form, this key feature of denotational semantics means that a program term's denotation is defined using *the denotations* of its immediate subterms. In our case denotations are sets, where each elements represents a possible behavior of the term, we are interested in establishing a directional generalization of compositionality:

Compositionality (Thm. 7.7). *For a program context $\Xi [-]$, if $\llbracket M \rrbracket \subseteq \llbracket N \rrbracket$ then $\llbracket \Xi [M] \rrbracket \subseteq \llbracket \Xi [N] \rrbracket$.*

This is a consequence of the monadic design of the denotational semantics using monotonic operators, and is not substantially different from previous work [e.g. 20].

3.3 Relating the Denotational Semantics to the Operational Semantics

Kang et al. presentation assumes top-level parallelism, a common practice in studies of weak-memory models. This comes at the cost of the uniformity and compositionality. In particular, the denotation $\llbracket M \parallel N \rrbracket$ cannot be defined. We resolve this by extending Kang et al.'s operational semantics to support first-class parallelism by organizing thread views in an evolving *view-tree*, a binary tree with view-labeled leaves, rather than in a fixed flat mapping. Thus, *states* that accompany executing terms consist of a memory and a view-tree. In discourse, we do not distinguish between a view-leaf and its label.

Remark. *Supporting first-class parallel composition allows us to decompose Write-Read Reordering (WR-Reord) $(x := v) ; y? \rightarrow \text{fst } \langle y?, (x := v) \rangle$, a crucial reordering of memory accesses valid under RA but not SC, into a combination of Write-Read Deorder (WR-Deord) $\langle (x := v), y? \rangle \rightarrow (x := v) \parallel y?$ together with structural transformations and laws of parallel programming:*

$$\begin{array}{ccccc}
 & \downarrow \text{Structural} & & \downarrow \text{(WR-Deord)} & \\
 (x := v) ; y? \rightarrow \text{snd } \langle (x := v), y? \rangle & \rightarrow & \text{snd } ((x := v) \parallel y?) & & \\
 \downarrow \text{Par. Prog. Law: Symmetry} & & \downarrow \text{Structural} & & \downarrow \text{Par. Prog. Law: Sequencing} \\
 \rightarrow \text{snd } (\text{swap } (y? \parallel (x := v))) & \rightarrow & \text{fst } (y? \parallel (x := v)) & \rightarrow & \text{fst } \langle y?, (x := v) \rangle
 \end{array}$$

This provides a separation of concerns: the components of this decomposition are supported by our semantics using independent arguments. It also sheds a light on the interesting part, as they are all valid under SC except for (WR-Deord).

Observability correspondence. We call some of our rewrite rules *abstract*, such as *absorb*, and others *concrete*, such as *stutter* and *mumble*. We denote the *concrete denotation* of a term M by $\llbracket M \rrbracket$, which is the denotation were it defined using only the concrete rewrite rules. Traces in the concrete denotations directly correspond to interrupted executions, but not so in the (regular) denotations. For example, in our analysis of (WW-Elim), by using *absorb*, we ended up with a trace in which only one message is added even though the program term adds two messages. Thus, the abstract rewrite rules break the direct correspondence.

Still, some indirect correspondence should remain to justify adequacy. In particular, we would like traces to correspond to observable behavior of programs. In one direction, an even stronger property holds, known as soundness:

Soundness (Thm. 7.8). *For every execution of a program M in the operational semantics of RA, there exists $\alpha \langle \mu, \rho \rangle \omega \cdot r \in \llbracket M \rrbracket$ that matches the execution: $\langle \alpha, \mu \rangle$ is the initial state, $\langle \omega, \rho \rangle$ is the final state, and r matches the value returned.*

To prove soundness, we take a trace where transitions correspond to the memory-accessing execution steps, and then use *mumble* to obtain a single transition.

Ignoring the final state, the correspondence holds in the other direction too:

Evaluation Lemma (Lem. 7.10). *For every program M and $\alpha \langle \mu, \rho \rangle \omega \cdot r \in \llbracket M \rrbracket$ there is an observable behavior of M with initial state $\langle \alpha, \mu \rangle$ and return value matching r .*

The lack of correspondence with the final state is an artifact of the concreteness-abstraction divergence between the operational and denotational semantics. Due to this divergence, it is significantly more challenging to establish this direction of the correspondence than in previous work.

The primary challenge in proving that abstract rewrite rules can be applied retroactively, deferring them to the top-level. That is, denoting closure under the abstract rewrite rules by $-\dagger$, we claim:

Retroactive Closure (Lem. 7.4). *If M is a program, then $\llbracket M \rrbracket = \llbracket M \rrbracket^\dagger$.*

Thus, to obtain all of the traces in the (regular) denotation of a term, it is enough to close only under the concrete rewrite rules as the denotation of a program is built-up from its subterms, applying the abstract rewrite rules only at the top level.

The intuition that guides the proof is that the abstract rewrite rules can be percolated outwards:

Rewrite Commutativity (Lem. 7.1). *Let τ and ρ be traces such that τ can be rewritten to ρ using both concrete and abstract rewrite rules (denoted $\tau \xrightarrow{\text{ca}} \rho$). Then there exists a trace π , such that τ can be rewritten to π using only concrete rewrite rules (denoted $\tau \xrightarrow{\text{c}} \pi$), and π can be rewritten to ρ using only abstract rewrite rules (denoted $\pi \xrightarrow{\text{a}} \rho$).*

The central result is (directional) adequacy, stating that denotational approximation corresponds to refinement of observable behaviors:

Adequacy (Thm. 7.9). *If $\llbracket M \rrbracket \subseteq \llbracket N \rrbracket$, then for all program contexts $\Xi [-]$, every observable behavior of $\Xi [M]$ is an observable behavior of $\Xi [N]$.*

In particular, $\llbracket M \rrbracket \subseteq \llbracket N \rrbracket$ implies that $N \rightarrow M$ is valid under RA, because the effect of applying it is unobservable. Adequacy follows immediately from the above results. Indeed, using soundness, an observable behavior of $\Xi [M]$ corresponds to a single-transition $\tau \in \llbracket \Xi [M] \rrbracket$; by the assumption and compositionality $\tau \in \llbracket \Xi [N] \rrbracket$; and using the evaluation lemma, τ corresponds to an observable behavior of $\Xi [N]$.

Abstraction. Brookes's semantics is *fully abstract*, meaning that the converse to adequacy also holds: if $N \rightarrow M$ is valid under SC, then $\llbracket N \rrbracket \supseteq \llbracket M \rrbracket$. However, Brookes's proof relies on an artificial program construct, **await**, that permits waiting for a specified memory snapshot and then step (atomically) to a second specified memory snapshot. Thus, in realistic languages, when this construct is unavailable, Brookes's full abstraction proof does not apply.

Nevertheless, even without full abstraction, one can still provide evidence that an adequate semantics is abstract by ensuring that it supports known transformations.

To the best of our knowledge, all transformations $N \rightarrow M$ proven to be valid under RA in the existing literature are supported by our denotational semantics, i.e. $\llbracket N \rrbracket \supseteq \llbracket M \rrbracket$. Structural transformations are supported by virtue of using Moggi's standard semantics. Our semantics also validates "algebraic laws of parallel programming", such as sequencing $M \parallel N \rightarrow \langle M, N \rangle$ and its generalization that Hoare and van Staden [22] recognized, $(M_1 ; M_2) \parallel (N_1 ; N_2) \rightarrow (M_1 \parallel N_1) ; (M_2 \parallel N_2)$, which in the functional setting can take the more expressive form in which the values returned are passed on to the following computation. See Figure 3 for a partial list.

4 Language and Typing

We consider a standard extension of Moggi's [40] computational lambda calculus with products and variants (labeled sums) further extending it with shared-memory constructs. We parameterize our language, which we call λ_{RA} , by its globally available locations, the values we store in and retrieve from these locations, and the primitives we use to atomically mutate these values through a unified *read-modify-write* construct.

Laws of Parallel Programming

Symmetry $M \parallel N \rightarrow \text{swap } (N \parallel M)$

Generalized Sequencing

$(\text{let } a = M_1 \text{ in } M_2) \parallel (\text{let } b = N_1 \text{ in } N_2) \rightarrow \text{match } M_1 \parallel N_1 \text{ with } \langle a, b \rangle. M_2 \parallel N_2$

Eliminations

Irrelevant Read $\ell? ; \langle \rangle \rightarrow \langle \rangle$

Write-Write $\ell := v ; \ell := w \xrightarrow{\text{Ab}} \ell := w$

Write-Read $\ell := v ; \ell? \rightarrow \ell := v ; v$

Write-FAA $\ell := v ; \text{FAA } (\ell, w) \xrightarrow{\text{Ab}} \ell := (v + w) ; v$

Read-Write $\text{let } a = \ell? \text{ in } \ell := (a + v) ; a \rightarrow \text{FAA } (\ell, v)$

Read-Read $\langle \ell?, \ell? \rangle \rightarrow \text{let } a = \ell? \text{ in } \langle a, a \rangle$

Read-FAA $\langle \ell?, \text{FAA } (\ell, v) \rangle \rightarrow \text{let } a = \text{FAA } (\ell, v) \text{ in } \langle a, a \rangle$

FAA-Read $\langle \text{FAA } (\ell, v), \ell? \rangle \rightarrow \text{let } a = \text{FAA } (\ell, v) \text{ in } \langle a, a + v \rangle$

FAA-FAA $\langle \text{FAA } (\ell, v), \text{FAA } (\ell, w) \rangle \xrightarrow{\text{Ab}} \text{let } a = \text{FAA } (\ell, v + w) \text{ in } \langle a, a + v \rangle$

Others

Irrelevant Read Introduction $\langle \rangle \rightarrow \ell? ; \langle \rangle$

Read to FAA $\ell? \xrightarrow{\text{Di}} \text{FAA } (\ell, 0)$

Write-Read Deorder $\langle (\ell := v), \ell'? \rangle \xrightarrow{\text{Ti}} (\ell := v) \parallel \ell'? \quad (\ell \neq \ell')$

Write-Read Reorder $(\ell := v) ; \ell'? \xrightarrow{\text{Ti}} \text{fst } \langle \ell'? , (\ell := v) \rangle \quad (\ell \neq \ell')$

Fig. 3. A selective list of supported non-structural transformations. Along with Symmetry, the denotational semantics supports all symmetric-monoidal laws with the binary operator (\parallel) and the unit $\langle \rangle$. Similar transformations, replacing FAA with other RMWs, are supported too. The abstract rewrite rules used to validate a transformation is mentioned, if there is one.

Locations and Storable Values. We fix two finite sets of (*shared memory*) *locations* Loc , ranged over by ℓ, ℓ' ; and (*storable*) *values* Val , ranged over by v, w, u . For example, we may take Loc and Val to be all 64-bit sequences. In concrete examples, we will use concrete names such as x, y, z for distinct locations, and numbers for values. For simplicity, we don't include primitives (such as addition) explicitly, since they require standard minor changes.

Read-modify-write (RMW). These constructs atomically read a value from memory and possibly modify it to some other computed value. Typical languages include the following constructs, which are efficiently compiled to hardware: **Compare-and-Swap**: modify when stored value match a parameter; **Fetch-and-Add**: increase by a parameter; and **Exchange**: always modify to a parameter. For convenience, we include a single RMW construct that expresses all such operations, *as well as standard loads*. This generalization, especially bringing together loads with RMW operations, is non-standard, but makes our development more uniform.

Formally, a *modifier* is a partial function $\Phi : \text{Val} \rightarrow \text{Val}$, which represents an RMW operation that reads a value v from memory; and if Φ is defined on v , atomically writes $\Phi(v)$ instead. For supporting parameters, an *n-ary modifier* is a partial function $\varphi_- : \text{Val}^n \times \text{Val} \rightarrow \text{Val}$. Our language requires a family RMW, indexed by the natural numbers, consisting of sets RMW_n of n -ary modifiers which we call *primitive* modifiers. For example, the primitive modifiers for common operations, which have efficient implementations on hardware, are as follows:

$M, N ::=$	term	$A, B ::=$	type
a	variable/identifier	$A \rightarrow B$	function
	function	$(A_1 * \dots * A_n)$	tuple/product
$ \lambda a : A. M$	abstraction	$ \{ \iota_1 \text{ of } A_1$	variant/sum
$ \ MN$	application	$ \dots \iota_n \text{ of } A_n \}$	
	constructor		
$ \langle M_1, \dots, M_n \rangle$	tuple		
$ \ A. \iota M$	variant		
	pattern matching		
$ \ \text{match } M \text{ with}$	on tuples	unit	$1 := ()$
$\langle a_1, \dots, a_n \rangle. N$		value tuple	$\text{Val}^n := (\text{Val} * \dots * \text{Val})$
$ \ \text{match } M \text{ with}$	on variants	enumeration	$\{ \iota_1 \dots \iota_n \} := \{ \iota_1 \text{ of } 1 \dots \iota_n \text{ of } 1 \}$
$\{ \iota_1 a_1. N_1 \dots \iota_n a_n. N_n \}$		label	$A. \iota := A. \iota \langle \rangle$
	shared-state	let-binding	$\text{let } a = M \text{ in } N := \text{match } \langle M \rangle \text{ with } \langle a \rangle. N$
$ \ \text{rmw}_\varphi(M; N)$	read-modify-write	sequencing	$M; N := \text{let } _ = M \text{ in } N$
$ \ M := N$	write		
$ \ M \parallel N$	parallel composition		

Fig. 4. Syntax of the λ_{RA} -calculus: terms and types. Shared-state constructs extending the core calculus are highlighted, and further syntactic sugar is defined in the bottom-right box.

Load $\text{load}(v) := \perp$ **Compare-and-Swap** $\text{cas}_{\langle w, u \rangle}(v) := \text{if } v = w \text{ then } u \text{ else } \perp$
Exchange $\text{xchg}_{\langle w \rangle}(v) := w$ **Fetch-and-Add** $\text{faa}_{\langle w \rangle}(v) := v + w$

Here, \perp means ‘undefined’, we omit load ’s 0-parameters ($\langle \rangle$), cas requires a semantic equality comparison operator on values ($=$), and faa requires a semantic addition operator on values ($+$).

Syntax. Given parameters Loc , Val , and RMW , Figure 4 presents λ_{RA} ’s syntax, and additional syntax admitted via syntactic sugar. The types are standard, comprising tuple, sum and function types. We draw constructor names for variants from a countably infinite set Lab , ranged over by ι . Assuming Lab contains Loc and Val , we identify them with sum types Val and Loc whose constructors are the locations and values, each labeling the empty tuple type.

The core term constructs in λ_{RA} are standard too. We draw program variables from a countably infinite set PVar , ranged over by a, b, c . For simplicity, we assume labels and variables are distinct: $\text{PVar} \cap \text{Lab} = \emptyset$. Tuple and variant constructors are standard, and we require the total sum type to disambiguate each variant constructor, which we omit when this type can be inferred. The pattern matching constructs for tuples and variants are standard, with binding variables occurrences in each pattern. In the tuple case, we require the variables in the pattern to be distinct. Function abstraction and application are standard, and we annotate the bound variable with its type, again omitting the annotation when we can infer it.

We index the RMW construct with a primitive modifier $\varphi \in \text{RMW}$, and its first argument is a location from which to read and possibly modify, followed by a tuple supplying the parameters. The term $\text{rmw}_\varphi(M; N)$ executes by evaluating M to a location ℓ , then evaluating N to a tuple of values $\vec{w} = \langle w_1, \dots, w_{\varphi.\text{ar}} \rangle$. Then, atomically, reading a value v from ℓ and writing $\varphi_{\vec{w}}v$ if it’s defined. Regardless of whether the write occurred, the read value is returned.

We desugar the typical memory dereferencing primitives using our example modifier primitives:

$$\boxed{\Gamma \vdash M : A}$$

$$\begin{array}{c}
\frac{(a : A) \in \Gamma}{\Gamma \vdash a : A} \quad \frac{\Gamma, a : A \vdash M : B}{\Gamma \vdash \lambda a : A. M : A \rightarrow B} \quad \frac{\Gamma \vdash M : A \quad \Gamma \vdash N : A \rightarrow B}{\Gamma \vdash NM : B} \\
\\
\frac{\forall i. \Gamma \vdash M_i : A_i}{\Gamma \vdash \langle M_1, \dots, M_n \rangle : (A_1 * \dots * A_n)} \quad \frac{\Gamma \vdash M : A_i \quad A = \{\iota_1 \text{ of } A_1 \mid \dots \mid \iota_n \text{ of } A_n\}}{\Gamma \vdash A.\iota_i M : A} \\
\\
\frac{\Gamma \vdash M : (A_1 * \dots * A_n) \quad \Gamma, a_1 : A_1, \dots, a_n : A_n \vdash N : A}{\Gamma \vdash \text{match } M \text{ with } \langle a_1, \dots, a_n \rangle. N : A} \quad \frac{\Gamma \vdash M : \{\iota_1 \text{ of } A_1 \mid \dots \mid \iota_n \text{ of } A_n\} \quad \forall i. \Gamma, a_i : A_i \vdash N_i : A}{\Gamma \vdash \text{match } M \text{ with } \{\iota_1 a_1.N_1 \mid \dots \mid \iota_n a_n.N_n\} : A} \\
\\
\frac{\varphi \in \text{RMW}_n \quad \Gamma \vdash M : \text{Loc} \quad \Gamma \vdash N : \text{Val}^n}{\Gamma \vdash \text{rmw}_\varphi(M; N) : \text{Val}} \quad \frac{\Gamma \vdash M : \text{Loc} \quad \Gamma \vdash N : \text{Val}}{\Gamma \vdash M := N : 1} \\
\\
\frac{\Gamma \vdash M : A \quad \Gamma \vdash N : B}{\Gamma \vdash M \parallel N : (A * B)}
\end{array}$$

Fig. 5. Typing rules for the λ_{RA} -calculus. Typing rules for the shared-state constructs are highlighted.

$$\begin{array}{l}
M? := \text{rmw}_{\text{load}}(M; \langle \rangle) \quad \text{CAS}(M, N, K) := \text{rmw}_{\text{cas}}(M; \langle N, K \rangle) \\
\text{XCHG}(M, N) := \text{rmw}_{\text{xchg}}(M; \langle N \rangle) \quad \text{FAA}(M, N) := \text{rmw}_{\text{faa}}(M; \langle N \rangle)
\end{array}$$

Assignment $M := N$ is standard, executing by first evaluating M to a location ℓ ; evaluating N to a value v ; storing the value v at the location ℓ in memory; and finally returning $\langle \rangle$. Unlike RMWs, assignment need not modify an existing message in memory. Indeed, $M := N$ is not equivalent to an XCHG that discards its read value, i.e. $\text{XCHG}(M, N); \langle \rangle$.

The operational semantics, defined in §5, follows a call-by-value evaluation strategy, adhering to a left-to-right convention except for parallel composition $M \parallel N$. There, the executions of its threads M and N interleave, evaluating to the pair of the results of each thread.

Remark. *We do not include recursion/loops in this language, which we leave to future work. While important, recursion will muddy the waters substantially, requiring us to bring into context domain theoretic concepts like least upper-bounds of ω -chains and powerdomain constructions. Even without recursion, λ_{RA} is expressive enough for us to discuss interesting examples and transformations.*

Type system. We present the type system in Figure 5. Each typing judgment $\Gamma \vdash M : A$ relates a type A , a term M , and a *typing context* Γ which associates to each of M 's unbound variable a a type B_a , written $(a : B_a) \in \Gamma$. We write \cdot for the empty context, and say that M is *closed* if $\vdash M : A$ for some type A . The *shadowing extension* of Γ by $c : C$, denoted $\Gamma, c : C$, is equal to Γ except for associating C to c . The typing rules for the shared-memory constructs are standard, and reflect their informal explanation above. In particular, for RMW the arity of the tuple must match the arity of the modifier. Each term has at most one type in a given typing context, and in that case the typing derivation is unique. We denote by $\Gamma \vdash A$ the set of terms $\{M \mid \Gamma \vdash M : A\}$.

A *program* is a closed term of *ground type*—iterated sum and product types:

$$G ::= (G_1 * \dots * G_n) \mid \{\iota_1 \text{ of } G_1 \mid \dots \mid \iota_n \text{ of } G_n\} \quad (\text{Ground types})$$

5 Operational Semantics for Release/Acquire Concurrency

We start this section with a precise account of the “view-based” machine (§5.1) presented in §2.3. We observe that this semantics admits a non-deterministic view-forwarding step (§5.2) which our metatheory uses. Our denotational semantics also accounts for both known and novel semantic invariants on the memories that can evolve when executing a well-typed program (§5.3).

5.1 View-based Semantics

Our formalization of the operational semantics follows Kang et al. [28] and Kaiser et al. [27]. The account below grounds the explanations we gave in §2.3 more formally.

Timestamps. We maintain a per-location *timestamp* order, which constrains the memory access of threads. We use rational numbers \mathbb{Q} as timestamps, ordered standardly, ranged over by t, q, p .

Views. A *view* is a location-indexed tuple of timestamps, i.e. an element $(\kappa_\ell)_{\ell \in \text{Loc}}$ in $\text{View} := \mathbb{Q}^{\text{Loc}}$. We let $\alpha, \kappa, \sigma, \omega$ range over views. In examples with $\text{Loc} = \{x, y\}$, we denote by $\langle\langle x@t ; y@q \rangle\rangle$ the view that has t in the x component and q in the y component. We order views location-wise, i.e. $\alpha \leq \omega$ when $\forall \ell \in \text{Loc}. \alpha_\ell \leq \omega_\ell$, and in this case say that ω *dominates* α . We also employ \sqcup and \sqcap for pointwise maximum and minimum of views, and denote by $\kappa[\ell \mapsto t]$ the view that is equal to κ everywhere except ℓ , where it equals t .

Messages. A *message* v is a tuple in $\text{Msg} := \text{Loc} \times \text{Val} \times \mathbb{Q} \times \text{View}$, written $v = \ell : v @ (q, \kappa_\ell) \langle\langle \kappa \rangle\rangle$, where $q < \kappa_\ell$. Here, ℓ is the location of the message, v is the value of the message, q is the *initial* timestamp of the message, and κ is the view of the message. We say this message *dovetails* another message if its timestamp is q .

We use projection-notation for components of v : $v.lc := \ell$, $v.vl := v$, $v.i := q$, and $v.vw := \kappa$. The (*final*) *timestamp* of the message is $v.t := \kappa_\ell$. In concrete examples, we reduce duplication by eliding the timestamp from the view, e.g. $y:0@(0.5, \underline{4.2}] \langle\langle x@1 \rangle\rangle$. The message’s two timestamps delimit the *segment* of the message: the interval $v.\text{seg} := (v.i, v.t]$.

We range over messages using ν, ϵ, β . We extend notation from messages to sets of messages by direct image: for example, given a set μ of messages, define $\mu.\text{seg} := \{v.\text{seg} \mid v \in \mu\}$.

Memories. A *memory* is a finite non-empty set of messages. We let μ, ρ, θ range over memories, and denote the set of messages in μ at location ℓ by $\mu_\ell := \{v \in \mu \mid v.lc = \ell\}$.

Example 5.1. The memory illustrated at the top of Figure 9 could have resulted from a program execution starting with the memory $\{v_1, \epsilon_1\}$: a program may add messages out of the timeline order (ϵ_3 before ϵ_2); dovetail messages ($v_2.t = v_3.i$); or leave gaps between messages ($v_1.t < v_2.i$). Message views need not increase along the timeline ($\epsilon_2.t \leq \epsilon_3.t$ yet $\epsilon_2.vw \not\leq \epsilon_3.vw$).

View trees. Kang et al.’s [28] original presentation of the view-based semantics studies top-level parallelism, and thus featured a flat thread-view mappings. Here we allow nesting of parallel composition anywhere in the program, so we use a tree of views instead, whose structure changes along with the execution of the program as threads are activated and synchronize.

Formally, a *view-tree* is a binary tree with view-labeled leaves. We denote the set of view-trees by VTree , ranged over by T, R, H . We denote: by $\dot{\kappa}$ the leaf with label κ ; by $\widehat{T}R$ the tree whose immediate left and right subtrees are T and R ; and by $T.lf$ the set of labels of leaves of T . We lift the order \leq from views to view-trees leaf-wise: $\dot{\kappa} \leq \dot{\sigma}$ when $\kappa \leq \sigma$, and $\widehat{T}R \leq \widehat{T'}R'$ when $T \leq T'$ and $R \leq R'$.

Operational semantics. Figure 6 presents the notable part of the operational semantics for λ_{RA} . A *configuration* $\langle T, \mu \rangle, M$ consists of a view-tree T capturing the view of all active threads; the

$$\boxed{\langle T, \mu \rangle, M \overset{e}{\rightsquigarrow}_{\text{RA}} \langle T', \mu' \rangle, M'}$$

<p style="text-align: center;">PARINIT</p> $\frac{}{\langle \dot{\kappa}, \mu \rangle, M \parallel N \overset{\circ}{\rightsquigarrow}_{\text{RA}} \langle \dot{\kappa} \widehat{\kappa}, \mu \rangle, M \parallel N}$ <p style="text-align: center;">PARLEFT</p> $\frac{\langle T, \mu \rangle, M \overset{e}{\rightsquigarrow}_{\text{RA}} \langle T', \mu' \rangle, M'}{\langle T \widehat{R}, \mu \rangle, M \parallel N \overset{e}{\rightsquigarrow}_{\text{RA}} \langle T' \widehat{R}, \mu' \rangle, M' \parallel N}$ <p style="text-align: center;">STORE</p> $\frac{\alpha_\ell < t \quad (q, t] \cap \bigcup \mu_\ell.\text{seg} = \emptyset \quad \omega = \alpha[\ell \mapsto t]}{\langle \dot{\alpha}, \mu \rangle, \ell := v \overset{\bullet}{\rightsquigarrow}_{\text{RA}} \langle \dot{\omega}, \mu \uplus \{ \ell : v @ (q, \omega_\ell] \langle \omega \rangle \} \rangle, \langle \rangle}$ <p style="text-align: center;">RMW</p> $\frac{\ell : v @ (q, \kappa_\ell] \langle \kappa \rangle \in \mu \quad \alpha_\ell \leq \kappa_\ell \quad \varphi_{\vec{w}} v \neq \perp \quad (\kappa_\ell, t] \cap \bigcup \mu_\ell.\text{seg} = \emptyset \quad \omega = (\alpha \sqcup \kappa)[\ell \mapsto t]}{\langle \dot{\alpha}, \mu \rangle, \mathbf{rmw}_\varphi(\ell; \vec{w}) \overset{\bullet}{\rightsquigarrow}_{\text{RA}} \langle \dot{\omega}, \mu \uplus \{ \ell : \varphi_{\vec{w}} v @ (\kappa_\ell, \omega_\ell] \langle \omega \rangle \} \rangle, v}$	<p style="text-align: center;">PARFIN</p> $\frac{\omega = \kappa \sqcup \sigma}{\langle \dot{\kappa} \widehat{\sigma}, \mu \rangle, V \parallel W \overset{\circ}{\rightsquigarrow}_{\text{RA}} \langle \dot{\omega}, \mu \rangle, \langle V, W \rangle}$ <p style="text-align: center;">PARRIGHT</p> $\frac{\langle R, \mu \rangle, N \overset{e}{\rightsquigarrow}_{\text{RA}} \langle R', \mu' \rangle, N'}{\langle T \widehat{R}, \mu \rangle, M \parallel N \overset{e}{\rightsquigarrow}_{\text{RA}} \langle T \widehat{R}', \mu' \rangle, M \parallel N'}$ <p style="text-align: center;">READONLY</p> $\frac{\ell : v @ (q, \kappa_\ell] \langle \kappa \rangle \in \mu \quad \alpha_\ell \leq \kappa_\ell \quad \varphi_{\vec{w}} v = \perp \quad \omega = \alpha \sqcup \kappa}{\langle \dot{\alpha}, \mu \rangle, \mathbf{rmw}_\varphi(\ell; \vec{w}) \overset{\circ}{\rightsquigarrow}_{\text{RA}} \langle \dot{\omega}, \mu \rangle, v}$
--	---

Fig. 6. The notable operational semantics rules of λ_{RA} .

APP

$$\frac{}{\langle \dot{\kappa}, \mu \rangle, (\lambda a : A. M) V \overset{\circ}{\rightsquigarrow}_{\text{RA}} \langle \dot{\kappa}, \mu \rangle, M[a \mapsto V]}$$

<p style="text-align: center;">APPLEFT</p> $\frac{\langle T, \mu \rangle, M \overset{e}{\rightsquigarrow}_{\text{RA}} \langle T', \mu' \rangle, M'}{\langle T, \mu \rangle, MN \overset{e}{\rightsquigarrow}_{\text{RA}} \langle T', \mu' \rangle, M'N}$	<p style="text-align: center;">APPRIGHT</p> $\frac{\langle T, \mu \rangle, N \overset{e}{\rightsquigarrow}_{\text{RA}} \langle T', \mu' \rangle, N'}{\langle T, \mu \rangle, VN \overset{e}{\rightsquigarrow}_{\text{RA}} \langle T', \mu' \rangle, VN'}$
--	---

Fig. 7. The function-application rules in operational semantics of λ_{RA} .

current memory μ ; and a closed term M . The *state* of the configuration is the pair $\langle T, \mu \rangle$. The relation $\overset{e}{\rightsquigarrow}_{\text{RA}}$ represents (atomic) steps between configurations. The label e , distinguishing the memory-accessing steps (\bullet) from the rest (\circ), is used as a proof tool (Appendix C) and can be otherwise ignored. We denote $\rightsquigarrow_{\text{RA}} := \overset{\bullet}{\rightsquigarrow}_{\text{RA}} \cup \overset{\circ}{\rightsquigarrow}_{\text{RA}}$.

Sequential CBV constructs. We demonstrate the standard CBV transitions for function application in Figure 7: the APPELEFT and APPRIGHT congruence steps, and the APP β -reduction step. Omitted are the congruence steps for the assignment and RMW constructs, as well as the steps for tuples and variants. More generally, β -reductions use the \circ -label and view-leaves, and do not change the state; the congruence steps simply carry the label and states over.

The operational semantics hinges on the standard designation of certain terms as *values*:

$$V, W ::= \langle V_1, \dots, V_n \rangle \mid A.t \mid \lambda a : A. M \quad (\text{Values})$$

Substitution. A (program) *substitution* Θ is a partial function from program variables to closed values, which extends to the identity on all other variables, and then to terms by recursively applying to subprograms, removing bound variables from the substitution's domain, e.g.:

$$\Theta(\text{match } M \text{ with } \langle a_1, \dots, a_m \rangle. N) := \text{match } \Theta M \text{ with } \langle a_1, \dots, a_m \rangle. \Theta|_{\notin \{a_1, \dots, a_m\}} N$$

where $\Theta|_{\neq X}$ is obtained by removing X from Θ 's domain. We write $M[V_1/a_1 \dots V_n/a_n]$ for the application of the substitution that maps $a_i \mapsto V_i$ on M .

Parallel composition. The PARINIT rule initializes a parallel composition by duplicating its view-leaf to a new node. The rules PARLEFT and PARRIGHT non-deterministically interleave the evaluation of the left and right threads. After both threads evaluate, PARFIN joins the thread views back into a single leaf, and returns the pair of results.

EXAMPLE. We show an example execution from a birds-eye view:

$$\begin{aligned} \langle \mu_0, \hat{\alpha} \rangle, M ; (N_1 \parallel N_2) \rightsquigarrow_{RA}^* \langle \mu_1, \hat{\alpha}' \rangle, N_1 \parallel N_2 \rightsquigarrow_{RA} \langle \mu_1, \hat{\alpha}' \widehat{\alpha}' \rangle, N_1 \parallel N_2 \\ \rightsquigarrow_{RA}^* \langle \rho, \hat{\omega}_1 \widehat{\omega}_2 \rangle, V_1 \parallel V_2 \rightsquigarrow_{RA} \langle \rho, \omega_1 \sqcup \omega_2 \rangle, \langle V_1, V_2 \rangle \end{aligned}$$

First, M runs until it returns a value discarded by the sequencing construct. Next, the parallel composition $N_1 \parallel N_2$ activates. The threads then interleave executions, each with its associated side of the view-tree, interacting via the shared memory. Finally, once each thread returns a value, they synchronize.

Assignment. The STORE rule for location ℓ picks a free segment $(q, t]$ where t is strictly greater than the thread's view for ℓ . The step updates this thread's view to ω by increasing the timestamp for ℓ to t ; adds a message to memory with this updated view ω ; and returns the unit value.

Read-modify-write. The READONLY and RMW rules for the **rmw** construct both start by picking a message to the given location to read from that has the same or a larger timestamp than the thread's view, then incorporate the message's view in the thread's view, and finally return the value they read. If the given primitive modifier is undefined for the given parameters and message's value, nothing else happens (READONLY rule). If the modifier is defined (RMW rule), much like the STORE rule, a timestamp strictly greater than the thread's view for the location is chosen to update the thread's view, and a message is added with this updated view. In contrast to the STORE rule, here the added message's segment must dovetail with the message from which the RMW read, still avoiding any existing segment in this location. This dovetailing is only possible if we read from a message with no dovetailing succeeding message. In particular, a message can only be picked once to justify the RMW rule during an execution.

Initial states. An *initial memory* μ is a memory in which every location has exactly one message whose view contains the timestamps of the other messages. An *initial state* is a state consisting of an initial memory and a view-leaf that maps each location to the timestamp of the unique message in memory of that location. An *initial configuration* is a configuration consisting of an initial state and a closed term.

Evaluation. We're interested in the behaviors closed terms exhibit when run to completion. Let the Kleene star (*) denote the reflexive-transitive closure of a relation. A configuration $\langle T, \mu \rangle, M$ evaluates to a value V , written $\langle T, \mu \rangle, M \Downarrow_{RA} V$, when $\langle T, \mu \rangle, M \rightsquigarrow_{RA}^* \langle R, \rho \rangle, V$ for some state $\langle R, \rho \rangle$. We write $\langle T, \mu \rangle, M \not\Downarrow_{RA} V$ when there is no such $\langle R, \rho \rangle$. In the next examples, we write $M \Downarrow_{RA} V$ when M may evaluate to V from every initial state, and $M \not\Downarrow_{RA} V$ when it cannot evaluate to V from any initial state.

Example 5.2. We can give a more precise account of the litmus tests (SB) and (MP) from §2:

$$\begin{aligned} x := 0 ; y := 0 ; ((x := 1 ; y?) \parallel (y := 1 ; x?)) \not\Downarrow_{RA} \langle 0, 0 \rangle \\ x := 0 ; y := 0 ; ((x := 1 ; y := 1) \parallel (y? ; x?)) \not\Downarrow_{RA} \langle \langle \rangle, \langle 1, 0 \rangle \rangle \end{aligned}$$

5.2 Non-deterministic View Forwarding

It is technically convenient to extend $\rightsquigarrow_{\text{RA}}$ with an additional step that non-deterministically advances the view of a thread, as presented in [Figure 8](#). The ADV step advances the thread's view like the READONLY rule without changing the term component of the configuration. The effect of this step is to prohibit the thread from reading certain messages from memory, and propagating this prohibition to other threads that read values this thread writes. We think of this step as read-independent propagation of updates to threads. Lahav et al. [32] propose a similar extension when defining liveness conditions for RA.

A-priori, the resulting system may exhibit more behaviors since STORE and RMW steps will append messages with further advanced views. However, advancing views within messages only further constrains possible behaviors. We formalize this intuition using a simulation argument. One direction is straightforward: every execution in RA is an execution in RA_{\leq} admitted via the EXT rule, and so RA_{\leq} exhibits every behavior RA does. For the converse, we define a binary relation between configuration states \succsim such that $\langle T, \mu \rangle \succsim \langle R, \rho \rangle$ when the following hold.

- The simulatee's view-tree dominates the simulator's view-tree: $R \leq T$.
- There are bijections $\phi_{\ell} : \mu_{\ell} \rightarrow \rho_{\ell}$ for every location ℓ such that if $\phi_{\ell}(v) = \epsilon$, then the view of the simulatee's message dominates the simulator's message: $\epsilon.vw \leq v.vw$, and the messages' value and segment agree: $v.vl = \epsilon.vl$, $v.i = \epsilon.i$, $v.t = \epsilon.t$.

The relation \succsim is a weak simulation:

PROPOSITION 5.3. *If $\langle T, \mu \rangle \succsim \langle R, \rho \rangle$ and $\langle T, \mu \rangle, M \rightsquigarrow_{\text{RA}_{\leq}} \langle T', \mu' \rangle, M'$, then there exists a configuration state $\langle R', \rho' \rangle$ such that $\langle R, \rho \rangle, M \rightsquigarrow_{\text{RA}}^* \langle R', \rho' \rangle, M'$ and $\langle T', \mu' \rangle \succsim \langle R', \rho' \rangle$.*

PROOF. By induction on the step. An ADV step preserves \succsim , so we take no steps in the required corresponding RA execution. For RA steps admitted by EXT, we take a corresponding single RA step using the same RA rule. This, we need to show, also retains the simulation.

In the STORE case, we store the unique corresponding message that is permissible according to the rule. Other than the timestamp, the view is determined by the current view tree. In the READONLY case, we load the corresponding message according to the bijection given by \succsim . The RMW case is a combination of both of the above. The other cases retain the simulation as they propagate the state by induction or without change. \square

Like $\rightsquigarrow_{\text{RA}}$, so does $\rightsquigarrow_{\text{RA}_{\leq}}$ yield an evaluation semantics $\Downarrow_{\text{RA}_{\leq}}$. By [Proposition 5.3](#), they coincide:

COROLLARY 5.4. *For a configuration $\langle T, \mu \rangle, M$ and value V : $\langle T, \mu \rangle, M \Downarrow_{\text{RA}} V$ iff $\langle T, \mu \rangle, M \Downarrow_{\text{RA}_{\leq}} V$.*

Thus, we denote both by \Downarrow .

Remark. In RA_{\leq} we can restrict loading to occur only when the view already points to the message to load. That is, READONLY and RMW can be restricted to the case where $\alpha = \kappa$. Instead of loading a message using the unrestricted rule, we use ADV to "prepare" the view for loading, and then load using the restricted rule.

$$\begin{array}{c}
 \text{EXT} \\
 \frac{\langle T, \mu \rangle, M \rightsquigarrow_{\text{RA}} \langle T', \mu' \rangle, M'}{\langle T, \mu \rangle, M \rightsquigarrow_{\text{RA}_{\leq}} \langle T', \mu' \rangle, M'} \\
 \\
 \text{ADV} \\
 \frac{\ell:v@(q, \kappa_{\ell}) \llbracket \kappa \rrbracket \in \mu \quad \alpha_{\ell} \leq \kappa_{\ell} \quad \omega = \alpha \sqcup \kappa}{\langle \hat{\alpha}, \mu \rangle, M \rightsquigarrow_{\text{RA}_{\leq}} \langle \hat{\omega}, \mu \rangle, M}
 \end{array}$$

Fig. 8. RA_{\leq} operational semantics.

5.3 Semantic Invariants

Our denotational model uses semantic invariants that initial states possess and RA_{\leq} steps preserve. During our presentation of the invariants we give intuitive explanations for why they hold. These are formally grounded in [Theorem 5.14](#) and [Proposition 5.17](#) below.

Basic memory invariants. A memory μ is *scattered* if segments of messages in the same location are pairwise disjoint: $\forall \ell \in \text{Loc} \forall v, \epsilon \in \mu_{\ell}. v.\text{seg} \cap \epsilon.\text{seg} \neq \emptyset \implies v = \epsilon$. Initial memories are scattered and execution steps preserve the fact that the memory is scattered since added messages can only occupy unused segments.

Example 5.5. The memory below (left) is scattered, the segments of which we visualize along the timeline order without overlap (right) thanks to the scattering condition:

$$\left\{ \begin{array}{l} y:1@(-1, \underline{0}] \langle \langle x@5 \rangle \rangle, y:3@(0, \underline{7}] \langle \langle x@8 \rangle \rangle \\ x:0@(-1, \underline{0}] \langle \langle y@0 \rangle \rangle, x:2@(4, \underline{5}] \langle \langle y@7 \rangle \rangle \end{array} \right\} \quad \begin{array}{l} y: \left[\begin{array}{l} \epsilon_1 \ 1@(-1, \langle \langle 0; x@5 \rangle \rangle] \\ \epsilon_2 \ 3@(0, \langle \langle 7; x@8 \rangle \rangle] \end{array} \right] \\ x: \left[\begin{array}{l} \nu_1 \ 0@(-1, \langle \langle 0; y@0 \rangle \rangle] \\ \nu_2 \ 2@(4, \langle \langle 5; y@7 \rangle \rangle] \end{array} \right] \end{array}$$

We think of timestamps as names, i.e., abstract pointers. Formally, a view κ *points to* a message ϵ , denoted by $\kappa \succ \epsilon$, when κ holds ϵ 's timestamp at ϵ 's location: $\kappa_{\epsilon.\text{lc}} = \epsilon.\text{t}$. A view κ *points to* memory μ , denoted by $\kappa \succ \mu$, when it points to a μ -message in all locations: $\forall \ell \in \text{Loc} \exists \epsilon \in \mu_{\ell}. \kappa \succ \epsilon$. A message v *points to* another message ϵ or memory μ when its view $v.\text{vw}$ points to that message or memory, denoted by $v \succ \epsilon$ and $v \succ \mu$. A memory μ is *connected* when it is scattered, and every message within it points to it: $\forall v \in \mu. v \succ \mu$.

Example 5.6. The memory from [Example 5.5](#) is not connected: ϵ_2 doesn't point to any message in x . In contrast, the memory below (left) is connected; we visualize its timestamp orders (middle) and points-to relations (right) thanks to the connectedness condition:

$$\left\{ \begin{array}{l} y:2@(-1, \underline{5}] \langle \langle x@0 \rangle \rangle, y:4@(0, \underline{7}] \langle \langle x@0 \rangle \rangle \\ x:1@(-1, \underline{0}] \langle \langle y@0 \rangle \rangle, x:3@(4, \underline{5}] \langle \langle y@7 \rangle \rangle \end{array} \right\} \quad \begin{array}{l} y: \left[\begin{array}{l} \epsilon_1 \ 2 \\ \epsilon_2 \ 4 \end{array} \right] \\ x: \left[\begin{array}{l} \nu_1 \ 1 \\ \nu_2 \ 3 \end{array} \right] \end{array} \quad \begin{array}{c} \nu_1 \xleftarrow{x} \epsilon_2 \\ \downarrow y \qquad \uparrow y \\ \epsilon_1 \xrightarrow{x} \nu_2 \end{array}$$

Initial memories are connected, and execution steps preserve memory connectedness, assuming that all thread views point to the current memory: when a thread adds a message to memory, it uses its own view with an advanced timestamp for the message's location, maintaining connectedness.

Causal memory invariants. The points-to relation tracks some causal dependencies. Intuitively, events should not be caused by future events, so causal paths, i.e. paths in $\mu.\text{gph} := \langle \mu, (\succ) \text{id}_{\mu} \rangle$, should not lead to the future along any timeline. We refine the points-to relation to enforce this.

Formally, we say that a view κ *points downwards to* a message ϵ , written $\kappa \hookrightarrow \epsilon$ when it points to it, $\kappa \succ \epsilon$, and it dominates ϵ 's view, $\kappa \geq \epsilon.\text{vw}$. A view *points downwards into* a scattered memory μ , denoted by $\kappa \hookrightarrow \mu$, when it points downward to a message in μ in every location, i.e.: $\forall \ell \in \text{Loc} \exists \epsilon \in \mu_{\ell}. \kappa \hookrightarrow \epsilon$. We say that a message points downward into a memory, writing $v \hookrightarrow \mu$, when its view does: $v.\text{vw} \hookrightarrow \mu$. We say that a memory μ is *causally connected*, when it is connected, and every message within it points downwards into it: $\forall v \in \mu. v \hookrightarrow \mu$.

To further conserve space in the following examples, we omit locations from messages, instead tagging the row in the set. For example, by $5@(\underline{6}, \underline{7}] \langle \langle 7 \rangle \rangle$ in the y row we mean $y:5@(\underline{6}, \underline{7}] \langle \langle x@7 \rangle \rangle$.

Example 5.7. The memory from [Example 5.6](#) is not causally connected because $\epsilon_1 \succ v_2$ while nonetheless $\epsilon_1.vw_y = 0 \not\geq 7 = v_2.vw_y$. The following memory is causally connected:

$$\left\{ \begin{array}{l} y : 1@(-1, \underline{0}] \langle\langle 0 \rangle\rangle, 3@(0, \underline{5}] \langle\langle 0 \rangle\rangle, 5@(6, \underline{7}] \langle\langle 7 \rangle\rangle \\ x : 0@(-1, \underline{0}] \langle\langle 0 \rangle\rangle, 2@(4, \underline{5}] \langle\langle 0 \rangle\rangle, 4@(5, \underline{7}] \langle\langle 7 \rangle\rangle \end{array} \right\} \quad \begin{array}{l} y: \begin{array}{|c|c|c|} \hline \epsilon_1 & \epsilon_2 & \epsilon_3 \\ \hline \end{array} \\ x: \begin{array}{|c|c|c|} \hline v_1 & v_2 & v_3 \\ \hline \end{array} \end{array} \quad \begin{array}{c} v_1 \xleftarrow{x} \epsilon_2 \quad v_3 \\ y \downarrow \quad \uparrow x \quad y \downarrow \quad \uparrow x \\ \epsilon_1 \xleftarrow{y} v_2 \quad \epsilon_3 \end{array}$$

Initial memories are causally connected, and execution steps preserve this together with view-trees labeled solely by downward-pointing views. In showing this, particularly when observing steps that load a message, the following fact helps; pointing downwards is a stronger condition than may first appear:

LEMMA 5.8. *Assume μ is causally connected. Then $\kappa \hookrightarrow \mu$ iff $\kappa = \sqcup \{\epsilon \in \mu \mid \kappa \succ \epsilon\}.vw$.*

PROOF. For the “if” (\Leftarrow) direction, let $\ell' \in \text{Loc}$. Then

$$\kappa_{\ell'} = (\sqcup \{\epsilon \in \mu \mid \kappa \succ \epsilon\}.vw)_{\ell'} = \max \{\epsilon.vw_{\ell'} \mid \kappa \succ \epsilon \in \mu\}$$

In particular, $\kappa \succ \mu$. Moreover, $\kappa \hookrightarrow \mu$ because whenever $\kappa \succ \epsilon \in \mu$, we have $\kappa \geq \epsilon.vw$.

Conversely (\Rightarrow), let $\ell' \in \text{Loc}$. Then $\{\epsilon \in \mu_{\ell'} \mid \kappa \succ \epsilon\}.vw = \{\epsilon.vw\}$, and $\kappa_{\ell'} = \epsilon.t = \epsilon.vw_{\ell'}$. Thus, $\kappa_{\ell'} = (\sqcup \{\epsilon \in \mu_{\ell'} \mid \kappa \succ \epsilon\}.vw)_{\ell'} \leq (\sqcup \{\epsilon \in \mu \mid \kappa \succ \epsilon\}.vw)_{\ell'}$. So $\kappa \leq \sqcup \{\epsilon \in \mu \mid \kappa \succ \epsilon\}.vw$. Since $\kappa \hookrightarrow \mu$, if $\kappa \succ \epsilon \in \mu$ then $\kappa \geq \epsilon.vw$. Thus $\kappa \geq \sqcup \{\epsilon \in \mu \mid \kappa \succ \epsilon\}.vw$. \square

Paths in a causally connected memory’s graph descend down its timelines:

PROPOSITION 5.9. *Let μ be a causally connected memory, with a path $v \succ^* \epsilon$ in $\mu.\text{gph}$.*

- (1) *Views decrease along the path: $v.vw \geq \epsilon.vw$.*
- (2) *If there is also a path $\epsilon \succ^* v$, i.e., v and ϵ are part of a cycle, then $v.vw = \epsilon.vw$.*
- (3) *If they share the location, $v.lc = \epsilon.lc$, their timestamps decrease along the path: $v.t \geq \epsilon.t$.*

PROOF. Item 1 follows from the fact that the memory is causally connected in the case of a single-edge path. This extends to a path of any length by induction. The other items are direct consequences of the first. \square

If a causally connected memory μ has a message in location ℓ , then it has a timestamp-minimal one which we denote by $\min \mu_{\ell}$, i.e. $(\min \mu_{\ell}).t = \min \mu_{\ell}.t$. We say that causally connected memory μ is *well-formed* when it has at least one message at each location, and cycles within $\mu.\text{gph}$ consist solely of minimal messages, i.e. if $v \in \mu$ is part of a cycle in $\mu.\text{gph}$, then $v = \min \mu_{v.lc}$.

Example 5.10. The memory from [Example 5.7](#) is not well-formed: its minimal messages are v_1 and ϵ_1 , but v_3 and ϵ_3 are on a cycle. The following memory is well-formed:

$$\left\{ \begin{array}{l} y : 1@(-1, \underline{0}] \langle\langle 0 \rangle\rangle, 3@(0, \underline{5}] \langle\langle 7 \rangle\rangle, 5@(6, \underline{7}] \langle\langle 0 \rangle\rangle \\ x : 0@(-1, \underline{0}] \langle\langle 0 \rangle\rangle, 2@(4, \underline{5}] \langle\langle 7 \rangle\rangle, 4@(5, \underline{7}] \langle\langle 0 \rangle\rangle \end{array} \right\} \quad \begin{array}{l} y: \begin{array}{|c|c|c|} \hline \epsilon_1 & \epsilon_2 & \epsilon_3 \\ \hline \end{array} \\ x: \begin{array}{|c|c|c|} \hline v_1 & v_2 & v_3 \\ \hline \end{array} \end{array} \quad \begin{array}{c} v_1 \xleftarrow{x} \epsilon_3 \xleftarrow{y} v_2 \\ y \downarrow \quad \uparrow x \quad y \downarrow \quad \uparrow x \\ \epsilon_1 \xleftarrow{y} v_3 \xleftarrow{x} \epsilon_2 \end{array}$$

Initial memories are well-formed, and being well-formed is an invariant of execution steps. Indeed, messages are added one-by-one and point to existing messages, so they cannot form a new cycle; and messages are added with a larger timestamp, so minimal messages remains minimal.

PROPOSITION 5.11. *Let μ be a well-formed memory, and $\ell \in \text{Loc}$.*

- (1) *Minimal messages point at minimal messages: if $\min \mu_{\ell} \hookrightarrow v$, then v is a minimal message.*
- (2) *Memory extension preserves minimal messages: if $\mu \subseteq \rho$ is well-formed, then $\min \mu_{\ell} = \min \rho_{\ell}$.*

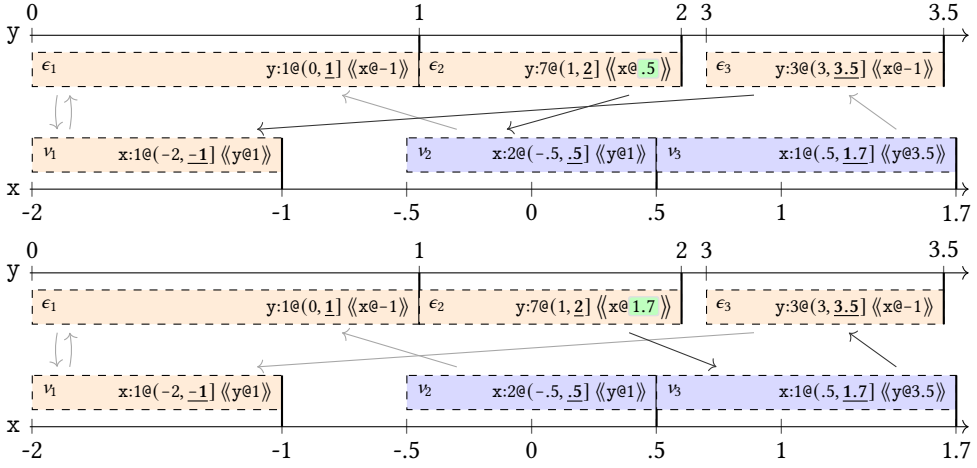


Fig. 9. Two variations on the memory illustrated in Figure 1. **Top:** This memory is well-formed. It demonstrates that the views of messages along a timeline do not have to be ordered: ϵ_2 appears earlier than ϵ_3 on y 's timeline but points to a later message on x 's timeline. **Bottom:** This memory is not well-formed because it contains an ascending path, in contradiction to Proposition 5.9. Intuitively, no thread could have written ϵ_2 because the view that ϵ_2 carries indicates that the thread would have already “known” about v_3 and therefore, following the causality chain, about ϵ_3 as well. Thus, the thread would have been forbidden from picking ϵ_2 's timestamp.

PROOF OF (1). Since μ is connected, there exists $\epsilon \in \mu_\ell$ such that $v \rightsquigarrow \epsilon$. By Proposition 5.9, $(\min \mu_\ell).t \geq \epsilon.t$. By minimality $(\min \mu_\ell).t = \epsilon.t$, and since μ is scattered, $\epsilon = \min \mu_\ell$. Thus v is on a cycle (with ϵ). Since μ is built-up, v is minimal. \square

PROOF OF (2). Since μ is well-formed, $\min \mu_\ell$ appears in a cycle in $\mu.gph$, and thus in a cycle of the supergraph $\rho.gph$. Since ρ is well-formed, $\min \mu_\ell$ is minimal in ρ . \square

We denote the set of well-formed memories by Mem . Figure 9 gives a positive example (top) and a negative example (bottom).

View-tree invariants. Like memories, view-trees also satisfy certain invariants during execution. In particular, the invariant that all thread views point downwards into the current memory depends on the invariants of memory, and vice-versa. Formally, we say that a view-tree *points to/downward into* memory μ , and write $T \rightsquigarrow \mu$ and $T \hookrightarrow \mu$ when $\kappa \rightsquigarrow \mu$ and $\kappa \hookrightarrow \mu$ for every $\kappa \in T$.l.f. We then say that a state $\langle T, \mu \rangle$ is *well-formed* when μ is well-formed and $T \hookrightarrow \mu$.

While the labels of the view-tree are related to the memory, its structure is intimately related to the syntactic structure of the configuration's term. We define this property as an inductive relation $T \Vdash M$ specifying when T is *well-formed* for a term M . Every view-leaf is well-formed for any term. A view-node is well-formed for a parallel composition only when its immediate subtrees are well-formed for each thread. The rest of the rules reach through the term's evaluation context until they find a parallel composition sub-term. These follow the congruence rules from the operational semantics, which we demonstrate with the λ -L and λ -R rules:

$$\begin{array}{c}
 \text{LEAF} \\
 \hline
 \dot{\kappa} \Vdash M
 \end{array}
 \qquad
 \begin{array}{c}
 \text{NODE} \\
 T \Vdash M \quad R \Vdash N \\
 \hline
 T \widehat{\ } R \Vdash M \parallel N
 \end{array}
 \qquad
 \begin{array}{c}
 \lambda\text{-L} \\
 T \widehat{\ } R \Vdash M \\
 \hline
 T \widehat{\ } R \Vdash MN
 \end{array}
 \qquad
 \begin{array}{c}
 \lambda\text{-R} \\
 T \widehat{\ } R \Vdash N \\
 \hline
 T \widehat{\ } R \Vdash VN
 \end{array}$$

Example 5.12. For $M = (\lambda a. N_1 \parallel N_2) (M_1 \parallel M_2)$, we have $\kappa \Vdash M$ and $\widehat{\kappa_1} \widehat{\kappa_2} \Vdash M$. Intuitively, the evaluation context in M is $(\lambda a. N_1 \parallel N_2) [-]$, and the active component—where reduction takes place—is $(M_1 \parallel M_2)$. The execution of $N_1 \parallel N_2$ is suspended under the λ -abstraction, so we associate no views with its threads. The view-node is well-formed for the active component by `NODE`.

For $N = (\lambda a. N_1 \parallel N_2) V$, the view node is *not* well-formed: $\widehat{\kappa_1} \widehat{\kappa_2} \not\Vdash N$. The evaluation context is empty, and the active (single) thread is $(\lambda a. N_1 \parallel N_2) V$: the next execution step has to be `λ -APP`. Only a view-leaf is well-formed for such a program.

By inspecting the inductive definition of (\Vdash) we find that no two rules can arrive at the same conclusion. This means that the rules are all invertible: for any instantiation of any rule, if the conclusion holds, then so do all the premises. This means that when $T \Vdash M$ we can easily and uniquely associate each subtree of T to a subterm of M . Moreover, the leaves of T are associated to threads within M such that there is no overlap.

Example 5.13. Returning to [Example 5.12](#), by inverting $\widehat{\kappa_1} \widehat{\kappa_2} \Vdash M$ we find which leaf associates to which subterm: $\kappa_i \Vdash M_i$. Similarly, by inverting $\kappa \Vdash M$ we find that $\kappa \Vdash M_1 \parallel M_2$. Intuitively, the subthreads in the latter have not yet been activated (using `PARINIT`).

Execution invariants. Collecting the invariants, a configuration $\langle T, \mu \rangle, M$ is *well-formed of type A* when: its state $\langle T, \mu \rangle$ is well-formed; its term is closed of type A , i.e. $\cdot \vdash M : A$; and its view-tree is well-formed for its term, i.e. $T \Vdash M$. We show that `RA≤` steps preserve well-formedness:

THEOREM 5.14 (PRESERVATION). *If $\langle T, \mu \rangle, M \rightsquigarrow_{\text{RA}_{\leq}} \langle R, \rho \rangle, N$ and $\langle T, \mu \rangle, M$ is a well-formed configuration of type A , then $\langle R, \rho \rangle, N$ is a well-formed configuration of type A .*

PROOF. By induction on the step. Type-preservation is standard, so we focus on showing the other aspects of well-formedness of the configuration: that the tree is well-formed (for the term) and that the tree points downwards (into the memory).

We start with the tree being well-formed for the term. The view-tree after the step is well-formed if it is a leaf by the `LEAF` rule. This covers `ADV`, `PARFIN`, and all the β -reductions.

Otherwise, we use the well-formedness rule that corresponds to the step. The well-formedness rules are invertible and derivations of well-formedness are unique, so we can apply the inverse rule before the step. For the `APPLEFT` case we use the λ -L rule and the induction hypothesis. Similarly with `PARLEFT` and the `NODE` rule. The only case where we do not have an inductive hypothesis to rely on is the `PARINIT` case, where we use the `NODE` rule and the `LEAF` rule for the premises.

It remains to show that the tree points downwards into the memory. The memory doesn't change in the `PARFIN` case, into which the view-leaf after the step points downward since it is the pointwise maximum of views that do. We use this fact that pointwise maximum preserves pointing downwards again for those steps that load a message (`LOADONLY` and `RMW`) and for the `ADV` rule. The steps that add a message (`STORE` and `RMW`) change the timestamp by increasing it, therefore preserving pointing downwards with respect to the other locations. With respect to the location itself, the property holds because the view-leaf points to the added message which has the same view, and views dominate themselves.

For `PARINIT` and the β -reductions, the claim is trivial because the set of views does not change. The remaining steps are congruence rules, where except for `PARLEFT` and `PARRIGHT`, the claim either follows immediately from the inductive hypothesis because the states are the same in the premise. For `PARLEFT` and `PARRIGHT`, we need to also show that the other side of the view-tree points downwards into the new memory. This holds because pointing downwards is stable under adding messages to memory, which is the only way the memory can change by taking any step. \square

Henceforth we restrict execution steps to be between well-formed configurations, noting that initial configurations are always well-formed. Under this assumption, execution steps maintain some relationships between states. To start, we observe that the timestamp of a new message lies between some thread's initial and final views:

LEMMA 5.15. *Assume $\langle T, \mu \rangle, M \rightsquigarrow_{RA_{\leq}}^* \langle R, \rho \rangle, N$ changed the memory, i.e. $\rho \neq \mu$. Then: the trees have the same shape; $T \leq R$; and there is a message v such that $\rho = \mu \uplus \{v\}$. Moreover, there are view-leaves $\dot{\alpha}$ in T and $\dot{\omega}$ in R in corresponding positions, such that $\alpha \leq v.v\omega \leq \omega$ and $\alpha_{v.1c} < v.t$.*

PROOF. The ADV rule does not change the memory. For the EXT rule we proceed by induction on the RA step. The congruence cases are all immediate from the induction hypothesis. Of the others, only STORE and RMW add a message, in which cases the premises ensure the claim holds. \square

The view-tree structure changes during PARINIT and PARFIN, so they cannot always be compared leaf-to-leaf as in Lemma 5.15. However, the sets of views that label each tree still maintain the Egli-Milner order induced by the view order:

LEMMA 5.16 (EGLI-MILNER FOR VIEW-LEAVES). *Assume $\langle T, \mu \rangle, M \rightsquigarrow_{RA_{\leq}}^* \langle R, \rho \rangle, N$.*

- *For every $\alpha \in T.1f$, there exists a leaf $\omega \in R.1f$, such that $\alpha \leq \omega$.*
- *For every $\omega \in R.1f$, there exists a leaf $\alpha \in T.1f$, such that $\alpha \leq \omega$.*

PROOF. This property extends from a single step inductively. In the ADV rule case the premise is the required $\alpha \leq \omega$ for both items. For the EXT rule, we proceed by induction on the RA step. The congruence cases and those that do not change the view-tree are all immediate from the induction hypothesis. In the memory-accessing steps, the claim follows from their premises. The cases that change the tree structure, PARINIT and PARFIN, are trivial to check. \square

We combine Lemmas 5.15 and 5.16 to obtain the following execution invariant:

PROPOSITION 5.17 (VIEWS DELIMIT EXECUTION). *Assume $\langle T, \mu \rangle, M \rightsquigarrow_{RA_{\leq}}^* \langle R, \rho \rangle, N$. Assume that α is dominated by every view in $T.1f$, and that ω dominates every view in $R.1f$. Then $\alpha \leq \omega$; and for every added message $v \in \rho \setminus \mu$, both $\alpha \leq v.v\omega \leq \omega$ and $\alpha_{v.1c} < v.t$.*

PROOF. That $\alpha \leq \omega$ follows from Lemma 5.16. The rest follows by induction on the number of steps. Indeed, combining our assumption with Lemma 5.15, when a message is added there exist $\alpha' \in T.1f$ and $\omega' \in R.1f$ such that, $\alpha \leq \alpha' \leq v.v\omega \leq \omega' \leq \omega$ and $\alpha_{v.1c} \leq \alpha'_{v.1c} < v.t$. \square

Interrupted executions. To analyze program behavior under concurrent contexts, we have to take into account all possible ways in which the environment can interfere during the execution. An interrupted execution $\langle T, \mu \rangle, M \rightsquigarrow_{RA_{\leq}}^* \dots \rightsquigarrow_{RA_{\leq}}^* \langle R, \rho \rangle, V$ is a sequence of executions of the form

$$\begin{aligned} \langle T, \mu \rangle, M = \langle T_1, \mu_1 \rangle, M_1 &\rightsquigarrow_{RA_{\leq}}^* \langle T_2, \rho_1 \rangle, M_2 \\ &\langle T_2, \mu_2 \rangle, M_2 \rightsquigarrow_{RA_{\leq}}^* \langle T_3, \rho_2 \rangle, M_3 \\ &\vdots \\ \langle T_n, \mu_n \rangle, M_n &\rightsquigarrow_{RA_{\leq}}^* \langle T_{n+1}, \rho_n \rangle, M_{n+1} = \langle R, \rho \rangle, V \end{aligned}$$

where $\rho_j \subseteq \mu_{j+1}$ for every $1 \leq j \leq n-1$. Between the executions in the sequence, the configuration only changes by adding *environment messages*, the messages in $\mu_{j+1} \setminus \rho_j$, to the memory—the only interference the environment can cause. We also have $\mu_i \subseteq \rho_i$, and we call the messages in $\rho_j \setminus \mu_j$ *local messages*. Proposition 5.17 extends to interrupted executions in a straightforward manner, replacing $\rightsquigarrow_{RA_{\leq}}^*$ with $\rightsquigarrow_{RA_{\leq}}^* \dots \rightsquigarrow_{RA_{\leq}}^*$ and replacing *added* messages with *local* messages.

6 Denotational Semantics

Based on Moggi's monadic approach (§6.1) to denotational semantics as a basis, we design a framework for denotational semantics using Brookes-style traces (§6.2) adapted to describe behavior under RA. We then build upon this framework progressively.

First we define the *generating* denotational semantics (§6.3). The monad structure underlying this semantics does not satisfy the monad laws, and so does not fully conform to the monadic approach. Still, it is useful in forming a base for the next stage, as a metatheoretic tool, as well as a means to simpler calculations.

Thus, we define the *concrete* denotational semantics (§6.4). Here we do have a monad, but the denotational semantics follows the operational semantics too closely to be as abstract as we would like, evident in program transformations that it does not support. This semantics is useful as an intermediate step, and plays a central role in our proof of the adequacy theorem.

Finally, we define the *abstract* denotational semantics (§6.5). This is the semantics we were aiming for: adequate and abstract enough to justify transformations of interest.

6.1 Monad-based Semantics

We recap Moggi's [40] approach to interpret a CBV calculus like λ_{RA} using a monad. A *monad structure* $\mathcal{T} = \langle \underline{\mathcal{T}}, \text{return}^{\mathcal{T}}, (\gg)^{\mathcal{T}} \rangle$ consists of three components: a set-level function $\underline{\mathcal{T}}$; a set-indexed function $\text{return}^{\mathcal{T}}$; and a two-argument set-indexed function $(\gg)^{\mathcal{T}}$. The set-level function assigns to each set X , whose elements represent fully-evaluated semantic values, the set $\underline{\mathcal{T}}X$, whose elements represent unevaluated effectful programs returning values in X . The functions $\text{return}_X^{\mathcal{T}} : X \rightarrow \underline{\mathcal{T}}X$, the *unit*, represent the program fragment that returns its input without any observable side-effects. The two-argument functions $(\gg)_{X,Y}^{\mathcal{T}} : (\underline{\mathcal{T}}X) \times (X \rightarrow \underline{\mathcal{T}}Y) \rightarrow \underline{\mathcal{T}}Y$, the *monadic bind*, represent the sequencing $P \gg_{X,Y}^{\mathcal{T}} f$ of an X -returning program P with an Y -returning program f that depends on the result of the former program P . We often omit the monad and the set-indexing from notations, leaving them implicit.

Moggi's innovation is to take the traditional type and value semantics, following a long tradition of denotational semantics, and retain its uniform structure even for effectful computation, by using a monad structure. Each construct has a corresponding semantic construct, and the interpretation proceeds structurally over the structure of types, context and terms.

Type semantics. Every type A denotes a set, where: product types denote the cartesian product; variants denote tagged unions; function types use the monad structure to denote the set of parameterized computations; and typing environments denote the cartesian product:

$$\begin{aligned} \llbracket \Gamma \rrbracket_{\mathcal{T}} &:= \prod_{(a:A) \in \Gamma} \llbracket A \rrbracket_{\mathcal{T}} & \llbracket A \rightarrow B \rrbracket_{\mathcal{T}} &:= \llbracket A \rrbracket_{\mathcal{T}} \rightarrow \underline{\mathcal{T}}\llbracket B \rrbracket_{\mathcal{T}} & \llbracket (A_1 * \dots * A_n) \rrbracket_{\mathcal{T}} &:= \llbracket A_1 \rrbracket_{\mathcal{T}} \times \dots \times \llbracket A_n \rrbracket_{\mathcal{T}} \\ \llbracket \{t_1 \text{ of } A_1 \mid \dots \mid t_n \text{ of } A_n\} \rrbracket_{\mathcal{T}} &:= (\{t_1\} \times \llbracket A_1 \rrbracket_{\mathcal{T}}) \cup \dots \cup (\{t_n\} \times \llbracket A_n \rrbracket_{\mathcal{T}}) \end{aligned}$$

In particular, denotations of ground types $\llbracket G \rrbracket_{\mathcal{T}}$ do not depend on the monad structure. For example, $\llbracket \text{Val} \rrbracket_{\mathcal{T}}$ is in a natural bijection with the (storable) values Val , and we will identify them.

Value semantics. Every value $\Gamma \vdash V : A$ denotes a function $\llbracket V \rrbracket_{\mathcal{T}}^{\vee} : \llbracket \Gamma \rrbracket_{\mathcal{T}} \rightarrow \llbracket A \rrbracket_{\mathcal{T}}$, taking as argument a *semantic environment* $\gamma \in \llbracket \Gamma \rrbracket_{\mathcal{T}}$ supplying a semantic value to each variable in Γ :

$$\begin{aligned} \llbracket b \rrbracket_{\mathcal{T}}^{\vee}(\gamma_{(a:A) \in \Gamma}) &:= \gamma_b & \llbracket A.tV \rrbracket_{\mathcal{T}}^{\vee} &:= \langle t, \llbracket V \rrbracket_{\mathcal{T}}^{\vee} \gamma \rangle & \llbracket (V_1, \dots, V_n) \rrbracket_{\mathcal{T}}^{\vee} &:= \langle \llbracket V_1 \rrbracket_{\mathcal{T}}^{\vee} \gamma, \dots, \llbracket V_n \rrbracket_{\mathcal{T}}^{\vee} \gamma \rangle \\ \llbracket \lambda b : B. M \rrbracket_{\mathcal{T}}^{\vee}(\gamma_{(a:A) \in \Gamma}) &:= \lambda \gamma_b. \llbracket M \rrbracket_{\mathcal{T}}^{\vee}(\gamma_{(a:A) \in \Gamma, b:B}) \end{aligned}$$

We capture the evolving assumptions and guarantees about memory messages by a *chronicle*: a possibly empty finite sequence of transitions $\xi = \langle \mu_1, \rho_1 \rangle \dots \langle \mu_n, \rho_n \rangle$ where $\rho_j \subseteq \mu_{j+1}$ for every j . When ξ is non-empty, we denote its *opening* and *closing* memories by $\xi.o := \mu_1$ and $\xi.c := \rho_n$. Its *local messages* are the ones added within transitions: $\xi.\text{own} := \bigcup_{i \in \{1, \dots, n\}} (\rho_i \setminus \mu_i)$, and its *environment messages* are the others. Let Chro be the set of chronicles, ranged over by ξ, η .

In the operational semantics, some messages are obscured from any particular thread due to its view. The trace captures only an *initial view* that declares which messages may be relied on to be available at the beginning, and a *final view* that declares which messages are guaranteed to be available at the end. Together, these are the *delimiting views*.

Finally, a trace includes a semantic representation of the returned value [e.g. 6] Given a set representing semantic return values X , an X -*pre-trace* is an element of $\text{View} \times \text{Chro} \times \text{View} \times X$, written $\alpha \boxed{\xi} \omega .: r$, whose chronicle component is non-empty. We range over pre-traces with τ, π, ϱ , and use $\tau.\text{ivw}$ (initial view), $\tau.\text{ch}$ (chronicle), $\tau.\text{fvw}$ (final view), $\tau.\text{ret}$ (returned value) to retrieve the components of a pre-trace $\tau = \alpha \boxed{\xi} \omega .: r$ in order.

Such an X -pre-trace τ is an X -*trace* when each transition in ξ consists of well-formed memories; the initial view precedes the final views, each pointing downwards into the opening and closing memories respectively: $\xi.o \leftrightarrow \alpha \leq \omega \leftrightarrow \xi.c$; and the view and segment of every local message are bound by the delimiting views, i.e.: $\forall v \in \xi.\text{own}. \alpha \leq v.\text{vw} \leq \omega \wedge \alpha_{v.\text{lc}} < v.\text{t}$. These conditions reflect well-formedness and the invariants from [Theorem 5.14](#) and [Proposition 5.17](#). We denote the set of X -traces by $\text{Trace}X$. The bottom of [Figure 1](#) depicts an example trace.

Rewrite rules. Semantics of terms $P \in \mathcal{T}X$ in trace semantics are sets of traces, representing the possible behaviors, including possible environment interference. As subsets, they carry a natural inclusion order. We write $\llbracket M \rrbracket_{\mathcal{T}}^c \subseteq \llbracket N \rrbracket_{\mathcal{T}}^c$ to mean containment in every context, that is $\forall \gamma \in \llbracket \Gamma \rrbracket_{\mathcal{T}}. \llbracket M \rrbracket_{\mathcal{T}\gamma}^c \subseteq \llbracket N \rrbracket_{\mathcal{T}\gamma}^c$. Intuitively, this means that every behavior of M is a behavior of N .

Particularly, we will be looking at sets of traces closed under certain rewrite rules reflecting the way in which traces represent possible behaviors. A *rewrite rule* \times is a binary relation between pre-traces. Its elements, written $\tau \xrightarrow{\times} \pi$, are called \times -*rewrites* from a *source* τ to a *target* π . Let \star be a set of rewrite rules. We write $\tau \xrightarrow{\star} \pi$ when $\tau \xrightarrow{\times} \pi$ for some $\times \in \star$. A set $U \subseteq \text{Trace}X$ is \star -*closed* when $\tau \in U$ and $\tau \xrightarrow{\star} \pi \in \text{Trace}X$ implies $\pi \in U$. The \star -*closure* of a set $U \subseteq \text{Trace}X$, denoted U^\star , is the least \star -closed superset of U . Thus U is \star -closed iff $U = U^\star$. We denote the set of countable \star -closed subsets of E by $\mathcal{P}_{\text{ctbl}}^\star(E) := \{U \in \mathcal{P}_{\text{ctbl}}(E) \mid U = U^\star\}$. We \star -*close* a function ϕ that returns sets of traces by composition with the closure: $\phi^\star := -^\star \circ \phi$. We say that a function ϕ is *pointwise \star -closed* when $\phi = \phi^\star$. We say that a function ϕ between subsets of traces is \star -*closed* when its restriction to \star -closed subsets is pointwise closed.

[Table 1](#) summarizes all the rewrite rules we will use. This compact figure packs many side conditions and new notation, which we explain as we present the rules. When presenting a rewrite rule we omit the return value, because they all maintain it.

Monad structure. Given a choice of rewrite rules \star , we define the \star -*monad structure* \mathcal{T} as follows. The set-level function of \mathcal{T} 's monad structure sends every set X to a countable \star -closed sets of X -traces: $\mathcal{T}X := \mathcal{P}_{\text{ctbl}}^\star(\text{Trace}X)$. The unit yields all single-transition traces that maintain the view and the memory. The bind appends traces with compatible intermediate views:

$$\begin{aligned} \text{return}_{\mathcal{T}}^\star r &:= \{\kappa \boxed{\langle \mu, \mu \rangle} \kappa .: r \in \text{Trace}X\}^\star \\ P \gg_{\mathcal{T}}^\star f &:= \{\alpha \boxed{\xi \eta} \omega .: s \in \text{Trace}Y \mid \alpha \boxed{\xi} \kappa .: r \in P, \kappa \leq \sigma, \sigma \boxed{\eta} \omega .: s \in fr\}^\star \end{aligned}$$

Parallel composition. $(\parallel^\mathcal{T})$ interleaves chronicles and pairs the returned values. The delimiting views must bound the views of the resulting traces, so we take the greatest lower bound of the

Table 1. Summary of all rewrite rules.

g	Loosen	$\alpha \overline{\xi(\eta \overline{\cup \{\epsilon\}})} \omega$	$\xrightarrow{\text{Ls}}$	$\alpha \overline{\xi(\eta \overline{\cup \{v\}})} \omega$	$v \leq_{\text{vw}} \epsilon$
	Expel	$\alpha \overline{\xi(\eta \overline{\cup \{\epsilon_1^{v.i}\}})} \omega$	$\xrightarrow{\text{Ex}}$	$\alpha \overline{\xi(\eta \overline{\cup \{v, \epsilon\}})} \omega$	$v \prec \epsilon$
	Condense	$\alpha \overline{\xi(\eta \overline{\cup \{v, \epsilon\}})} \omega$	$\xrightarrow{\text{Cn}}$	$(\alpha \overline{\xi(\eta \overline{\cup \{v\}})} \omega) [\uparrow \epsilon]$	$v \not\prec \epsilon$
c	Stutter	$\alpha \overline{\xi \eta} \omega$	$\xrightarrow{\text{St}}$	$\alpha \overline{\xi \langle \mu, \mu \rangle \eta} \omega$	Rewind $\kappa \overline{\xi} \omega \xrightarrow{\text{Rw}}$ $\alpha \overline{\xi} \omega$ $\alpha \leq \kappa$
	Mumble	$\alpha \overline{\xi \langle \mu, \rho \rangle \langle \rho, \theta \rangle \eta} \omega$	$\xrightarrow{\text{Mu}}$	$\alpha \overline{\xi \langle \mu, \theta \rangle \eta} \omega$	Forward $\alpha \overline{\xi} \kappa \xrightarrow{\text{Fw}}$ $\alpha \overline{\xi} \omega$ $\kappa \leq \omega$
a	Tighten	$\alpha \overline{\xi \langle \mu, \rho \cup \{v\} \rangle \eta \overline{\cup \{v\}}} \omega$	$\xrightarrow{\text{Tt}}$	$\alpha \overline{\xi \langle \mu, \rho \cup \{\epsilon\} \rangle \eta \overline{\cup \{\epsilon\}}} \omega$	$v \leq_{\text{vw}} \epsilon$
	Absorb	$\alpha \overline{\xi \langle \mu, \rho \cup \{v, \epsilon\} \rangle \eta \overline{\cup \{v, \epsilon\}}} \omega$	$\xrightarrow{\text{Ab}}$	$\alpha \overline{\xi \langle \mu, \rho \cup \{\epsilon_1^{v.i}\} \rangle \eta \overline{\cup \{\epsilon_1^{v.i}\}}} \omega$	$v \prec \epsilon$
	Dilute	$(\alpha \overline{\xi \langle \mu, \rho \cup \{v\} \rangle \eta \overline{\cup \{v\}}}) [\uparrow \epsilon]$	$\xrightarrow{\text{Di}}$	$\alpha \overline{\xi \langle \mu, \rho \cup \{v, \epsilon\} \rangle \eta \overline{\cup \{v, \epsilon\}}} \omega$	$v \not\prec \epsilon$

initial views, and the least upper bound of the final views. To define these bounds, denote the set of views pointing downward into a well-formed memory μ by $- \hookrightarrow \mu := \{\kappa \in \text{View} \mid \kappa \hookrightarrow \mu\}$. This set is finite since Loc and μ are finite, and each κ mentions only timestamps that appear in μ ; and it has a minimum: the view that points to all the initial messages $\lambda \ell$. $\min \mu_\ell$. t . Consider a non-empty $U \subseteq - \hookrightarrow \mu$. Since $- \hookrightarrow \mu$ is finite and closed under \sqcup , the set U has a least upper bound $\sup_\mu U := \sqcup U$. It also has a greatest lower bound $\inf_\mu U := \sqcap \{\kappa \in \text{View} \mid \sqcap U \geq \kappa \hookrightarrow \mu\}$, noting $\sqcap U$ might not point downward into μ .

Example 6.2. For μ the memory from [Example 5.10](#), $\alpha_1 := \langle \langle x@5; y@7 \rangle \rangle$ and $\alpha_2 := \langle \langle x@7; y@5 \rangle \rangle$, we have $\alpha_1 \hookrightarrow \mu$ and $\alpha_2 \hookrightarrow \mu$, but $\alpha_1 \sqcap \alpha_2 = \langle \langle x@5; y@5 \rangle \rangle \not\hookrightarrow \mu$. Here, $\inf_\mu \{\alpha_1, \alpha_2\} = \langle \langle x@0; y@0 \rangle \rangle$.

Denoting by $\xi_1 \parallel \xi_2$ the set of all the interleavings of ξ_1 and ξ_2 that form chronicles, we define:

$$P_1 \parallel_{X_1, X_2}^{\mathcal{T}} P_2 := \left\{ \inf_{\xi.o} \{\alpha_1, \alpha_2\} \overline{\xi} \sup_{\xi.c} \{\omega_1, \omega_2\} \cdot \langle r_1, r_2 \rangle \in \text{Trace}(X_1 \times X_2) \right\}^* \\ \left\{ \mid \xi \in (\xi_1 \parallel \xi_2) \wedge \forall i \in \{1, 2\}. \alpha_i \overline{\xi_i} \omega_i \cdot r_i \in P_i \right\}$$

Memory access. Mirroring the operational semantics, we interpret:

$$\llbracket \text{store}_{\ell, v} \rrbracket_{\mathcal{T}} := \left\{ \kappa \overline{\langle \mu, \mu \cup \{\ell: v @ (q, t) \langle \kappa[\ell \mapsto t] \rangle \rangle \rangle} \kappa[\ell \mapsto t] \cdot \langle \rangle \in \text{Trace1} \right\}^* \\ \llbracket \text{rmw}_{\ell, \Phi} \rrbracket_{\mathcal{T}} := \llbracket \text{rmw}_{\ell, \Phi}^{\text{RO}} \rrbracket_{\mathcal{T}} \cup \llbracket \text{rmw}_{\ell, \Phi}^{\text{RMW}} \rrbracket_{\mathcal{T}} \text{ where:} \\ \llbracket \text{rmw}_{\ell, \Phi}^{\text{RO}} \rrbracket_{\mathcal{T}} := \left\{ \kappa \overline{\langle \mu, \mu \rangle} \kappa \cdot v.v1 \in \text{TraceVal} \mid \Phi(v.v1) = \perp \wedge \kappa \mapsto v \in \mu_\ell \right\}^* \\ \llbracket \text{rmw}_{\ell, \Phi}^{\text{RMW}} \rrbracket_{\mathcal{T}} := \left\{ \kappa \overline{\langle \mu, \mu \cup \{\epsilon\} \rangle} \kappa[\ell \mapsto t] \cdot v.v1 \in \text{TraceVal} \right. \\ \left. \mid \epsilon = \ell: \Phi(v.v1) @ (v.t, t) \langle \kappa[\ell \mapsto t] \rangle, \kappa \mapsto v \in \mu_\ell \right\}^*$$

Requiring the resulting pre-traces to form traces ensures the constraints on their timestamps and segment hold. Assignment adds a new message. The RMW interpretation loads a message, and adds a new message depending on the modifier's result.

Remark. *Loading is restricted to messages to which the initial view already points. This restriction will make the denotations from §6.3 more convenient to use, but makes no difference in the presence of the rewind rewrite rule from §6.4.*

Monotonicity. To accommodate reasoning about refinement, we require that the trace monad operators be monotonic with respect to set inclusion:

PROPOSITION 6.3. *Let $P_i, Q_i \in \underline{\mathcal{G}}X_i$ and $f, g : X_1 \rightarrow \underline{\mathcal{G}}X_2$. If $P_i \subseteq Q_i$ and $\forall r \in X_1. fr \subseteq gr$ then:*

$$P_1 \gg f \subseteq Q_1 \gg g \quad P_1 \parallel P_2 \subseteq Q_1 \parallel Q_2$$

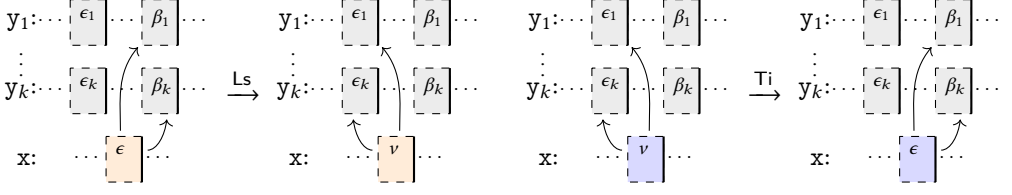


Fig. 10. Schematic depictions of the loosen (left) and tighten (right) rewrite rules, focusing on some particular memory snapshot within the trace. For every i , β_i and ϵ_i may dovetail, coincide, or be separated. **Left:** The environment message ϵ is “loosened” to ν . **Right:** The local message ν is “tightened” to ϵ .

PROOF. The $(-)^*$ operator is monotonic by virtue of being a closure operator. Thus, it is sufficient to show the containment for the operators as defined before taking the \star -closure, which follows straightforwardly from the set-definitions, where traces are obtained from traces in the operands. \square

6.3 Generating Denotations

In the degenerate case of the \emptyset -monad structure, which we call the *null model* and denote by \mathcal{N} , neither identity axiom hold, as evidenced by $\text{return } r \Vdash \text{return } r$, where only on the left the traces have two transitions. As merely a monad structure, the induced denotational semantics is insufficiently abstract. For example, this inequation implies $\llbracket \langle \rangle ; \langle \rangle \rrbracket^c \neq \llbracket \langle \rangle \rrbracket^c$ – this model fails to satisfy even the most basic semantic equivalences. Still, we will find that we can use less abstract models as stepping stones to more abstract ones.

We identify a set of rewrite rules $\mathfrak{g} := \{\text{Ls}, \text{Ex}, \text{Cn}\}$ under which the operations of \mathcal{N} are closed. That is, return is pointwise closed under \mathfrak{g} ; if f is pointwise \mathfrak{g} -closed, then $\Vdash f$ is \mathfrak{g} -closed; and similarly for the effect operations. We explain how the \mathfrak{g} -rewrite maintain this proposition as we present them. For now, let the *generating model* be the \mathfrak{g} -monad structure, which we denote by \mathcal{G} . So we have:

PROPOSITION 6.4. For all $P_i \in \underline{\mathcal{G}}X_i$ and $f : X_1 \rightarrow \underline{\mathcal{G}}X_2$:

$$P_1 \Vdash^{\mathcal{N}} f = P_1 \Vdash^{\mathcal{G}} f \quad P_1 \lll^{\mathcal{N}} P_2 = P_1 \lll^{\mathcal{G}} P_2$$

Moreover, $\text{return}^{\mathcal{N}} = \text{return}^{\mathcal{G}}$, $\llbracket \text{store}_{\ell, \nu} \rrbracket_{\mathcal{N}} = \llbracket \text{store}_{\ell, \nu} \rrbracket_{\mathcal{G}}$, and $\llbracket \text{rmw}_{\ell, \Phi} \rrbracket_{\mathcal{N}} = \llbracket \text{rmw}_{\ell, \Phi} \rrbracket_{\mathcal{G}}$.

This means that we can calculate in \mathcal{G} quite concretely; we need not worry about traces that are obtained from the set-definitions after some arbitrarily long chain of rewrites. So the connections we establish later between \mathcal{G} and the more abstract monad structures (§7.1) become easier to use.

Remark. The difference between denotations in \mathcal{G} and in \mathcal{N} lies in the higher-order fragment. For example, return values of traces in $\llbracket \lambda f : 1 \rightarrow 1. f \langle \rangle \rrbracket_{\mathcal{T}}^c$ are functions that take as argument elements in $\llbracket 1 \rrbracket \rightarrow \underline{\mathcal{T}} \llbracket 1 \rrbracket$. In particular, the denotation depends on $\underline{\mathcal{T}}$.

In presenting the \mathfrak{g} -rewrite rules below, we provide operational intuition by drawing explicit connections with interrupted executions. However, this intuition should be taken with a grain of salt: the abstract model (§6.5) uses these rules as well, where traces do not correspond to interrupted executions as they do here.

Loosen. When a program relies on a message from the environment, it relies on the message’s view being small enough, to not obstruct the behavior that follows. In addition, it relies on the message’s timestamp, which is part of the view, to be big enough for it not to be obscured when needed. The rule is depicted on the left of Figure 10.

Define the loosen (Ls) rewrite rule:

$$\text{Assuming } v \leq_{vw} \epsilon, \alpha \left[\xi(\eta \bar{\cup} \{\epsilon\}) \right] \omega \xrightarrow{\text{Ls}} \alpha \left[\xi(\eta \bar{\cup} \{v\}) \right] \omega \quad (\text{Loosen})$$

Here, we use the partial order on messages $v \leq_{vw} \epsilon$ defined by requiring that they may only differ in their timestamps for other memory locations for which v 's must precede ϵ 's: $v.lc = \epsilon.lc$, $v.vl = \epsilon.vl$, $v.seg = \epsilon.seg$, and $v.vw \leq \epsilon.vw$. If the source in (Loosen) is a trace, then the target is a trace iff either η is empty or $v \hookrightarrow (\eta \bar{\cup} \{v\}).o$.

Intuitively, the source behavior can only use the view in ϵ by incorporating it into its view and the view of its local messages using the max (\sqcup) operation on views. Since allowing threads to silently increase their own view does not change the observed behavior, we would still be able to guarantee the same local messages if the environment message has a smaller view. To make this intuition more precise, we outline a simulation argument in the case the program exhibits the source behavior through an interrupted execution that matches the trace exactly. We do not bother with a formal proof, since the abstract model §6.5 violates this simplifying assumption anyway.

Given an interrupted execution, we can replace an environment message ϵ with a message $v \leq_{vw} \epsilon$ and obtain an interrupted execution of the same program. Whenever a thread with view α loads ϵ via the READONLY step in the original interrupted execution, its view becomes $\omega := \alpha \sqcup \epsilon.vw$. In the new interrupted execution, we instead use the ADV rule to compensate for the earlier view in v , once for every other location ℓ , and forward the view to the message at location ℓ with timestamp ω_ℓ . Then we are able to load ϵ via READONLY, since the message has the same timestamp and the thread's view at the location $\epsilon.lc = v.lc$ hasn't changed during the ADV steps. The RMW-modifier still fails in the new execution because v and ϵ hold the same value and the decision whether to modify it depends only on the value and the parameters, not the view. Loading via the RMW rule is similar, where the modifier still succeeds with the same modification. We choose the same timestamp for the new message we dovetail to v , and it inherits the current view: ω . Steps via other rules remain the same.

The \mathcal{N} operations are {Ls}-closed since the inclusion of a trace never relies on the view of an environment message other than its value, segment, and it being dominated by another view: $\epsilon.vw \leq \kappa$. Since $v \leq_{vw} \epsilon$, the value and segment agree and $v.vw \leq \epsilon.vw \leq \kappa$, and so the target trace will appear in the result of the operation.

Expel. The rewrite expel (Ex) replaces an environment message with two dovetailing messages that occupy the same segment and have the same view, the latter message also having the same value, as depicted on the left of Figure 11. This ensures that the value is available at the same timestamp with the same carried view, and that no more of the timeline is occupied. Formally:

$$\text{Assuming } v \prec \epsilon, \alpha \left[\xi(\eta \bar{\cup} \{\epsilon [i \mapsto v.i]\}) \right] \omega \xrightarrow{\text{Ex}} \alpha \left[\xi(\eta \bar{\cup} \{v, \epsilon\}) \right] \omega \quad (\text{Expel})$$

Here, $v \prec \epsilon$ is the *monotone dovetailing* relation, i.e., the two messages dovetail: $v.lc = \epsilon.lc$, $v.t = \epsilon.i$; and moreover their views compare: $v.vw \leq \epsilon.vw$. The final condition relaxes the rule as depicted in Figure 11 where the $v.vw = \epsilon.vw$ was required. This makes no difference to the model, because the relaxed version is obtained by applying loosen after the strict version, to obtain the required target.

As was the case for loosen, if the source in (Expel) is a trace, then the target is a trace iff either η is empty or $v \hookrightarrow (\eta \bar{\cup} \{v, \epsilon\}).o$.

To justify the rule for interrupted executions, suppose ϵ' is an environment message in an interrupted execution. By replacing ϵ' with v and ϵ , we obtain another interrupted execution, in which the environment added these two messages. Throughout the interrupted execution, no view ever points to v , as if v was not there.



Fig. 11. Schematic depictions of the expel (left) and absorb (right) rewrite rules, that focus on the segment of the dovetailed messages together with all pointers into and out of them, within a particular memory snapshot. The *circular* cloud represents the subset of the memory that the messages in focus are pointing to, showing their views are the same. The *elliptical* cloud represents views—possibly including the initial and final view, as well as other messages—that point to each of the dovetailing messages. Thus, no view may point to v . A condition that is not depicted is that all the messages must appear in the same places in the chronicle. **Left:** The environment message v is “expelled” from the message ϵ' , which becomes ϵ . **Right:** The local message v is “absorbed” into the message ϵ , which becomes ϵ' .



Fig. 12. Schematic depictions of the condense (left) and dilute (right) rewrite rules, in the style of Figure 11. A condition that is not depicted is that v and v' must appear in the same places in the chronicle, and ϵ may not appear before them. The views that point to v' in the source can point either to v or to ϵ in the target. **Left:** The message v turns into v' by “condensing” the environment message ϵ . **Right:** The message v' turns into v by “diluting” out the local message ϵ .

The operations of \mathcal{N} are $\{\text{Ex}\}$ -closed since they never rely on the absence of messages, only for the availability of segments, which is preserved by this rule.

Condense. In the condense (Cn) rewrite rule, the source behavior may include an environment message ϵ dovetailing some prior message v that carries the same value and view. The target behavior removes ϵ , and modifies v to a message v' that occupies the same segment as the two messages combined, as depicted on the left of Figure 12.

To formally capture how the views in the trace change in this rule, we define *pulling* a view κ along a message ϵ to be the view in which, if the timestamp at $\epsilon.\text{lc}$ is the initial timestamp of ϵ , then we update the timestamp to be the final timestamp (depicted on the right):

$$\kappa[\uparrow\epsilon] := \begin{cases} \kappa_{\epsilon.\text{lc}} = \epsilon.\text{i} : \kappa[\epsilon.\text{lc} \mapsto \epsilon.\text{t}] & \kappa_{\epsilon.\text{lc}} \quad \kappa[\uparrow\epsilon]_{\epsilon.\text{lc}} \\ \text{otherwise: } \kappa & \downarrow \quad \downarrow \\ & (\epsilon.\text{i}, \epsilon.\text{t}) \end{cases}$$

We extend the pulling operation to messages, memories, chronicles, (pre-)traces, and view trees, by pulling the view associated with these objects. In particular, pulling a dovetailing message preceding ϵ along ϵ merges them into one contiguous message.

The rewrite rule, formally:

$$\text{Assuming } v \dashv\!\in \epsilon, \alpha \left[\xi(\eta \bar{\cup} \{v, \epsilon\}) \right] \omega \xrightarrow{\text{Cn}} \left(\alpha \left[\xi(\eta \bar{\cup} \{v\}) \right] \omega \right) [\uparrow\epsilon] \quad (\text{Condense})$$

Here we use the monotone *repetitive* dovetailing relation $v \dashv\!\in \epsilon$ where the two messages dovetail monotonically: $v \subset \epsilon$, and have the same value: $v.v1 = \epsilon.v1$. As was the case for expel, relaxing the condition that the views must be equal as depicted more strictly in Figure 12 is admissible, this time by applying loosen before the strict version, to obtain the required source.

The decomposition of the chronicle in the rule determines where ϵ first appears, but v can first appear earlier. This situation is depicted in Figure 13.

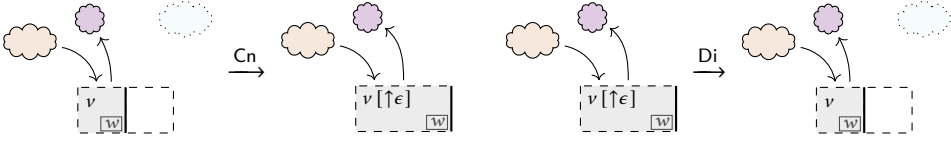


Fig. 13. Schematic depictions of the condense (left) and dilute (right) rewrite rules as in Figure 12, focusing this time on a memory without ϵ . **Left:** Since ϵ is to appear as an environment message in the chronicle, it can appear since the opening memory, not appear even in the closing memory, or somewhere in between. **Right:** Since ϵ is to appear as a local message, it cannot appear in the opening memory, and must appear in the closing memory.

Unlike *loosen* and *expel*, when η is empty the target may differ from the source even though ϵ , nor any other message, is removed. This is due to condense pulling views along ϵ , whether ϵ is there or not. So when η is empty the target differs from the source iff there is a message at $\epsilon.i = v.t$. In this case, assuming the source is a trace, for the target to be a trace $\epsilon.\text{seg}$ must be available, otherwise there will be a memory that is not scattered. If $\epsilon.\text{seg}$ is available, then the target will be a trace, because pulling along a free segment retains the well-formed memory properties. For example, pointing downwards is preserved due to the following lemma:

LEMMA 6.5. $\forall \epsilon \in \text{Msg} \forall \kappa, \sigma \in \text{View}. \kappa_{\epsilon.lc}, \sigma_{\epsilon.lc} \notin \epsilon.\text{seg} \setminus \epsilon.t \implies \kappa \leq \sigma \implies \kappa[\uparrow\epsilon] \leq \sigma[\uparrow\epsilon]$.

To summarize, if the source in (**Condense**) is a trace, then the target is a trace iff either η is non-empty, $v.t \notin \xi.c.t$, or $\epsilon.\text{seg} \cap \bigcup \xi.c.\text{seg} = \emptyset$.

If we have an interrupted execution with two messages v and ϵ as in condense, we will also have an interrupted execution without the environment message ϵ , and with v' instead of v . In the new interrupted execution, v' is used whenever either v or message ϵ were used in the original.

The operations of \mathcal{N} are $\{\text{Cn}\}$ -closed. This is harder to demonstrate compared to the previous rules. Considerations involving the value available to load, and the segment available to store, are similar. If a message dovetailed with ϵ in the source, it dovetails with v' in the target. Thus, if a message was added due to an RMW in the source, the condition to dovetail with a message that holds the loaded value is still met in the target. There are also new considerations involving the rewrite affecting the entire trace rather than just one or two messages. For instance, to show that $(\gg)=$ preserves the rule, we replace an application of condense after binding the traces with applications of condense (with the same messages) on each of the traces before binding. This is subtle because the delimiting views change, and thus the condition imposed on binding the traces changes from $\kappa \leq \sigma$ to $\kappa[\uparrow\epsilon] \leq \sigma[\uparrow\epsilon]$. The condition still holds due to Lemma 6.5 since neither κ nor σ point into the *interior* of $\epsilon.\text{seg}$, because no message has a timestamp there. This insight resolves similar subtleties for the other \mathcal{N} -constructs.

6.4 Concrete Denotations

Brookes [13] pioneered two rewrite rules to make denotations abstract and support desired program transformations: stuttering and mumbling. To define our next model, we adapt these to our setting, as well as add two additional ones: $c := \{\text{St}, \text{Mu}, \text{Fw}, \text{Rw}\}$. We combine notations of rewrite-rule sets, e.g. $gc := g \cup c$. Thus, we denote by \mathcal{M} the gc -monad structure. We call this model the *concrete model* because, like the generating model, it still maintains a close correspondence to the operational semantics RA_{\leq} . However, \mathcal{M} is a monad, a crucial element in the proof of the adequacy theorem (Appendix D).

PROPOSITION 6.6. \mathcal{M} is a monad.

Stutter. A program can always make the same memory guarantees on which it relies. This is captured stutter (St), which inserts a transition with equal components somewhere:

$$\alpha \boxed{\xi \eta} \omega \xrightarrow{\text{St}} \alpha \boxed{\xi \langle \mu, \mu \rangle \eta} \omega \quad (\text{Stutter})$$

Note that for the target in (Stutter) to form a trace, provided that its source is a trace, we need to further require that μ is a well-formed memory, and that α points to μ (which may not be the case if ξ is empty).

We can also understand stutter using interrupted executions. Given an interrupted execution, a sequence of 0 steps $\langle T, \mu \rangle, M \rightsquigarrow^* \langle T, \mu \rangle, M$ can be inserted anywhere as long as $\langle T, \mu \rangle$ is well-formed and μ contains previous, and is contained in subsequent, memories. This insertion does not change the initial or the final configurations of the interrupted execution.

As a concrete (contrived) example, stutter is used for validating the transformation $\langle \rangle ; \langle \rangle \rightarrow \langle \rangle ; \langle \rangle ; \langle \rangle$. Indeed, though $\llbracket \langle \rangle ; \langle \rangle \rrbracket_{\mathcal{G}}^c \not\subseteq \llbracket \langle \rangle ; \langle \rangle ; \langle \rangle \rrbracket_{\mathcal{G}}^c$, we do have $\llbracket \langle \rangle ; \langle \rangle \rrbracket_{\mathcal{G}}^c \stackrel{\text{St}}{\supseteq} \llbracket \langle \rangle ; \langle \rangle ; \langle \rangle \rrbracket_{\mathcal{G}}^c$.

Mumble. A program can omit a guarantee and rely on that guarantee internally. This is captured by mumble (Mu), which combines transitions with the same memory at their common edge:

$$\alpha \boxed{\xi \langle \mu, \rho \rangle \langle \rho, \theta \rangle \eta} \omega \xrightarrow{\text{Mu}} \alpha \boxed{\xi \langle \mu, \theta \rangle \eta} \omega \quad (\text{Mumble})$$

If the source in (Mumble) is a trace then so is its target.

We can also understand mumble using interrupted executions. If we have an interrupted execution of the form $\dots \langle T, \mu \rangle, M \rightsquigarrow_{\text{RA} \leq}^* \langle R, \rho \rangle, N \rightsquigarrow_{\text{RA} \leq}^* \langle H, \theta \rangle, K \dots$ that is compatible with the source trace, then we clearly have a shorter interrupted execution $\dots \langle T, \mu \rangle, M \rightsquigarrow_{\text{RA} \leq}^* \langle H, \theta \rangle, K \dots$ that is compatible with the target trace.

As a concrete example, mumble is used for validating the transformation $\ell?; M \rightarrow M$, which also demonstrates the importance of the internalized operational invariants, i.e. the use of traces rather than pre-traces. Indeed, $\alpha \boxed{\langle \mu, \rho \rangle \xi} \omega \cdot r \in \llbracket M \rrbracket_{\mathcal{G}}^c$ is a trace, so $\alpha \rightsquigarrow \mu$. Therefore, there is some $\alpha \boxed{\langle \mu, \mu \rangle} \alpha \cdot v \in \llbracket \ell? \rrbracket_{\mathcal{G}}^c$. So $\alpha \boxed{\langle \mu, \mu \rangle \langle \mu, \rho \rangle \xi} \omega \cdot r \in \llbracket \ell? ; M \rrbracket_{\mathcal{G}}^c$. Thus $\alpha \boxed{\langle \mu, \rho \rangle \xi} \omega \cdot r \in \llbracket \ell? ; M \rrbracket_{\mathcal{G}}^c \stackrel{\text{Mu}}{\supseteq} \llbracket M \rrbracket_{\mathcal{G}}^c$.

Forward. If a program fragment can operate and guarantee a certain set of messages remain visible, it can operate in the same way and guarantee a subset of these messages remain visible. The final view serves to guarantee revealed messages to subsequent computation, so we reflect this fact by forward (Fw), which increases the final view:

$$\text{Assuming } \kappa \leq \omega, \alpha \boxed{\xi} \kappa \xrightarrow{\text{Fw}} \alpha \boxed{\xi} \omega \quad (\text{Forward})$$

The rule is also depicted in Figure 14. Note that for the target in (Forward) to form a trace (rather than a pre-trace), provided that its source is a trace, we need to further require that $\omega \hookrightarrow \xi.c$.

We can also understand forward using interrupted executions. If we have an interrupted execution of the form $\dots \langle T, \mu \rangle, M \rightsquigarrow_{\text{RA} \leq}^* \langle R, \rho \rangle, N$, we can append Adv steps to the final sequence of steps to obtain $\dots \langle T, \mu \rangle, M \rightsquigarrow_{\text{RA} \leq}^* \langle R', \rho \rangle, N$, where $R \leq R' \hookrightarrow \rho$.

As a concrete example, stutter and forward are used in validating the transformation $\langle \rangle \rightarrow \ell?; \langle \rangle$. We can use stutter to compensate for the additional transition. However, this is insufficient on its own, because not only is there an extra transition, the initial and final views from $\llbracket \ell? ; \langle \rangle \rrbracket_{\mathcal{G}}^c$ may be different. To compensate for that we use forward.

Rewind. If a program fragment can operate by relying on a certain set of visible messages, it can operate in the same way by relying on a superset of these messages being visible. The initial view serves to guarantee revealed messages from previous computation, so we reflect this fact by rewind (Rw), which decreases the initial view:

$$\text{Assuming } \alpha \leq \kappa, \kappa \boxed{\xi} \omega \xrightarrow{\text{Rw}} \alpha \boxed{\xi} \omega \quad (\text{Rewind})$$

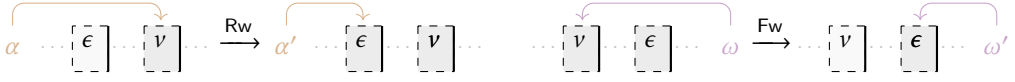


Fig. 14. Schematic depictions of the rewind and forward rewrite rule, focusing on a single location, where the initial/final view points to v before and points to ϵ after. The messages v and ϵ may coincide, dovetail, or be separated. **Left:** The initial view α is “rewound” to α' . **Right:** The final view ω is “forwarded” to ω' .

The rule is also depicted in Figure 14. Note that for the target in (Rewind) to form a trace (rather than a pre-trace), provided that its source is a trace, we need to further require that $\alpha \hookrightarrow \xi.o$.

We can also understand rewind using interrupted executions, as we did for forward. Instead of appending ADV steps to the final sequence, we *prepend* ADV steps to the *initial* sequence.

As a concrete example, rewind and stutter are used in validating the transformation $M \rightarrow \langle \rangle ; M$.

6.5 Abstract Denotations

Finally, we define the *abstract model*, \mathcal{A} as the gca -monad structure, where $\mathbf{a} := \{\text{Ti}, \text{Ab}, \text{Di}\}$ are rewrite rules, presented below. This model fulfills the basic requirement of a monadic model:

PROPOSITION 6.7. \mathcal{A} is a monad.

By including the additional rewrite rules of \mathbf{a} we give up the strictly operational interpretation that we have assumed when presenting the previous rules. This allows us to obtain the abstraction that the concrete model lacks. We took a parsimonious approach, only proposing rules that we need to justify program transformations that the RA model is expected to validate. With each rewrite rule, we present a program transformations whose validation uses that particular rule, though other gc -rewrites are often required as well.

Tighten. The role of the view that a message carries, other than providing the timestamp, is to constrain the loading thread by increasing its view when it loads the message. Considering a local message v , its view serves to guarantee that loading it would not obscure any message within a certain portion of the memory. Therefore, replacing v by ϵ that only differs in its view, where $v \leq_{\text{vw}} \epsilon$, as depicted on the right of Figure 10, means that only a sub-portion of the memory is guaranteed not to become obscured by loading the message, and keeps everything else the same. This is the effect of the tighten (Ti) rewrite rule. Formally:

Assuming $v \leq_{\text{vw}} \epsilon$, $\alpha \left[\xi \langle \mu, \rho \uplus \{v\} \rangle \eta \bar{\uplus} \{v\} \right] \omega \xrightarrow{\text{Ti}} \alpha \left[\xi \langle \mu, \rho \uplus \{\epsilon\} \rangle \eta \bar{\uplus} \{\epsilon\} \right] \omega$ (Tighten)

See Figure 15 for a concrete example.

As a concrete benefit of tighten, consider the RA-valid (but SC-invalid) write-read-reordering transformation $\ell := v ; \text{let } a = \ell' ? \text{in } a \rightarrow \text{let } a = \ell' ? \text{in } \ell := v ; a$, where $\ell \neq \ell'$. On the right, the added message carries the view of the thread after it is increased by the view of the loaded message, but, on the left, the added message carries the initial view of the thread. By applying tighten to traces of the left, we compensate for this difference.

Absorb. Applying absorb (Ab) removes a local message v and decreases the initial timestamp of a dovetailing local message ϵ with the same view, such that the resulting ϵ' covers the segment of v . This is depicted on the right of Figure 11. In this way, the rule weakens its memory guarantee to the environment because it has less messages available to load from, without strengthening the guarantee by way of making any more of the location’s timeline available. No view can point to v before applying this rule, otherwise the resulting pre-trace would not be a trace. The rule is formally specified as follows, where we abbreviate by denoting $\epsilon_i^t := \epsilon [i \mapsto t]$:

Assuming $v \subset \epsilon$, $\alpha \left[\xi \langle \mu, \rho \uplus \{v, \epsilon\} \rangle \eta \bar{\uplus} \{v, \epsilon\} \right] \omega \xrightarrow{\text{Ab}} \alpha \left[\xi \langle \mu, \rho \uplus \{\epsilon_i^{v.i}\} \rangle \eta \bar{\uplus} \{\epsilon_i^{v.i}\} \right] \omega$ (Absorb)

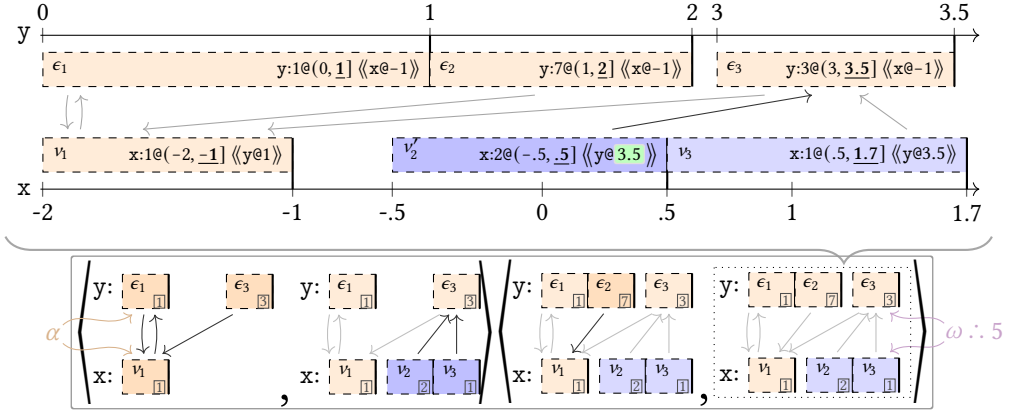


Fig. 15. A possible result from rewriting the trace from Figure 1 using tighten. Since v_2 is local in the trace from Figure 1, tighten can advance its view to point to ϵ_3 instead of ϵ_1 . The same replacement is applied throughout the trace's sequence, not just the closing memory.

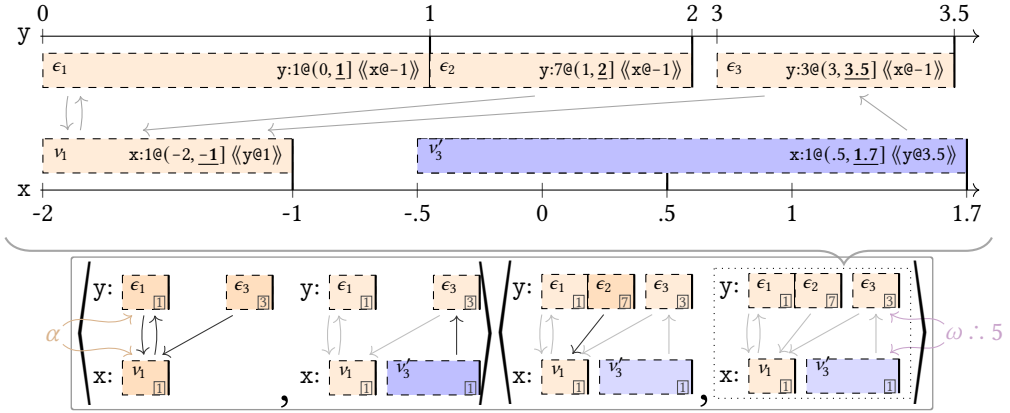


Fig. 16. A possible result from rewriting of the trace from Figure 15 using absorb. The dovetailed messages v_2 and v_3 are local in the trace from Figure 1, added within the same transition, so by rewriting by absorb they can be replaced by v'_3 obtained by stretching v_3 's segment to cover v_2 's segment.

See Figure 16 for a concrete example.

As in expel, we relax the condition of equal views, admissible due to tighten.

The transformation $\ell := w ; \ell := v \rightarrow \ell := v$ is a concrete example where this rule is useful, in which we use absorb to compensate for the extra message. Specifically, if the local message on the right is β , we pick some t from the interior of $\beta.\text{seg}$, a trace with a local message due to $\ell := w$ that has the segment $(\beta.i, t]$ and a trace with a local message due to $\ell := v$ that has the segment $(t, \beta.\tau]$. After binding, we use mumble to combine the transitions, then absorb to replace these two messages with β .

Dilute. Formally, the dilute (Di) rule is specified as follows:

$$\text{Assuming } v \in \epsilon, \left(\alpha \left[\xi \langle \mu, \rho \uplus \{v\} \rangle \eta \overline{\uplus} \{v\} \right] \omega \right) [\uparrow \epsilon] \xrightarrow{\text{Di}} \alpha \left[\xi \langle \mu, \rho \uplus \{v, \epsilon\} \rangle \eta \overline{\uplus} \{v, \epsilon\} \right] \omega \quad (\text{Dilute})$$

See Figure 17 for a concrete example.

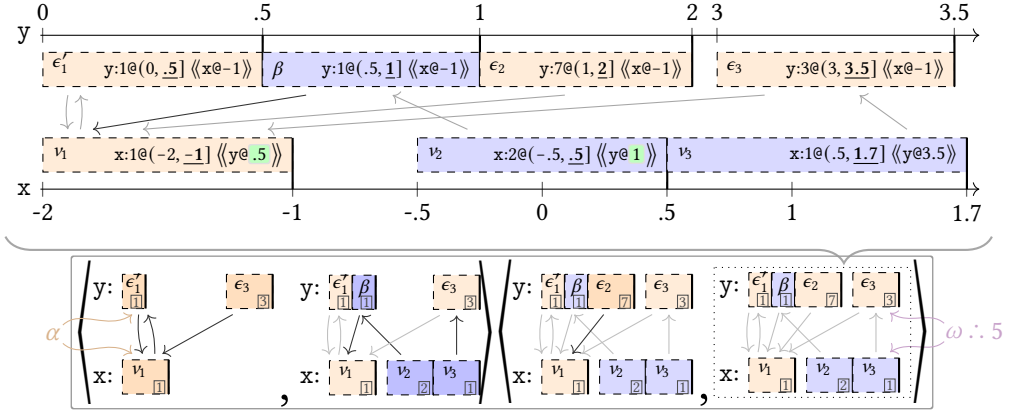


Fig. 17. A possible result from rewriting of the trace from Figure 1 using dilute. The message ϵ_1 from Figure 1 was replaced with ϵ'_1 , with the same value 1. The local message β —which takes up the rest of the missing space left behind by ϵ_1 —always appears with ϵ'_1 , dovetailing with it and carrying the same value. The message ϵ_2 , that used to dovetail with ϵ_1 , now dovetails with β .

We restrict to the case that $v.vw = \epsilon.vw$ when explaining the rule. The rest can be seen as a formal extension which is admissible in the presence of tighten, as with condense and loosen.

Unpacking this definition, we first note that, although we are focusing on the case where the source and target are traces, the pre-trace expression $\tau' := \alpha \left[\xi \langle \mu, \rho \uplus \{v\} \rangle \eta \bar{\uplus} \{v\} \right] \omega$ within the source may not be a trace itself. In particular, there could be views in τ' that point to ϵ even though there is no message there until the pulling ($-$) $[\uparrow\epsilon]$ takes effect, after which they will point to v $[\uparrow\epsilon]$. There could also be views that point to v in τ' , which too point to v $[\uparrow\epsilon]$ in the source. Therefore, views that point to v $[\uparrow\epsilon]$ in the source could point to either v or ϵ in the target—the latter being a pointer moving. That is, in terms of the memory graph's structure, we think of v $[\uparrow\epsilon]$ and v being the same vertex labeled differently in the memories before and after the rewrite respectively, with some pointers moved to the newly added ϵ -labeled vertex. This is depicted on the right of Figure 12.

Another tricky thing about this rule is that v could appear first before ϵ , and could be either a local message or an environment message. This is depicted on the right of Figure 13.

Justifying this rewrite, if the program relies on $v.v1$ being available at v $[\uparrow\epsilon].t$, it can instead rely on it being available with $v.t$ with a view that will impose the same restrictions on the program once it loads the message and inherits the view; all this, so long as the remainder of the segment remains unoccupied until it guarantees the same value and the same view there (with ϵ). Similarly, if the program guarantees the value with v $[\uparrow\epsilon]$, it can guarantee it with v instead, so long as only ϵ can occupy the remaining segment.

As a concrete example of this rule in use, consider the transformation $\ell? \rightarrow \text{FAA}(\ell, 0)$. A trace from the target has a message ϵ added to dovetail with an existing message v . So there is a matching trace in the source without that added message. By closure under g , we can Cn-rewrite the trace, pulling v by ϵ (the Cn-rewrite is defined even when pulling by a message that is not there). Then, we can apply dilute to add ϵ .

7 Metatheory

The difference between the different monads from §6 are due to the abstraction afforded to them by the rewrite rules under which they are closed. Ultimately, it is the monad \mathcal{A} that we are interested in, as it is the one over which we define satisfactory denotational semantics. To prove the

results that justify this, we first relate the different monads using properties of the rewrites rules and their interactions (§7.1). Then, focusing on \mathcal{A} , we prove (directional) compositionality (§7.2) and soundness (§7.3). These results serve as stepping stones towards the main result: (directional) adequacy (§7.4). Finally, we exhibit the sufficient abstraction of the denotational semantics with various transformations it supports (§7.5).

7.1 Commutativity of Rewrites

A complicating aspect of these trace models is how intricately rewrites between traces interact. For example, an application of forward may only be possible after adding a transition to the end of the chronicle with stutter, in which the messages that the final view is intended to point to exist. So given a rewrite $\tau \xrightarrow{\text{St}} \pi \xrightarrow{\text{Fw}} \varrho$, it may not be possible to find any π' such that $\tau \xrightarrow{\text{Fw}} \pi' \xrightarrow{\text{St}} \varrho$.

At other times, commuting in this way is guaranteed to be possible. As a relatively simple example, an application of loosen can always be made before one of stutter rather than after it. Even if the message that loosen acts on happens to appear in the transition that stutter adds to the chronicle in the sequence $\tau \xrightarrow{\text{St}} \pi \xrightarrow{\text{Ls}} \varrho$, in the alternative sequence $\tau \xrightarrow{\text{Ls}} \pi' \xrightarrow{\text{St}} \varrho$ the transition added already includes the “loosened” message. It is important to check that the pre-trace π' is in fact a trace. Since τ is a trace, then—other than the trivial case in which π' is τ itself—we only need to check that the “loosened” message points downwards into the memories in which it appears. This we infer from the fact that every memory in π' appears in ϱ , which is itself a trace.

More generally, every sequence of rewrites can be rearranged such that \mathfrak{g} -rewrites appear first, then \mathfrak{c} -rewrites, and finally \mathfrak{a} -rewrites. This property will play a pivotal rule in the metatheory, and it is an immediate consequence of the following lemma. We write $x \sqsubseteq y$ when $\overset{x}{\rightarrow} \overset{y}{\rightarrow} \subseteq \overset{y}{\rightarrow} \overset{x}{\rightarrow}$, where $\overset{x}{\rightarrow}$ and $\overset{y}{\rightarrow}$ are restricted to traces.

LEMMA 7.1 (REWRITE COMMUTATIVITY). *If $x \in \mathfrak{a}$ and $y \in \mathfrak{gc}$, or $x \in \mathfrak{ca}$ and $y \in \mathfrak{g}$, then $x \sqsubseteq y$.*

PROOF. The proof proceeds by case analysis on x and y , each encapsulated in diagram(s) such as the two in Figure 18. The detailed proof, including all of the diagrams, is in Appendix F. Specifically, see diagrams 31 and 42 for larger and more detailed versions of those in Figure 18.

Each diagram shows the assumed rewrite sequence $\tau \xrightarrow{x} \pi \xrightarrow{y} \varrho$ on the left, with the conditions that are known because they were required for the rewrites to be applicable; and the deduced sequence $\tau \xrightarrow{x} \pi' \xrightarrow{y} \varrho$ on the right, with the conditions that need to hold for the rewrites to be applicable. The conditions are enough to show that the rewrite rules apply for pre-traces, but for the sequence to be valid, we must verify that π' is a trace. This is done by inferring from the fact that it was x -rewritten from the trace τ , and y -rewritten to the trace π , using the conditions we have collected as we presented the rewrite rules.

The cases in Figure 18 are among the more interesting cases in which the activities of x and y overlap. The left diagram shows a sub-case of $\text{Ab} \sqsubseteq \text{Cn}$ in which the absorbing message (ϵ) also serves as the condensing message. On the right, a sub-case of $\text{Di} \sqsubseteq \text{Cn}$ in which the diluted message ($\hat{\epsilon}$) is also the message that is being condensed. This case is particularly tricky because the pulls need to be commuted, as in $(-\ [\uparrow\epsilon]) [\uparrow\hat{\epsilon} [\uparrow\epsilon]] = (- [\uparrow\hat{\epsilon}]) [\uparrow\epsilon [\uparrow\hat{\epsilon}]]$. \square

Remark. *When defining the rewrite rules, we could have restricted $v \prec \epsilon$ (and similarly $v \prec \hat{\epsilon}$) to messages with equal views: $v.vw = \epsilon.vw$, resulting in equivalent closures. For example, to apply the restricted version of absorb, one first applies tighten, which is also an \mathfrak{a} -rewrite, to make the views equal. In fact, we used this slightly simpler presentation in the abridged version of this paper [21].*

Using the simpler presentation would require a less tidy statement in Rewrite Commutativity. For example, we would not have $\text{Di} \sqsubseteq \text{Ls}$, because it may be the case that we “dilute” an environment

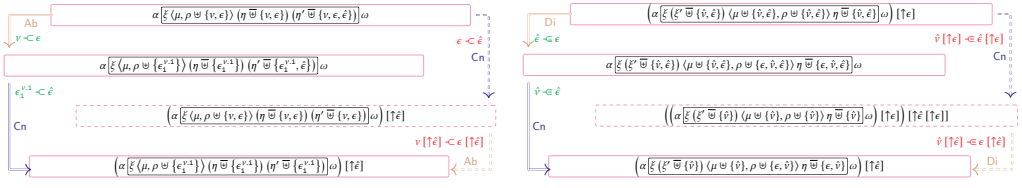


Fig. 18. Two cases from the proof of **Rewrite Commutativity** in which “active” messages overlap.

message and then “loosen” it. After commuting, using the restricted version of dilute, we would need to then “tighten” the new local message to recover the resulting trace from the original rewrite sequence.

As a corollary to **Rewrite Commutativity**, we can commute ca -rewrites out of the \mathcal{G} -operators:

LEMMA 7.2 (DEFERRAL OF CLOSURE). *Let $\star \subseteq \star \subseteq \text{ca}$. For all $P_i \in \underline{\mathcal{G}}X_i$ and $f : X_1 \rightarrow \underline{\mathcal{G}}X_2$:*

$$\left(P_1^\star \gg^{\mathcal{G}} f^\star\right)^\star = \left(P_1 \gg^{\mathcal{G}} f\right)^\star \quad \left(P_1^\star \parallel^{\mathcal{G}} P_2^\star\right)^\star = \left(P_1 \parallel^{\mathcal{G}} P_2\right)^\star$$

PROOF. In the proof we rely on the fact that every closure in \mathfrak{a} is mirrored in \mathfrak{g} . For example, instead of rewriting some trace $\tau \in P_1$ by Ab and then “binding” it with a trace $\pi \in f(\tau.v1)$, we can instead mirror its effect by Ex -rewriting π to make its messages match τ ’s, bind *those* together, and then use Ab after the bind.

The detailed proof is in **Appendix A**. \square

Deferral of Closure also applies to \mathcal{M} and \mathcal{A} instead of \mathcal{G} , since $\underline{\mathcal{G}}X \supseteq \underline{\mathcal{M}}X \supseteq \underline{\mathcal{A}}X$. Since calculations in \mathcal{G} are relatively simple, this lemma is quite convenient to have.

Example 7.3. The associativity laws for \mathcal{M} and \mathcal{A} are implied by the one for \mathcal{G} . To show this for \mathcal{M} , we specialize **Deferral of Closure** to $\star = \text{c}$, and restrict to $P \in \underline{\mathcal{M}}X$, $f : X \rightarrow \underline{\mathcal{M}}Y$, and $g : Y \rightarrow \underline{\mathcal{M}}Z$, obtaining:

$$\begin{aligned} \left(P \gg^{\mathcal{M}} f\right) \gg^{\mathcal{M}} g &= \left(\left(P \gg^{\mathcal{G}} f\right) \gg^{\mathcal{G}} g\right)^{\text{c}} = \left(\left(P \gg^{\mathcal{G}} f\right) \gg^{\mathcal{G}} g\right)^{\text{c}} \\ P \gg^{\mathcal{M}} \left(\lambda r. f(r) \gg^{\mathcal{M}} g\right) &= \left(P \gg^{\mathcal{G}} \left(\lambda r. f(r) \gg^{\mathcal{G}} g\right)\right)^{\text{c}} = \left(P \gg^{\mathcal{G}} \left(\lambda r. f(r) \gg^{\mathcal{G}} g\right)\right)^{\text{c}} \end{aligned}$$

The same can be repeated for \mathcal{A} by specializing to $\star = \text{ca}$.

When calculating denotations of terms, we can use **Deferral of Closure** to similarly delay taking the closure. For programs specifically, we can delay all the way through, only taking the closure at the top level. Relating \mathcal{M} to \mathcal{A} in this way is a key step in our proof of adequacy. Thus, we state:

LEMMA 7.4 (RETROACTIVE CLOSURE). *If M is a program, then $\llbracket M \rrbracket_{\mathcal{A}}^{\text{c}} = \llbracket M \rrbracket_{\mathcal{M}}^{\text{c}}{}^{\text{a}}$.*

The proof is in **Appendix A**.

The main focus in the rest of §7 is the denotational semantics over \mathcal{A} . To emphasize this fact, and to avoid clutter, we henceforth omit \mathcal{A} from its semantic notations. In particular, we write $\llbracket M \rrbracket^{\text{c}}$ rather than $\llbracket M \rrbracket_{\mathcal{A}}^{\text{c}}$.

7.2 Compositionality

To state compositionality, and later adequacy, we need a few technical concepts involving capturing and capture-avoiding substitution in λ_{RA} and its semantics. We extend λ_{RA} with well-typed second-order *metavariables*: these are binding-aware identifiers $\Gamma \vdash \mathbf{M} : A$. Metavariables represent “holes” into which we can slot well-typed terms $\Gamma \vdash M : A$, in an operation called *metavariable*

substitution. When such a metavariable appears in a term, it is accompanied by an explicit value substitution governing which values to substitute when we slot a term into it. Metavariable substitution captures the variables of which the metavariables are aware.

Example 7.5. Consider the following metavariable that is aware of a context with two variables: $a : \text{Loc}, b : \text{Val} \vdash \mathbf{M} : \mathbf{1}$. The term $\vdash \mathbf{M}[a \mapsto x, b \mapsto 42] : \mathbf{1}$ contains this metavariable and no other variables. Metasubstituting the open term $a : \text{Loc}, b : \text{Val} \vdash a := b$ for \mathbf{M} yields $\vdash x := 42 : \mathbf{1}$.

This treatment of metavariables and their substitution is tedious but standard given the binding structure in the syntax. A (*term*) *context* $\Delta \vdash \Xi [\Gamma \vdash - : A] : B$ is a term of type B with variables from Δ and one meta-variable $\Gamma \vdash - : A$ of type A that assumes a binding context Γ . It is a *program context* if Δ is empty and $B = G$ is ground.

The recursive definition of a term's denotation only uses the denotations of its subterms, so the semantics is automatically compositional. Abbreviating $\Gamma \vdash M : A$ and $\Gamma \vdash N : A$ into $\Gamma \vdash M, N : A$:

PROPOSITION 7.6 (COMPOSITIONALITY). *Let $\Delta \vdash \Xi [\Gamma \vdash - : A] : B$ be a term context and assume $\Gamma \vdash M, N : A$. If $\llbracket M \rrbracket^c = \llbracket N \rrbracket^c$ then $\llbracket \Xi [M] \rrbracket^c = \llbracket \Xi [N] \rrbracket^c$.*

However, we are interested in a directional version of this, dealing not only with set equality but also with set inclusion. Simply replacing $=$ with \subseteq in [Proposition 7.6](#) results in a false claim. This is because the language is higher-order, so only a “nested” form of containment holds, which degenerates to containment when restricted programs:

THEOREM 7.7 (DIRECTIONAL COMPOSITIONALITY). *Let $\cdot \vdash \Xi [\Gamma \vdash - : A] : G$ be a program context and assume $\Gamma \vdash M, N : A$. If $\llbracket M \rrbracket^c \subseteq \llbracket N \rrbracket^c$ then $\llbracket \Xi [M] \rrbracket^c \subseteq \llbracket \Xi [N] \rrbracket^c$.*

The proof is in [Appendix B](#).

7.3 Soundness

A basic part of the correspondence between the denotational and the operational semantics is its soundness, in the sense that the denotation of a program has traces corresponding to evaluations. More specifically, program evaluation is reflected in the denotation of the program by a single-transition trace, using the greatest lower bound of the initial view tree as the initial view:

THEOREM 7.8 (SOUNDNESS). *For a program M , if $\langle T, \mu \rangle, M \Downarrow V$, then there exist μ' and ω such that $\inf_{\mu} T \langle \mu, \mu' \rangle \omega \cdot V \in \llbracket M \rrbracket_{\mathcal{M}}^c$*

The proof is in [Appendix C](#). By [Retroactive Closure](#) we can replace $\llbracket M \rrbracket_{\mathcal{M}}^c$ with $\llbracket M \rrbracket^c$ above.

Impossible outcomes. The contrapositive presentation of [Soundness](#) states that certain evaluations of a program can be ruled out by inspecting its denotation. For example, the impossible evaluations of (MP) from [Example 5.2](#) can be shown indirectly by calculating its denotation.

7.4 Adequacy

Adequacy uses contextual refinements to formalize how denotations capture behavior within any context: for $\Gamma \vdash M, N : A$, we say that M *contextually refines* N , denoted $\Gamma \vdash M \sqsubseteq N : A$, or $M \sqsubseteq N$ for short, if $\langle \dot{\alpha}, \mu \rangle, \Xi [M] \Downarrow V \implies \langle \dot{\alpha}, \mu \rangle, \Xi [N] \Downarrow V$ for every program context $\cdot \vdash \Xi [\Gamma \vdash - : A] : G$, initial configuration state $\langle \dot{\alpha}, \mu \rangle$, and value V .

THEOREM 7.9 (DIRECTIONAL ADEQUACY). *If $\llbracket M \rrbracket^c \subseteq \llbracket N \rrbracket^c$ then $M \sqsubseteq N$.*

The proof begins by examining the tight correspondence between traces in denotations over \mathcal{M} and interrupted executions. Formally, we write $M : \tau \cdot V$ when M *executes through* τ to V : there

is an interrupted execution from M to V such that $\tau.v1 = \llbracket V \rrbracket_{\mathcal{M}}^v$, which starts with the view-leaf labeled by $\tau.ivw$, passes exactly through the memory transitions of $\tau.ch$, and ends with the view-leaf labeled by $\tau.fvw$. By the **Fundamental Lemma**, the statement and proof of which we relegate to **Appendix D**, if $\tau \in \llbracket M \rrbracket_{\mathcal{M}}^c$ then there exists an appropriate value V such that $M : \tau : V$.

Traces in denotations over \mathcal{A} do not enjoy this correspondence, due to the model's abstraction. However, a looser correspondence holds, between denotations of programs to their evaluations:

LEMMA 7.10 (EVALUATION LEMMA). *For a program M , if $\alpha \langle \overline{\mu, \rho} \rangle \omega \cdot r \in \llbracket M \rrbracket^c$ then $\langle \dot{\alpha}, \mu \rangle, M \Downarrow r$.*

PROOF. By **Retroactive Closure** the trace is obtained by α -rewriting a trace in the \mathcal{M} denotation. We proceed by induction on the length of this sequence. In the base case, we use the **Fundamental Lemma** which, for a single-transition trace degenerates to an uninterrupted execution. For the step, we observe that α -rewrites preserve evaluation. We leave the details to **Appendix D**. \square

The converse of the **Evaluation Lemma** we already have as a special case of **Soundness**. These two, together with **Directional Compositionality**, give us **Directional Adequacy**:

PROOF OF DIRECTIONAL ADEQUACY. Assume $\llbracket M \rrbracket^c \subseteq \llbracket N \rrbracket^c$. Let $\cdot \vdash \Xi [\Gamma \vdash - : A] : G$ be a program context and assume $\langle \dot{\alpha}, \mu \rangle, \Xi [M] \Downarrow V$. By **Soundness**, $\tau \in \llbracket \Xi [M] \rrbracket^c$ for some τ of the form $\alpha \langle \overline{\mu, -} \rangle - \cdot \cdot V$. By **Directional Compositionality** and the assumption, $\tau \in \llbracket \Xi [N] \rrbracket^c$. By the **Evaluation Lemma**, $\langle \dot{\alpha}, \mu \rangle, \Xi [N] \Downarrow V$. \square

7.5 Validating Transformations

Using **Directional Adequacy**, we can validate $M \rightarrow N$ in our model by showing that $\llbracket M \rrbracket^c \supseteq \llbracket N \rrbracket^c$. This already justifies structural transformations by virtue of using standard denotational semantics, as mentioned in §6.1. For others, thanks to **Deferral of Closure** and closure preserving containment, we can use the \mathcal{G} operators instead of the \mathcal{A} operators, making calculations simpler.

Figure 3 lists various transformations that we support in this way. As indicated there, some transformations are supported thanks to the inclusion of the abstract rewrite rules. Another feature that facilitates abstraction is the restriction of denotations to traces in which semantic invariants are captured; this additional abstraction is witnessed by $\llbracket x? ; \langle \rangle \rrbracket^c \supseteq \llbracket \langle \rangle \rrbracket^c$ supporting Irrelevant Read Elimination. **Appendix E** includes a more general collection (**Table 2**) and proofs.

The listed memory-access transformations are stated in ground terms, but imply more general variants. For example, Write-Write Elimination is stated as $\ell := w ; \ell := v \rightarrow \ell := v$, from which we can deduce e.g., $\lambda a : \text{Loc}. a := w ; a := v \rightarrow \lambda a : \text{Loc}. a := v$. This is a consequence of using the standard semantics: structural transformations include any pure computations that result in the same value, and in particular, we can replace the locations and (storable) values with pure computations that result in them, or program variables of the same type.

All told, we claim that our adequate denotational semantics is sufficiently abstract. This supports the case that **Moggi's** semantic toolkit can successfully scale to handle the intricacies of RA concurrency by adapting **Brookes's** traces.

8 Related Work and Concluding Remarks

Our work follows the approach of Brookes [13] and its extension to higher-order functions using monads by Benton et al. [6]. Brookes developed a denotational semantics for shared memory concurrency under standard sequentially consistency [35], and established full abstraction w.r.t. a language that has a global atomic **await** instruction that locks the entire memory. The concepts behind this approach had been used in multiple related developments, e.g. [12, 36, 37, 50]. We hope that our work that targets RA will pave the way for similar continuations.

Jagadeesan et al. [25] adapted Brookes’s semantics to the x86-TSO memory model [42]. They showed that for x86-TSO it suffices to include the final store buffer at the end of the trace and add two additional simple closure rules that emulate non-deterministic propagation of writes from store buffers to memory, and identify observably equivalent store buffers. The x86-TSO model, however, is much closer to sequential consistency than RA, which we study in this paper. In particular, unlike RA, x86-TSO is “multi-copy-atomic” (writes by one thread are made globally visible to *all* other threads at the same time) and successful RMW operations are immediately globally visible. Additionally, the parallel composition construct in Jagadeesan et al. [25] is rather strong: threads are forked and joined only when the store buffers are empty. Being non-multi-copy-atomic, RA requires a more delicate notion of traces and closure rules, but it has more natural meta-theoretic properties, which one would expect from a programming language concurrency model: sequencing, a.k.a. thread-inlining, is unsound under x86-TSO [see 25, 33] but sound under RA (see Figure 3).

Burckhardt et al. [14] developed a denotational semantics for hardware weak memory models (including x86-TSO) following an alternative approach. They represent sequential code blocks by sequences of operations that the code performs, and close them under certain rewrite rules (reorderings and eliminations) that characterize the memory model. This approach does not validate important optimizations, such as Read-Read Elimination. Moreover, unlike x86-TSO, RA cannot be characterized by rewrite operations on SC traces [33].

Dodds et al. [19] developed a fully abstract denotational semantics for RA, extended with fences and non-atomic accesses. Their semantics is based on RA’s *declarative* (a.k.a. axiomatic) formulation as acyclicity criteria on execution graphs. Roughly speaking, their denotation of code blocks (that they assume to be sequential) quantifies over all possible context execution graphs and calculates for each context the “happens-before” relation between context actions that is induced by the block. They further use a finite approximation of these histories to atomically validate refinement in a model checker. While we target RA as well, there are two crucial differences between Dodds et al.’s work and ours. First, we employ Brookes-style totally ordered traces and use interleaving-based operational presentation of RA. Second, and more importantly, we strive for a compositional semantics where denotations of compound programs are defined as functions of denotations of their constituents, which is not the case for Dodds et al.’s definitions. Their model can nonetheless validate transformations by checking them locally without access to the full program.

Others present non-compositional techniques and tools to check refinement under weak memory models between whole-thread sequential programs that apply for any concurrent context. Poetzl and Kroening [46] considered the SC-for-DRF model, using locks to avoid races. Their approach matches source to target by checking that they perform the same state transitions from lock to subsequent unlock operations and that the source does not allow more data-races. Morisset et al. [41] and Chakraborty and Vafeiadis [16] addressed this problem for the C/C++11 model, of which RA is a central fragment, by implementing matching algorithms between source and target that validate that all transformations between them have been independently proven to be safe under C/C++11.

Cho et al. [18] introduced a specialized semantics for *sequential* programs that can be used for justifying compiler optimizations under weak memory concurrency. They showed that behavior refinement under their sequential semantics implies refinement under any (sequential or parallel) context in the Promising Semantics 2.1 [17]. Their work focuses on optimizations of race-free accesses that are similar to C11’s “non-atomics” [4, 34]. It cannot be used to establish the soundness of program transformations that we study in this paper. Adding non-atomics to our model is an important future work.

Denotational approaches were developed for models much weaker than RA [15, 24, 26, 29, 43] that allow the infamous Read-Write Reorder and thus, for a high-level programming language, require addressing the challenge of detecting semantic dependencies between instructions [3]. These approaches are based on summarizing multiple partial orders between actions that may arise when a given program is executed under some context. In contrast, we use totally ordered traces by relating to RA’s interleaving operational semantics. In particular, Kavanagh and Brookes [29] use partial orders, Castellan, Paviotti et al. [15, 43] use event structures, and Jagadeesan et al., Jeffrey et al. [24, 26] employ “Pomsets with Preconditions” which trades compositionality for supporting non-multi-copy-atomicity, as in RA. These approaches do not validate certain access eliminations, nor Irrelevant Load Introduction, which our model validates.

An exciting aspect of our work is the connection between memory models to Moggi’s monadic approach [40]. For SC, Abadi and Plotkin, Dvir et al. [1, 20] have made an even stronger connection via algebraic theories [44]. These allow to modularly combine shared memory concurrency with other computational effects. Birkedal et al. [11] develop semantics for a type-and-effect system for SC memory which they use to enhance compiler optimizations based on assumptions on the context that come from the type system. We hope to the current work can serve as a basis to extend such accounts to weaker models.

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A Proofs for Commutativity

The proof of **Rewrite Commutativity** is in **Appendix F**. Below are proofs of other claims from §7.1.

PROOF OF DEFERRAL OF CLOSURE. Since $(-)^*$ is a closure operator, it is monotonic, so the \supseteq containment follows from the monotonicity of $(\gg^{\mathcal{G}})$ and $(\|\|\mathcal{G})$ (**Proposition 6.3**). Moreover, for the \subseteq containment, suffice it we show that $P_1^* \gg^{\mathcal{G}} f^* \subseteq (P_1 \gg^{\mathcal{G}} f)^*$ and $P_1^* \|\|\mathcal{G} P_2^* \subseteq (P_1 \|\|\mathcal{G} P_2)^*$.

Denote by P^n the set of traces obtained by \star -rewriting n times a trace from P , and similarly for f^n . So it is sufficient to show that for all $n_1, n_2 \in \mathbb{N}$, $P_1^{n_1} \gg^{\mathcal{G}} f^{n_2} \subseteq (P_1 \gg^{\mathcal{G}} f)^*$ and $P_1^{n_1} \|\|\mathcal{G} P_2^{n_2} \subseteq (P_1 \|\|\mathcal{G} P_2)^*$. We show this by induction on $n_1 + n_2$, where the base case $P_1 \gg^{\mathcal{G}} f \subseteq (P_1 \gg^{\mathcal{G}} f)^*$ and $P_1 \|\|\mathcal{G} P_2 \subseteq (P_1 \|\|\mathcal{G} P_2)^*$ holds since $(-)^*$ is a closure operator.

For the induction step, the induction hypothesis is that the claim holds for $n_1 + n_2 \leq m$, and we must show it holds for $n_1 + n_2 = m + 1$. So either $n_1 = n'_1 + 1$ or $n_2 = n'_2 + 1$. We focus on the claim for $(\|\|\mathcal{G})$, since we find that proving the claim for $(\gg^{\mathcal{G}})$ to be similar and somewhat easier.

Let $\tau \in P_1^{n_1} \|\|\mathcal{G} P_2^{n_2}$. So $\tau = \inf_{\xi.o} \{\alpha_1, \alpha_2\} \boxed{\xi} \omega_1 \sqcup \omega_2 \cdot \langle r_1, r_2 \rangle$ where $\tau_i := \alpha_i \boxed{\xi_i} \omega_i \cdot r_i \in P_i^{n_i}$ and $\xi \in \xi_1 \|\|\xi_2$. Assume w.l.o.g. that $n_1 = n'_1 + 1$. So there is some $\tau'_1 \in P_1^{n'_1}$ and $x \in \star$ such that $\tau'_1 \xrightarrow{x} \tau_1$. By case analysis on x , we show that there exists $\tau' \in P_1^{n'_1} \|\|\mathcal{G} P_2^{n_2}$ such that τ' \star -rewrites to τ . By the induction hypothesis $\tau' \in (P_1 \|\|\mathcal{G} P_2)^*$, and so $\tau \in (P_1 \|\|\mathcal{G} P_2)^*$.

For the $x \in \star \cap c$ cases, we construct τ' from τ'_1 and τ_2 . The procedure depends on x :

Rw. So $\tau'_1 = \alpha'_1 \boxed{\xi_1} \omega_1 \cdot r_1$ where $\alpha_1 \leq \alpha'_1$. We take

$$\tau' := \inf_{\xi.o} \{\alpha'_1, \alpha_2\} \boxed{\xi} \omega_1 \sqcup \omega_2 \cdot \langle r_1, r_2 \rangle$$

Since $\inf_{\xi.o} \{\alpha_1, \alpha_2\} \leq \inf_{\xi.o} \{\alpha'_1, \alpha_2\}$, we have $\tau' \xrightarrow{\text{Rw}} \tau$.

Fw. Similar to **Rw**.

St. So $\tau'_1 = \alpha_1 \boxed{\eta_1 \eta'_1} \omega_1 \cdot r_1$ where $\xi_1 = \eta_1 \langle \mu, \mu \rangle \eta'_1$. Since $\xi \in \xi_1 \|\|\xi_2$, there exist η, η' such that $\xi = \eta \langle \mu, \mu \rangle \eta'$, where η includes the transitions from η_1 and η' includes the transitions from

η'_1 . Formally, there exist η_2, η'_2 such that $\xi_2 = \eta_2 \eta'_2$, $\eta \in \eta_1 \parallel \eta_2$ and $\eta' \in \eta'_1 \parallel \eta'_2$. In particular, $\eta \eta' \in \eta_1 \eta'_1 \parallel \eta_2 \eta'_2 = \eta_1 \eta'_1 \parallel \xi_2$. Denoting $\xi' := \eta \eta'$, we take

$$\tau' := \inf_{\xi'.o} \{\alpha_1, \alpha_2\} \boxed{\xi'} \omega_1 \sqcup \omega_2 \cdot \langle r_1, r_2 \rangle$$

We have $\xi.o \subseteq \xi'.o$, so $\inf_{\xi.o} \{\alpha_1, \alpha_2\} \leq \inf_{\xi'.o} \{\alpha_1, \alpha_2\}$. So $\tau' \xrightarrow{\text{Rw}} \tau''$, where

$$\tau'' := \inf_{\xi.o} \{\alpha_1, \alpha_2\} \boxed{\xi'} \omega_1 \sqcup \omega_2 \cdot \langle r_1, r_2 \rangle$$

Since $\xi = \eta \langle \mu, \mu \rangle \eta'$, we have $\tau'' \xrightarrow{\text{St}} \tau$.

Mu. So $\tau'_1 = \alpha_1 \boxed{\eta_1 \langle \mu, \rho \rangle \langle \rho, \theta \rangle \eta'_1} \omega_1 \cdot r_1$ where $\xi_1 = \eta_1 \langle \mu, \theta \rangle \eta'_1$. Since $\xi \in \xi_1 \parallel \xi_2$, there exist η, η' such that $\xi = \eta \langle \mu, \theta \rangle \eta'$, where η includes the transitions from η_1 and η' includes the transitions from η'_1 . Formally, there exist η_2, η'_2 such that $\xi_2 = \eta_2 \eta'_2$, $\eta \in \eta_1 \parallel \eta_2$ and $\eta' \in \eta'_1 \parallel \eta'_2$. In particular, $\eta \langle \mu, \rho \rangle \langle \rho, \theta \rangle \eta' \in \eta_1 \langle \mu, \rho \rangle \langle \rho, \theta \rangle \eta'_1 \parallel \eta_2 \eta'_2 = \eta_1 \langle \mu, \rho \rangle \langle \rho, \theta \rangle \eta'_1 \parallel \xi_2$. Denoting $\xi' := \eta \langle \mu, \rho \rangle \langle \rho, \theta \rangle \eta'$, we take

$$\tau' := \inf_{\xi'.o} \{\alpha_1, \alpha_2\} \boxed{\xi'} \omega_1 \sqcup \omega_2 \cdot \langle r_1, r_2 \rangle$$

Since $\xi = \eta \langle \mu, \theta \rangle \eta'$, and $\xi'.o = \xi.o$ and $\eta'.o = \eta.o$, we have $\tau' \xrightarrow{\text{Mu}} \tau$.

For the $x \in \star \cap a$ cases, we construct τ' from τ'_1 and a τ'_2 defined such that $\tau_2 \xrightarrow{y} \tau'_2$ for some $y \in g$. By iterating **Rewrite Commutativity** n_2 times to commute \xrightarrow{y} through the \star -rewrite sequence that resulted in τ_2 , we find that $\tau'_2 \in P_2^{n_2}$. This is because $P_2 \in \underline{GX}_2$. The procedure depends on x :

Ti. So $\tau'_1 = \alpha_1 \boxed{\eta_1 \langle \mu, \rho \uplus \{v\} \rangle \eta'_1 \bar{\uplus} \{v\}} \omega_1 \cdot r_1$ where $\xi_1 = \eta_1 \langle \mu, \rho \uplus \{\epsilon\} \rangle \eta'_1 \bar{\uplus} \{\epsilon\}$ and $v \leq_{vw} \epsilon$. Since $\xi \in \xi_1 \parallel \xi_2$, there are $\eta, \eta', \eta_2, \eta'_2$ such that $\xi = \eta \langle \mu, \rho \uplus \{\epsilon\} \rangle (\eta' \bar{\uplus} \{\epsilon\})$, $\xi_2 = \eta_2 (\eta'_2 \bar{\uplus} \{\epsilon\})$, $\eta \in \eta_1 \parallel \eta_2$ and $\eta' \bar{\uplus} \{\epsilon\} \in \eta'_1 \bar{\uplus} \{\epsilon\} \parallel \eta'_2 \bar{\uplus} \{\epsilon\}$. Taking the same order of interleaving, $\eta' \bar{\uplus} \{v\} \in \eta'_1 \bar{\uplus} \{v\} \parallel \eta'_2 \bar{\uplus} \{v\}$. Therefore, we have $\xi' \in \xi'_1 \parallel \xi'_2$, where

$$\xi' := \eta \langle \mu, \rho \uplus \{v\} \rangle (\eta' \bar{\uplus} \{v\}), \quad \xi'_1 := \eta_1 \langle \mu, \rho \uplus \{v\} \rangle \eta'_1 \bar{\uplus} \{v\}, \quad \text{and} \quad \xi'_2 := \eta_2 (\eta'_2 \bar{\uplus} \{v\})$$

Define $\tau'_2 := \alpha_2 \boxed{\xi'_2} \omega_2 \cdot r_2$. Since $\tau_2 \xrightarrow{\text{Ls}} \tau'_2$, indeed $\tau'_2 \in P_2^{n_2}$. We take

$$\tau' := \inf_{\xi'.o} \{\alpha_1, \alpha_2\} \boxed{\xi'} \omega_1 \sqcup \omega_2 \cdot \langle r_1, r_2 \rangle$$

Since $\xi.o = \xi'.o$, we have $\tau' \xrightarrow{\text{Ti}} \tau$.

Ab. Similar to Ti, using $\tau_2 \xrightarrow{\text{Ex}} \tau'_2$.

Di. So $\tau'_1 = \left(\alpha_1 \boxed{\eta_1 \langle \mu, \rho \uplus \{v\} \rangle \eta'_1 \bar{\uplus} \{v\}} \omega_1 \cdot r \right) [\uparrow \epsilon]$, where $\xi = \eta_1 \langle \mu, \rho \uplus \{v, \epsilon\} \rangle \eta'_1 \bar{\uplus} \{v, \epsilon\}$. The reasoning in this case proceeds similarly, using $\tau_2 \xrightarrow{\text{Cn}} \tau'_2$ and interleaving τ'_1 with τ'_2 to take

$$\tau' := \inf_{\xi'.o} \{\alpha_1 [\uparrow \epsilon], \alpha_2 [\uparrow \epsilon]\} \boxed{\xi'} \omega_1 [\uparrow \epsilon] \sqcup \omega_2 [\uparrow \epsilon] \cdot \langle r_1, r_2 \rangle$$

We have $\xi.o = \xi'.o$ again too. Moreover, ξ is the chronicle of a trace, and ϵ appears in it. So no view that appears in the trace can point into the interior of ϵ 's segment. Otherwise, since view must point to timestamps of messages, we would have a memory that is not scattered. We show $\inf_{\xi.o} \{\alpha_1, \alpha_2\} [\uparrow \epsilon] \leq \inf_{\xi'.o} \{\alpha_1 [\uparrow \epsilon], \alpha_2 [\uparrow \epsilon]\}$. Indeed, for $\kappa \hookrightarrow \xi.o$, assume $\kappa \leq \alpha_i$. Therefore, $\kappa [\uparrow \epsilon] \leq \alpha_i [\uparrow \epsilon]$, and so $\kappa [\uparrow \epsilon] \leq \inf_{\xi'.o} \{\alpha_1 [\uparrow \epsilon], \alpha_2 [\uparrow \epsilon]\}$. Thus in particular for $\kappa = \inf_{\xi.o} \{\alpha_1, \alpha_2\}$.

By order-comparing $(\omega_i)_{\epsilon.1c}$ to $\epsilon.i$, one also finds that $(\omega_1 \sqcup \omega_2) [\uparrow \epsilon] = \omega_1 [\uparrow \epsilon] \sqcup \omega_2 [\uparrow \epsilon]$.

And so we obtain $\tau' \xrightarrow{\text{Rw}} \xrightarrow{\text{Di}} \tau$. \square

From here on we work to prove **Retroactive Closure** via logical relation. To compensate for the rewrite closure being taken at different stages of higher-order constructions, we use a refined notion of equality.

Egli-Milner lifting. The *trace lifting* of a relation $\sim \subseteq X \times Y$ is a relation $\sim \subseteq \text{Trace}X \times \text{Trace}Y$ defined $\tau \sim \tau' := \tau.\text{st} = \tau'.\text{st} \wedge \tau.\text{v1} \sim \tau'.\text{v1}$. This in turn lifts to the Egli-Milner relation $\sim \subseteq \mathcal{P}(\text{Trace}X) \times \mathcal{P}(\text{Trace}Y)$ where $U \sim E := \forall \tau \in U \exists \tau' \in E. \tau \sim \tau' \wedge \forall \tau' \in E \exists \tau \in U. \tau \sim \tau'$. We call this last relation the *EM-trace lifting* of the first relation. We use the same notation for the relations because we will always be able to infer which relation is meant by the objects related.

Logical relation. For every type A we define $\mathcal{V}^\dagger\{A\} \subseteq \llbracket A \rrbracket \times \llbracket A \rrbracket_{\mathcal{M}}$ and $C^\dagger\{A\} \subseteq \mathcal{A} \llbracket A \rrbracket \times \mathcal{M} \llbracket A \rrbracket_{\mathcal{M}}$ by mutual recursion. The definition of $\mathcal{V}^\dagger\{A\}$ follows the standard “related-inputs to related-outputs” mantra:

$$\begin{aligned} \mathcal{V}^\dagger\{A \rightarrow B\} &:= \{ \langle f, g \rangle \mid \forall \langle r, s \rangle \in \mathcal{V}^\dagger\{A\}. \langle fr, gs \rangle \in C^\dagger\{B\} \} \\ \mathcal{V}^\dagger\{A_1 * \dots * A_n\} &:= \{ \langle \langle r_1, \dots, r_n \rangle, \langle s_1, \dots, s_n \rangle \rangle \mid \forall i. \langle r_i, s_i \rangle \in \mathcal{V}^\dagger\{A_i\} \} \\ \mathcal{V}^\dagger\{ \{t_1 \text{ of } A_1 \mid \dots \mid t_n \text{ of } A_n\} \} &:= \bigcup_i \{ \langle t_i r, t_i s \rangle \mid \langle r, s \rangle \in \mathcal{V}^\dagger\{A_i\} \} \end{aligned}$$

The relation trivializes on ground types: $\mathcal{V}^\dagger\{G\}$ is equality. In particular for $V, W \in \cdot \vdash G$, if $\langle \llbracket V \rrbracket^v, \llbracket W \rrbracket_{\mathcal{M}}^v \rangle \in \mathcal{V}^\dagger\{G\}$, then $V = W$ because as program values, $\llbracket V \rrbracket^v = V$ and $\llbracket W \rrbracket_{\mathcal{M}}^v = W$.

The bespoke $C^\dagger\{A\} := \{ \langle P, Q \rangle \mid \langle P, Q^a \rangle \in \mathcal{V}^\dagger\{A\} \}$ uses the EM-trace lifting of $\mathcal{V}^\dagger\{A\}$ to relate abstract denotations to generating denotations by nesting α -closures.

In regards to open terms, for every typing context Γ we define $\mathcal{X}^\dagger\{\Gamma\} \subseteq \llbracket \Gamma \rrbracket \times \llbracket \Gamma \rrbracket_{\mathcal{M}}$ by:

$$\mathcal{X}^\dagger\{\Gamma\} := \{ \langle \gamma, \delta \rangle \mid \forall (a : A) \in \Gamma. \langle \gamma a, \delta a \rangle \in \mathcal{V}^\dagger\{A\} \}$$

and define $\Gamma \vDash^\dagger M : A$ as follows: $\forall \langle \gamma, \delta \rangle \in \mathcal{X}^\dagger\{\Gamma\}. \langle \llbracket M \rrbracket^c \gamma, \llbracket M \rrbracket_{\mathcal{M}}^c \delta \rangle \in C^\dagger\{A\}$. We show this semantic judgment is sound with respect to the typing relation, following some supportive lemmas.

LEMMA A.1. *If $\langle r, s \rangle \in \mathcal{V}^\dagger\{A\}$ then $\langle \text{return } r, \text{return}^M s \rangle \in C^\dagger\{A\}$.*

PROOF. For the first half of the EM-trace lifting, let $\tau \in \text{return } r = (\text{return}^M r)^a$, where we used **Rewrite Commutativity** to reorder the rewrites. So there exists $\pi \in \text{return}^M r$ such that $\pi \xrightarrow{a} \tau$. Obtain τ', π' from τ, π respectively by replacing their return value r with s . By construction, $\langle \tau, \tau' \rangle \in \mathcal{V}^\dagger\{A\}$. Moreover, $\pi' \in \text{return}^M s$. By reusing the rewrite sequence, $\pi' \xrightarrow{a} \tau'$. Therefore, $\tau' \in (\text{return}^M s)^a$ is a witness as required.

The same idea in reverse shows the second half of the EM-trace lifting. \square

LEMMA A.2. *If $\langle P, Q \rangle \in C^\dagger\{A\}$ and $\langle f, g \rangle \in \mathcal{V}^\dagger\{A \rightarrow B\}$ then $\langle P \gg f, Q \gg^M g \rangle \in C^\dagger\{B\}$.*

PROOF. For the first half of the EM-trace lifting, let $\tau \in P \gg f = (P \gg^M f)^a$, where we used **Lemma 7.1** to reorder the rewrites. So there exists $\pi \in P \gg^M f$ such that $\pi \xrightarrow{a} \tau$. So there exist $\alpha \llbracket \xi \rrbracket \kappa \cdot r \in P$ and $\sigma \llbracket \eta \rrbracket \omega \cdot s \in fr$ where $\kappa \leq \sigma$ such that $\pi = \alpha \llbracket \xi \eta \rrbracket \omega \cdot s$.

- By the first assumption, there exists r' such that $\langle r, r' \rangle \in \mathcal{V}^\dagger\{A\}$ and $\alpha \llbracket \xi \rrbracket \kappa \cdot r' \in Q^a$.
- By the second assumption, there exists s' such that $\langle s, s' \rangle \in \mathcal{V}^\dagger\{B\}$ and $\sigma \llbracket \eta \rrbracket \omega \cdot s' \in (gr')^a$.

So $\pi' := \alpha \llbracket \xi \eta \rrbracket \omega \cdot s' \in Q^a \gg^M g^a$. Obtain τ' from τ by replacing its return value s by s' . By reusing the rewrite sequence, $\pi' \xrightarrow{a} \tau'$. By **Deferral of Closure**, $\tau' \in (Q^a \gg^M g^a)^a = (Q \gg^M g)^a$.

The same idea in reverse shows the second half of the EM-trace lifting. \square

LEMMA A.3. $\langle \llbracket \text{store}_{\ell, v} \rrbracket, \llbracket \text{store}_{\ell, v} \rrbracket_{\mathcal{M}} \rangle \in C^\dagger\{\mathbf{1}\}$ and $\langle \llbracket \text{rmw}_{\ell, \varphi} \rrbracket, \llbracket \text{rmw}_{\ell, \varphi} \rrbracket_{\mathcal{M}} \rangle \in C^\dagger\{\mathbf{Val}\}$.

PROOF. Since $\mathbf{1}$ and \mathbf{Val} are ground types, the sets are equal by [Deferral of Closure](#), reasoning as in [Lemma A.1](#). \square

LEMMA A.4. If $\langle P_i, Q_i \rangle \in C^\dagger\{A_i\}$ then $\langle P_1 \parallel P_2, Q_1 \parallel Q_2 \rangle \in C^\dagger\{(A_1 * A_2)\}$.

PROOF. Similar to [Lemma A.2](#). \square

PROPOSITION A.5. If $\Gamma \vdash M : A$ then $\Gamma \vDash^\dagger M : A$.

PROOF. By induction on the derivation of $\Gamma \vdash M : A$. We detail some paradigmatic examples:

$\Gamma, a : A \vDash^\dagger M : B$	Let $\langle \gamma, \delta \rangle \in \mathcal{X}^\dagger\{\Gamma\}$. If $\langle r, s \rangle \in \mathcal{V}^\dagger\{A\}$, then $\langle \gamma[a \mapsto r], \delta[a \mapsto s] \rangle \in \mathcal{X}^\dagger\{\Gamma, a : A\}$. By assumption, $\langle \llbracket M \rrbracket^c \gamma[a \mapsto r], \llbracket M \rrbracket_{\mathcal{M}}^c \delta[a \mapsto s] \rangle \in C^\dagger\{B\}$. Therefore, $\langle \lambda r. \llbracket M \rrbracket^c \gamma[a \mapsto r], \lambda s. \llbracket M \rrbracket_{\mathcal{M}}^c \delta[a \mapsto s] \rangle \in \mathcal{V}^\dagger\{A \rightarrow B\}$. Applying Lemma A.1 , $\langle \llbracket \lambda a. M \rrbracket^c \gamma, \llbracket \lambda a. M \rrbracket_{\mathcal{M}}^c \delta \rangle \in C^\dagger\{A \rightarrow B\}$.
$\Gamma \vDash^\dagger \lambda a : A. M : A \rightarrow B$	

$\Gamma \vDash^\dagger M : A \quad \Gamma \vDash^\dagger N : A \rightarrow B$	Let $\langle \gamma, \delta \rangle \in \mathcal{X}^\dagger\{\Gamma\}$. If $\langle f, g \rangle \in \mathcal{V}^\dagger\{A \rightarrow B\}$, then by Lemma A.2 with the first assumption, $\langle \llbracket M \rrbracket^c \gamma \Vdash f, \llbracket M \rrbracket_{\mathcal{M}}^c \delta \Vdash^M g \rangle \in C^\dagger\{B\}$. Thus $\langle \lambda f. \llbracket M \rrbracket^c \gamma \Vdash f, \lambda g. \llbracket M \rrbracket_{\mathcal{M}}^c \delta \Vdash^M g \rangle \in \mathcal{V}^\dagger\{(A \rightarrow B) \rightarrow B\}$.
$\Gamma \vDash^\dagger NM : B$	

So by [Lemma A.2](#) with the second assumption, $\langle \llbracket NM \rrbracket^c \gamma, \llbracket NM \rrbracket_{\mathcal{M}}^c \delta \rangle \in C^\dagger\{B\}$.

The other cases follow by similar reasoning with [Lemmas A.1](#) and [A.2](#), where in the cases of the effects we also use the respective [Lemmas A.3](#) and [A.4](#). \square

PROOF OF [RETROACTIVE CLOSURE](#). Since M is a program, by [Proposition A.5](#), $\cdot \vDash M : G$ for some ground type G . That is, $\langle \llbracket M \rrbracket^c, \llbracket M \rrbracket_{\mathcal{M}}^c \rangle \in C^\dagger\{A\}$. Since the EM-trace lifting degenerates to equality on ground types, $\llbracket M \rrbracket^c = \llbracket M \rrbracket_{\mathcal{M}}^c$. \square

B Proof of Directional Compositionality

We prove [Directional Compositionality](#) via logical relation. For this, we use a refinement of the notion of set-containment.

Hoare lifting. The *trace lifting* of a relation $\sim \subseteq X \times Y$ is a relation $\sim \subseteq \text{Trace}X \times \text{Trace}Y$ defined $\tau \sim \tau' := \tau.\text{st} = \tau'.\text{st} \wedge \tau.\text{v1} \sim \tau'.\text{v1}$. This in turn lifts to the Hoare relation $\sim \subseteq \mathcal{P}(\text{Trace}X) \times \mathcal{P}(\text{Trace}Y)$ where $U \sim E := \forall \tau \in U \exists \tau' \in E. \tau \sim \tau'$. We call this last relation the *H-trace lifting* of the first relation.

Logical relation. For every type A we define $\mathcal{V}^\circ\{A\} \subseteq \llbracket A \rrbracket \times \llbracket A \rrbracket$ and $C^\circ\{A\} \subseteq \mathcal{A} \llbracket A \rrbracket \times \mathcal{A} \llbracket A \rrbracket$ by mutual recursion. The definition of $\mathcal{V}^\circ\{A\}$ follows the standard “related-inputs to related-outputs” mantra:

$$\begin{aligned} \mathcal{V}^\circ\{A \rightarrow B\} &:= \{ \langle f, g \rangle \mid \forall \langle r, s \rangle \in \mathcal{V}^\circ\{A\}. \langle f r, g s \rangle \in C^\circ\{B\} \} \\ \mathcal{V}^\circ\{A_1 * \dots * A_n\} &:= \{ \langle \langle r_1, \dots, r_n \rangle, \langle s_1, \dots, s_n \rangle \rangle \mid \forall i. \langle r_i, s_i \rangle \in \mathcal{V}^\circ\{A_i\} \} \\ \mathcal{V}^\circ\{\{t_1 \text{ of } A_1 \mid \dots \mid t_n \text{ of } A_n\}\} &:= \bigcup_i \{ \langle t_i r, t_i s \rangle \mid \langle r, s \rangle \in \mathcal{V}^\circ\{A_i\} \} \end{aligned}$$

The relation trivializes on ground types: $\mathcal{V}^\circ\{G\}$ is equality. In particular for $V, W \in \cdot \vdash G$, if $\langle \llbracket V \rrbracket^v, \llbracket W \rrbracket^v \rangle \in \mathcal{V}^\circ\{G\}$, then $V = W$ because as program values, $\llbracket V \rrbracket^v = V$ and $\llbracket W \rrbracket^v = W$. We H-trace lift $\mathcal{V}^\circ\{A\}$ to obtain $C^\circ\{A\}$. It too trivializes on ground types: $C^\circ\{G\}$ is containment. In regards to open terms, for every typing context Γ we define $\mathcal{X}^\circ\{\Gamma\} \subseteq \llbracket \Gamma \rrbracket \times \llbracket \Gamma \rrbracket$ by:

$$\mathcal{X}^\circ\{\Gamma\} := \{ \langle \gamma, \delta \rangle \mid \forall (a : A) \in \Gamma. \langle \gamma a, \delta a \rangle \in \mathcal{V}^\circ\{A\} \}$$

and define $\Gamma \vDash M \lesssim N : A$ as follows: $\forall \langle \gamma, \delta \rangle \in \mathcal{X}^\circ\{\Gamma\} . \langle \llbracket M \rrbracket^c \gamma, \llbracket N \rrbracket^c \delta \rangle \in \mathcal{C}^\circ\{A\}$.

As in [Appendix A](#), we have the same supportive lemmas for this logical relation. The proofs are similar, though slightly simpler because there is no need for [Lemma 7.2](#).

LEMMA B.1. *If $\langle r, s \rangle \in \mathcal{V}^\circ\{A\}$ then $\langle \text{return } r, \text{return } s \rangle \in \mathcal{C}^\circ\{A\}$.*

LEMMA B.2. *If $\langle P, Q \rangle \in \mathcal{C}^\circ\{A\}$ and $\langle f, g \rangle \in \mathcal{V}^\circ\{A \rightarrow B\}$ then $\langle P \gg f, Q \gg g \rangle \in \mathcal{C}^\circ\{B\}$.*

LEMMA B.3. *$\langle \llbracket \text{store}_{\ell,v} \rrbracket, \llbracket \text{store}_{\ell,v} \rrbracket \rangle \in \mathcal{C}^\circ\{1\}$ and $\langle \llbracket \text{rmw}_{\ell,\varphi} \rrbracket, \llbracket \text{rmw}_{\ell,\varphi} \rrbracket \rangle \in \mathcal{C}^\circ\{\text{Val}\}$.*

LEMMA B.4. *If $\langle P_i, Q_i \rangle \in \mathcal{C}^\circ\{A_i\}$ then $\langle P_1 \parallel P_2, Q_1 \parallel Q_2 \rangle \in \mathcal{C}^\circ\{(A_1 * A_2)\}$.*

The judgment is closed under term contexts:

LEMMA B.5. *For $\Delta \vdash \Xi [\Gamma \vdash - : A] : B$, if $\Gamma \vDash M \lesssim N : A$, then $\Delta \vDash \Xi [M] \lesssim \Xi [N] : B$.*

PROOF. By induction on the derivation of $\Delta \vdash \Xi [\Gamma \vdash - : A] : B$. The metavariable case holds by assumption. The rest uses the supportive lemmas [Lemmas B.1 to B.4](#) as in the proof of [Proposition A.5](#). \square

PROPOSITION B.6. *If $\mathcal{A} \llbracket A \rrbracket \ni P' \subseteq P$ and $\langle P, Q \rangle \in \mathcal{C}^\circ\{A\}$ then $\langle P', Q \rangle \in \mathcal{C}^\circ\{A\}$.*

PROOF. Assuming a statement about all elements of P we deduce the same statement about all elements of P' . \square

LEMMA B.7. *For $M, N \in \Gamma \vdash A$, if $\llbracket M \rrbracket^c \subseteq \llbracket N \rrbracket^c$ then $\Gamma \vDash M \lesssim N : A$.*

PROOF. Let $\langle \gamma, \delta \rangle \in \mathcal{X}^\circ\{\Gamma\}$. By [Lemma B.5](#) with N itself as the context (the degenerate case with no metavariable appearance), $\langle \llbracket N \rrbracket^c \gamma, \llbracket N \rrbracket^c \delta \rangle \in \mathcal{C}^\circ\{A\}$. By assumption, $\llbracket M \rrbracket^c \gamma \subseteq \llbracket N \rrbracket^c \gamma$. So by [Proposition B.6](#), $\langle \llbracket M \rrbracket^c \gamma, \llbracket N \rrbracket^c \delta \rangle \in \mathcal{C}^\circ\{A\}$. \square

PROOF OF [THEOREM 7.7](#). By [Lemma B.7](#), $\Gamma \vDash M \lesssim N : A$. By [Lemma B.5](#), $\cdot \vDash \Xi [M] \lesssim \Xi [N] : G$. That is, $\langle \llbracket \Xi [M] \rrbracket^c, \llbracket \Xi [N] \rrbracket^c \rangle \in \mathcal{C}^\circ\{G\}$. Since G is ground, this degenerates to $\llbracket \Xi [M] \rrbracket^c \subseteq \llbracket \Xi [N] \rrbracket^c$. \square

C Proof of Soundness

To enable optimizations, the abstract model decouples traces far enough from the operational semantics to make it non-trivial to prove [Soundness](#). To overcome this challenge we use a logical relation to relate the abstract model to a model which corresponds tightly to the execution steps of the operational semantics, by tracking the initial view-tree and the memory accesses individually. Formally, for a set X , an X -run-trace is an element of $\text{VTree} \times \text{Mem} \times \text{Chro} \times \text{View} \times X$, written $\tau = \langle T, \mu \rangle \boxed{\xi} \omega \cdot r$. We denote the set of X -run-traces by $\text{OpTrace}X$.

As in traces, the run-trace's main component is its chronicle $\tau.\text{ch} = \xi$, with transitions consisting of well-formed memories. Here, each transition represents a *single memory-accessing step* during the interrupted execution; i.e. those labeled by \bullet . We call such steps *loud*, and the other step *silent*; i.e. those labeled by \circ . Respectively, the run-trace is *silent* if ξ is empty, otherwise it is *loud*.

The run-trace's initial state is $\langle T, \mu \rangle$. This represents the state from the execution's initial configuration, so we require that $T \hookrightarrow \mu$. However, the environment may add messages before the term starts running, so in the loud case we only require $\mu \subseteq \xi.\circ$.

The run-trace's final view is $\tau.\text{fvw} = \omega$. The corresponding interrupted execution ends with $\hat{\omega}$. In the silent case we require $T = \hat{\omega}$ since silent steps do not change the state. As a derived notion, the run-trace's final state is $\langle \hat{\omega}, (\langle \mu, \mu \rangle \xi).c \rangle$, so we require $\omega \hookrightarrow \langle \mu, \mu \rangle \xi.c$.

In light of [Lemma 5.16](#), we require moreover that $\kappa \leq \omega$ for every $\kappa \in T.\text{lf}$, denote by $T \leq \omega$. Moreover, considering [Lemma 5.15](#), we require $\forall v \in \xi.\text{own} \exists \alpha \in T.\text{lf} . \alpha \leq v.\text{vw} \leq \omega \wedge \alpha_{v.\text{lc}} < v.\text{t}$.

Finally, the run-trace's return value is $\tau.\text{ret} = r$. This corresponds to the program value the interrupted execution returns.

We define a monad structure $\mathcal{R}X := \langle \underline{\mathcal{R}X}, \text{return}^{\mathcal{R}}, \gg^{\mathcal{R}} \rangle$:

$$\underline{\mathcal{R}X} := \mathcal{P}(\text{OpTrace}X) \quad \text{return}^{\mathcal{R}}r := \{\langle \dot{k}, \mu \rangle \sqsupset \kappa \cdot r\}$$

$$P \gg^{\mathcal{R}} f := \{\langle T, \mu \rangle \boxed{\xi\eta} \omega \cdot s \in \underline{\mathcal{R}Y} \mid \exists r, \kappa. \langle T, \mu \rangle \boxed{\xi} \kappa \cdot r \in P \wedge \langle \dot{k}, (\langle \mu, \mu \rangle \xi).c \rangle \boxed{\eta} \omega \cdot s \in fr\}$$

In the return operator, we make sure that the initial and final states are equal. In the bind operator, we make sure that the final state of the first run-trace is the initial state of the second run-trace.

PROPOSITION C.1. \mathcal{R} is a monad.

Next we extend \mathcal{R} with shared-memory constructs.

Concurrent execution. Consider a program $M \parallel N$. Either the state has a leaf \dot{k} as its view-tree, in which case the first step it takes has to be `PARINIT`, or it has a node as its view tree $T \widehat{\ } \mathcal{R}$, in which case the first step it takes cannot be `PARINIT`. Either way, it then takes some steps due to steps of M and N (with `PARLEFT` and `PARRIGHT`), then finally it steps with `PARFIN` to synchronize.

$$P_1 \parallel^{\mathcal{R}} P_2 := \left\{ \langle T, \mu \rangle \boxed{\xi} \omega_1 \sqcup \omega_2 \cdot \langle r_1, r_2 \rangle \in \underline{\mathcal{R}}(X_1 \times X_2) \mid \exists \xi_1, \xi_2. \xi \in \xi_1 \parallel \xi_2 \wedge \exists T_1, T_2. \right. \\ \left. \left(\forall i \in \{1, 2\}. \langle T_i, \mu \rangle \boxed{\xi_i} \omega_i \cdot r_i \in P_i \right) \wedge \left(T = T_1 \widehat{\ } T_2 \vee \exists \kappa. T = T_1 = T_2 = \dot{k} \right) \right\}$$

Memory access. The definitions follow the `STORE`, `READONLY`, and `RMW` rules:

$$\begin{aligned} \llbracket \text{store}_{\ell, v} \rrbracket_{\mathcal{R}} &:= \{\langle \dot{\alpha}, \mu \rangle \boxed{\langle \rho, \rho \uplus \{\ell: v @ (q, t) \llbracket \alpha[\ell \mapsto t] \rrbracket \} \rangle} \alpha[\ell \mapsto t] \cdot \langle \rangle \in \underline{\mathcal{R}1}\} \\ \llbracket \text{rmw}_{\ell, \Phi}^{\text{RO}} \rrbracket_{\mathcal{R}} &:= \{\langle \dot{\alpha}, \mu \rangle \boxed{\langle \rho, \rho \rangle} \alpha \sqcup \kappa \cdot v \in \underline{\mathcal{R}Val} \mid \Phi v = \perp, \ell: v @ (-, \kappa_{\ell}) \llbracket \kappa \rrbracket \in \rho, \alpha_{\ell} \leq \kappa_{\ell}\} \\ \llbracket \text{rmw}_{\ell, \Phi}^{\text{RMW}} \rrbracket_{\mathcal{R}} &:= \left\{ \langle \dot{\alpha}, \mu \rangle \boxed{\langle \rho, \rho \uplus \{\ell: \Phi v @ (\kappa_{\ell}, t) \llbracket \omega \rrbracket \} \rangle} \omega \cdot v \in \underline{\mathcal{R}Val} \right. \\ &\quad \left. \mid \omega = (\alpha \sqcup \kappa) [\ell \mapsto t], \ell: v @ (-, t) \llbracket \kappa \rrbracket \in \rho \right\} \\ \llbracket \text{rmw}_{\ell, \Phi} \rrbracket_{\mathcal{R}} &:= \llbracket \text{rmw}_{\ell, \Phi}^{\text{RO}} \rrbracket_{\mathcal{R}} \cup \llbracket \text{rmw}_{\ell, \Phi}^{\text{RMW}} \rrbracket_{\mathcal{R}} \end{aligned}$$

Some of the premises of the corresponding rules appear as conditions in the set notations, while other do not appear because they hold implicitly due to the requirements on run-traces.

The importance of a run-trace's initial memory is in making sense of the initial view-tree, even if the chronicle is empty. In particular, messages unseen by the initial view-tree are redundant:

LEMMA C.2. If $\langle T, \mu' \rangle \boxed{\xi} \omega \cdot r \in \llbracket M \rrbracket_{\mathcal{R}}^c$ and $T \hookrightarrow \mu \subseteq \mu'$ then $\langle T, \mu \rangle \boxed{\xi} \omega \cdot r \in \llbracket M \rrbracket_{\mathcal{R}}^c$.

Single-step soundness. To make the relationship between these denotations and the operational semantics precise, we can follow an execution backwards, adding a transition for every \bullet -step:

LEMMA C.3. Assume $\langle T, \mu \rangle, M \xrightarrow{e}_{\text{RA}} \langle T', \mu' \rangle, M'$ and $\langle T', \mu' \rangle \boxed{\xi} \omega \cdot r \in \llbracket M' \rrbracket_{\mathcal{R}}^c$.

- If $e = \circ$, then $\langle T, \mu \rangle \boxed{\xi} \omega \cdot r \in \llbracket M \rrbracket_{\mathcal{R}}^c$.
- If $e = \bullet$, then $\langle T, \mu \rangle \boxed{\langle \mu, \mu' \rangle \xi} \omega \cdot r \in \llbracket M \rrbracket_{\mathcal{R}}^c$.

PROOF. By induction on the derivation of $\langle T, \mu \rangle, M \xrightarrow{e}_{\text{RA}} \langle T', \mu' \rangle, M'$. Paradigmatic examples follow:

APP Assume $\langle \dot{k}, \mu \rangle, (\lambda a. M) V \xrightarrow{\circ}_{\text{RA}} \langle \dot{k}, \mu \rangle, M[a \mapsto V]$ and $\tau := \langle \dot{k}, \mu \rangle \boxed{\xi} \omega \cdot r \in \llbracket M[a \mapsto V] \rrbracket_{\mathcal{R}}^c$. By the **Substitution Lemma**, $\llbracket M[a \mapsto V] \rrbracket_{\mathcal{R}}^c = \llbracket (\lambda a. M) V \rrbracket_{\mathcal{R}}^c$. So indeed $\tau \in \llbracket (\lambda a. M) V \rrbracket_{\mathcal{R}}^c$.

PARLEFT Assume $\langle T \widehat{\ } R, \mu \rangle, M \parallel N \overset{\bullet}{\rightsquigarrow}_{RA} \langle T' \widehat{\ } R, \mu' \rangle, M' \parallel N$ and $\langle T' \widehat{\ } R, \mu' \rangle \boxed{\xi} \omega \cdot \langle r, s \rangle \in \llbracket M' \parallel N \rrbracket_{\mathcal{R}}^c$. So there exist ξ_1, ξ_2 such that $\xi \in \xi_1 \parallel \xi_2$, and there exist ω_1, ω_2 where $\omega = \omega_1 \sqcup \omega_2$ such that $\langle T', \mu' \rangle \boxed{\xi_1} \omega_1 \cdot r \in \llbracket M' \rrbracket_{\mathcal{R}}^c$ and $\langle R, \mu' \rangle \boxed{\xi_2} \omega_2 \cdot s \in \llbracket N \rrbracket_{\mathcal{R}}^c$. In that latter we can replace μ' with μ using [Lemma C.2](#). By the induction hypothesis and the former, $\langle T, \mu \rangle \boxed{\langle \mu, \mu' \rangle \xi_1} \omega_1 \cdot r \in \llbracket M \rrbracket_{\mathcal{R}}^c$. Since $\langle \mu, \mu' \rangle \xi \in \langle \mu, \mu' \rangle \xi_1 \parallel \xi_2$, we have $\langle T \widehat{\ } R, \mu \rangle \boxed{\langle \mu, \mu' \rangle \xi} \omega \cdot \langle r, s \rangle \in \llbracket M \parallel N \rrbracket_{\mathcal{R}}^c$. \square

We say a chronicle ξ is *gapless* if $\rho = \rho'$ whenever $\langle \mu, \rho \rangle$ is followed by $\langle \rho', \theta \rangle$ in ξ . Traces that feature gapless chronicles can be rewritten using mumble to obtain single-transition traces.

PROPOSITION C.4. *If $\langle T, \mu \rangle, M \overset{*}{\rightsquigarrow}_{RA} \langle \dot{\omega}, \mu' \rangle, V$, then $\langle T, \mu \rangle \boxed{\xi} \omega \cdot \llbracket V \rrbracket_{\mathcal{R}}^v \in \llbracket M \rrbracket_{\mathcal{R}}^c$, where $\eta = \langle \mu, \mu \rangle \xi$ is gapless and $\eta.c = \mu'$, i.e. either: (i) ξ is empty and $\mu = \mu'$; or (ii) $\xi.o = \mu, \xi.c = \mu'$, and ξ is gapless.*

PROOF. By induction on the number of small-steps. Case (i) applies so long as all the steps so far are silent. Case (ii) applies otherwise. \square

Hoare run-lifting. The *run-trace lifting* of a relation $\sim \subseteq X \times Y$ is a relation $\sim \subseteq \text{OpTrace}X \times \text{Trace}Y$ defined $\tau \sim \tau' := \exists T, \mu, \xi, \omega, r, s. \tau = \langle T, \mu \rangle \boxed{\xi} \omega \cdot r \wedge \tau' = \inf_{\mu} T \langle \langle \mu, \mu \rangle \xi \rangle \omega \cdot s \wedge r \sim s$. This in turn lifts to the Hoare relation $\sim \subseteq \mathcal{P}(\text{OpTrace}X) \times \mathcal{P}(\text{Trace}Y)$ where $U \sim E := \forall \tau \in U \exists \tau' \in E. \tau \sim \tau'$. We call this last relation the *H-run-trace lifting* of the first relation.

Logical relation. For every type A we define $\mathcal{V}^*\{A\} \subseteq \llbracket A \rrbracket_{\mathcal{R}} \times \llbracket A \rrbracket_{\mathcal{M}}$ and $C^*\{A\} \subseteq \mathcal{R}\llbracket A \rrbracket_{\mathcal{R}} \times M\llbracket A \rrbracket_{\mathcal{M}}$ by mutual recursion. The definition of $\mathcal{V}^*\{A\}$ follows the standard “related-inputs to related-outputs” mantra, while the bespoke $C^*\{A\}$ part transforms the view tree to its greatest lower bound using the notation $\inf_{\mu} T := \inf_{\mu} T.1\mathfrak{f}$, and adds a transition for the first memory:

$$\begin{aligned} \mathcal{V}^*\{A \rightarrow B\} &:= \{ \langle f, g \rangle \mid \forall \langle r, s \rangle \in \mathcal{V}^*\{A\}. \langle fr, gs \rangle \in C^*\{B\} \} \\ \mathcal{V}^*\{A_1 * \dots * A_n\} &:= \{ \langle \langle r_1, \dots, r_n \rangle, \langle s_1, \dots, s_n \rangle \rangle \mid \forall i. \langle r_i, s_i \rangle \in \mathcal{V}^*\{A_i\} \} \\ \mathcal{V}^*\{\{ \iota_1 \text{ of } A_1 \mid \dots \mid \iota_n \text{ of } A_n \}\} &:= \bigcup_i \{ \langle \iota_i r, \iota_i s \rangle \mid \langle r, s \rangle \in \mathcal{V}^*\{A_i\} \} \end{aligned}$$

The relation trivializes on ground types: $\mathcal{V}^*\{G\}$ is equality. In particular for $V, W \in \cdot \vdash G$, if $\langle V, W \rangle \in \mathcal{V}^*\{G\}$, then $V = W$ because as ground-typed values, $\llbracket V \rrbracket_{\mathcal{R}}^v = V$ and $\llbracket W \rrbracket_{\mathcal{M}}^v = W$. We H-trace lift $\mathcal{V}^*\{A\}$ to obtain $C^*\{A\}$.

In regards to open terms, for every typing context Γ we define $\mathcal{X}^*\{\Gamma\} \subseteq \llbracket \Gamma \rrbracket_{\mathcal{R}} \times \llbracket \Gamma \rrbracket_{\mathcal{M}}$ by:

$$\mathcal{X}^*\{\Gamma\} := \{ \langle \gamma, \delta \rangle \mid \forall (a : A) \in \Gamma. \langle \gamma a, \delta a \rangle \in \mathcal{V}^*\{A\} \}$$

and define $\Gamma \vDash^* M : A$ as follows: $\forall \langle \gamma, \delta \rangle \in \mathcal{X}^*\{\Gamma\}. \langle \llbracket M \rrbracket_{\mathcal{R}}^c \gamma, \llbracket M \rrbracket_{\mathcal{M}}^c \delta \rangle \in C^*\{A\}$. We show this semantic judgment is sound with respect to the typing relation, following some supportive lemmas.

LEMMA C.5. *If $\langle r, s \rangle \in \mathcal{V}^*\{A\}$ then $\langle \text{return}^{\mathcal{R}} r, \text{return } s \rangle \in C^*\{A\}$.*

PROOF. Assume $\langle r, s \rangle \in \mathcal{V}^*\{A\}$. W.l.o.g., let $\langle \dot{\kappa}, \mu \rangle \sqsupset \kappa \cdot r \in \text{return}^{\mathcal{R}} r$, where $\kappa \hookrightarrow \mu$. Note that $\kappa \boxed{\langle \mu, \mu \rangle} \kappa \cdot s \in \text{return } s$. Trivially, $\langle \mu, \mu \rangle \cdot = \langle \mu, \mu \rangle$ and $\inf_{\mu} \dot{\kappa} = \kappa$. Substituting these, together with our assumption, we obtain the required precisely:

$$\forall \langle \dot{\kappa}, \mu \rangle \sqsupset \kappa \cdot r \in \text{return}^{\mathcal{R}} r \exists s. \inf_{\mu} \dot{\kappa} \boxed{\langle \mu, \mu \rangle} \kappa \cdot s \in \text{return } s \wedge \langle r, s \rangle \in \mathcal{V}^*\{A\} \quad \square$$

LEMMA C.6. *If $\langle P, Q \rangle \in C^*\{A\}$ and $\langle f, g \rangle \in \mathcal{V}^*\{A \rightarrow B\}$ then $\langle P \rrbracket_{\mathcal{R}}^{\mathcal{R}} f, Q \rrbracket_{\mathcal{M}}^{\mathcal{R}} g \rangle \in C^*\{B\}$.*

PROOF. Assume $\langle P, Q \rangle \in C^*\{A\}$ and $\langle f, g \rangle \in \mathcal{V}^*\{A \rightarrow B\}$. Let $\langle T, \mu \rangle \boxed{\xi\eta} \omega \cdot : r' \in P \Vdash^R f$. So there exist r and κ such that $\langle T, \mu \rangle \boxed{\xi} \kappa \cdot : r \in P$ and $\langle \dot{\kappa}, (\langle \mu, \mu \rangle \xi).c \rangle \boxed{\eta} \omega \cdot : r' \in fr$.

By the first assumption there exists an s such that $\inf_\mu T \boxed{\langle \mu, \mu \rangle \xi} \kappa \cdot : s \in Q$ and $\langle r, s \rangle \in \mathcal{V}^*\{A\}$. Using the second assumption we find that $\langle fr, gs \rangle \in C^*\{B\}$. In particular, there exists an s' such that $\kappa \boxed{\langle \langle \mu, \mu \rangle \xi \rangle.c, (\langle \mu, \mu \rangle \xi).c} \boxed{\eta} \omega \cdot : s' \in gs$ and $\langle r', s' \rangle \in \mathcal{V}^*\{B\}$. So we have

$$\inf_\mu T \boxed{\langle \mu, \mu \rangle \xi \langle \langle \mu, \mu \rangle \xi \rangle.c, (\langle \mu, \mu \rangle \xi).c} \boxed{\eta} \omega \cdot : s' \in Q \Vdash g$$

By using mumble, we have the required $\inf_\mu T \boxed{\langle \mu, \mu \rangle \xi \eta} \omega \cdot : s' \in Q \Vdash g$. \square

LEMMA C.7. $\langle \llbracket \text{store}_{\ell,v} \rrbracket_{\mathcal{R}}, \llbracket \text{store}_{\ell,v} \rrbracket_{\mathcal{M}} \rangle \in C^*\{1\}$ and $\langle \llbracket \text{rmw}_{\ell,\varphi} \rrbracket_{\mathcal{R}}, \llbracket \text{rmw}_{\ell,\varphi} \rrbracket_{\mathcal{M}} \rangle \in C^*\{\text{Val}\}$.

PROOF. Using stutter to compensate for the additional transition from the initial memory, and rewind to compensate for the view not necessarily already pointing to the loaded message. \square

LEMMA C.8. If $\langle P_i, Q_i \rangle \in C^*\{A_i\}$ then $\langle P_1 \parallel P_2, Q_1 \parallel Q_2 \rangle \in C^*\{(A_1 * A_2)\}$.

PROOF. Assume $\langle P_i, Q_i \rangle \in C^*\{A_i\}$, and let $\tau \in P_1 \parallel P_2$. We proceed by case analysis depending on whether the initial view tree is a leaf:

Leaf. W.l.o.g., $\tau = \langle \dot{\kappa}, \mu \rangle \boxed{\xi} \omega_1 \sqcup \omega_2 \cdot : \langle r_1, r_2 \rangle$, where $\langle \dot{\kappa}, \mu \rangle \boxed{\xi_i} \omega_i \cdot : r_i \in P_i$ and $\xi \in \xi_1 \parallel \xi_2$.

So there exist s_i such that $\kappa \boxed{\langle \mu, \mu \rangle \xi_i} \omega_i \cdot : s_i \in Q_i$ and $\langle r_i, s_i \rangle \in \mathcal{V}^*\{A_i\}$. By definition, $\kappa \boxed{\langle \mu, \mu \rangle \langle \mu, \mu \rangle \xi} \omega_1 \sqcup \omega_2 \cdot : \langle s_1, s_2 \rangle \in Q_1 \parallel Q_2$. Using mumble we have $\kappa \boxed{\langle \mu, \mu \rangle \xi} \omega_1 \sqcup \omega_2 \cdot : \langle s_1, s_2 \rangle \in Q_1 \parallel Q_2$. Since $\langle \langle r_1, r_2 \rangle, \langle s_1, s_2 \rangle \rangle \in \mathcal{V}^*\{(A_1 * A_2)\}$, we are done.

Node. W.l.o.g., $\tau = \langle T_1 \widehat{T}_2, \mu \rangle \boxed{\xi} \omega_1 \sqcup \omega_2 \cdot : \langle r_1, r_2 \rangle$, where $\langle T_i, \mu \rangle \boxed{\xi_i} \omega_i \cdot : r_i \in P_i$ and $\xi \in \xi_1 \parallel \xi_2$.

So there exist s_i such that $\inf_\mu T_i \boxed{\langle \mu, \mu \rangle \xi_i} \omega_i \cdot : s_i \in Q_i$ and $\langle r_i, s_i \rangle \in \mathcal{V}^*\{A_i\}$. Rudimentarily, $\inf_\mu \{ \inf_\mu T_1, \inf_\mu T_2 \} = \inf_\mu (T_1 \widehat{T}_2)$, so $\inf_\mu (T_1 \widehat{T}_2) \boxed{\langle \mu, \mu \rangle \langle \mu, \mu \rangle \xi} \omega_1 \sqcup \omega_2 \cdot : \langle s_1, s_2 \rangle \in Q_1 \parallel Q_2$. The rest is like before. \square

PROPOSITION C.9. If $\Gamma \vdash M : A$ then $\Gamma \vDash^* M : A$.

PROOF. By induction on the derivation of $\Gamma \vdash M : A$. We detail some paradigmatic examples:

$\frac{\Gamma, a : A \vDash^* M : B}{\Gamma \vDash^* \lambda a : A. M : A \rightarrow B}$	Let $\langle \gamma, \delta \rangle \in \mathcal{X}^*\{\Gamma\}$. We have $\llbracket \lambda a. M \rrbracket_{\mathcal{R}\gamma}^c = \text{return}^R \lambda r. \llbracket M \rrbracket_{\mathcal{R}\gamma}^c [a \mapsto r]$ and $\llbracket \lambda a. M \rrbracket_{\mathcal{M}\delta}^c = \text{return} \lambda s. \llbracket M \rrbracket_{\mathcal{M}\delta}^c [a \mapsto s]$ by definition. By Lemma C.5 and the definition of $\mathcal{V}^*\{A \rightarrow B\}$, it suffices to show that if $\langle r, s \rangle \in \mathcal{V}^*\{A\}$, then $\langle \llbracket M \rrbracket_{\mathcal{R}\gamma}^c [a \mapsto r], \llbracket M \rrbracket_{\mathcal{M}\delta}^c [a \mapsto s] \rangle \in C^*\{B\}$, which is implied by the induction hypothesis.
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$\frac{\Gamma \vDash^* M : A \quad \Gamma \vDash^* N : A \rightarrow B}{\Gamma \vDash^* NM : B}$	Let $\langle \gamma, \delta \rangle \in \mathcal{X}^*\{\Gamma\}$. By definition, $\llbracket NM \rrbracket_{\mathcal{R}\gamma}^c = \llbracket N \rrbracket_{\mathcal{R}\gamma}^c \Vdash^R \lambda f. \llbracket M \rrbracket_{\mathcal{R}\gamma}^c \Vdash^R f$, and $\llbracket NM \rrbracket_{\mathcal{M}\delta}^c = \llbracket N \rrbracket_{\mathcal{M}\delta}^c \Vdash \lambda g. \llbracket M \rrbracket_{\mathcal{M}\delta}^c \Vdash g$. By the first induction hypothesis, $\langle \llbracket M \rrbracket_{\mathcal{R}\gamma}^c, \llbracket M \rrbracket_{\mathcal{M}\delta}^c \rangle \in C^*\{A\}$.
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So by Lemma C.6, if $\langle f, g \rangle \in \mathcal{V}^*\{A \rightarrow B\}$ then $\langle \llbracket M \rrbracket_{\mathcal{R}\gamma}^c \Vdash^R f, \llbracket M \rrbracket_{\mathcal{M}\delta}^c \Vdash g \rangle \in C^*\{B\}$. But this is exactly the definition of $\langle \lambda f. \llbracket M \rrbracket_{\mathcal{R}\gamma}^c \Vdash^R f, \lambda g. \llbracket M \rrbracket_{\mathcal{M}\delta}^c \Vdash g \rangle \in \mathcal{V}^*\{(A \rightarrow B) \rightarrow B\}$.

By the second induction hypothesis, $\langle \llbracket N \rrbracket_{\mathcal{R}\gamma}^c, \llbracket N \rrbracket_{\mathcal{M}\delta}^c \rangle \in C^*\{A \rightarrow B\}$. Using Lemma C.6 again, we have $\langle \llbracket N \rrbracket_{\mathcal{R}\gamma}^c \Vdash^R \lambda f. \llbracket M \rrbracket_{\mathcal{R}\gamma}^c \Vdash^R f, \llbracket N \rrbracket_{\mathcal{M}\delta}^c \Vdash \lambda g. \llbracket M \rrbracket_{\mathcal{M}\delta}^c \Vdash g \rangle \in C^*\{B\}$, as required.

The other cases follow by similar reasoning with Lemmas C.5 and C.6, where in the cases of the effects we also use the respective Lemmas C.7 and C.8. \square

The proof of soundness concludes by using Propositions C.4 and C.9:

PROOF OF SOUNDNESS. We have $\langle T, \mu \rangle \llbracket \xi \rrbracket \omega \cdot V \in \llbracket M \rrbracket_{\mathcal{R}}^c$ by [Proposition C.4](#) since M is of ground type. Therefore, [Proposition C.9](#) implies that $\inf_{\mu} T \llbracket \langle \mu, \mu \rangle \xi \rrbracket \omega \cdot V \in \llbracket M \rrbracket_{\mathcal{M}}^c$. Thanks to the extra conclusions of [Proposition C.4](#), $\inf_{\mu} T \llbracket \langle \mu, \mu' \rangle \rrbracket \omega \cdot V \in \llbracket M \rrbracket_{\mathcal{M}}^c$ by iteratively mumble-rewriting. \square

D Proof of Adequacy

The proof of adequacy starts with the [Fundamental Lemma](#), stating that \mathcal{M} -traces correspond to interrupted executions. The main reason behind this fact is simple: \mathfrak{c} -rewrites preserve this correspondence. That is:

LEMMA D.1. *If $M : \tau : V$ and $\tau \xrightarrow{x} \pi$ for $x \in \mathfrak{c}$, then $M : \pi : V$.*

PROOF. We split to the different $x \in \mathfrak{c}$ cases:

St Add a transition that doesn't change the configuration.

Mu Meld adjacent transitions with equal configurations at the boundary.

Fw Append an ADV step to the final transition.

Rw Prepend an ADV step to the initial transition. \square

Logical relation. We mutually define, indexed over type A , sets $\mathcal{V}\{A\}$ of closed values of type A and sets $\mathcal{C}\{A\}$ of closed terms of type A :

$$\begin{aligned} \mathcal{V}\{A \rightarrow B\} &:= \{\lambda a. M \mid \forall V \in \mathcal{V}\{A\}. M[a \mapsto V] \in \mathcal{C}\{B\}\} \\ \mathcal{V}\{A_1 * \dots * A_n\} &:= \{\langle V_1, \dots, V_n \rangle \mid \forall i. V_i \in \mathcal{V}\{A_i\}\} \\ \mathcal{V}\{\{t_1 \text{ of } A_1 \mid \dots \mid t_n \text{ of } A_n\}\} &:= \bigcup_i \{t_i V \mid V \in \mathcal{V}\{A_i\}\} \\ \mathcal{C}\{A\} &:= \{M \in \cdot \vdash A \mid \forall \tau \in \llbracket M \rrbracket_{\mathcal{M}}^c \exists V \in \mathcal{V}\{A\}. M : \tau : V\} \end{aligned}$$

In regards to open terms, for every typing context Γ we define

$$\mathcal{X}\{\Gamma\} := \{\Theta \in \text{Sub}_{\Gamma} \mid \forall (a : A) \in \Gamma. \Theta_a \in \mathcal{V}\{A\}\}$$

and define $\Gamma \vDash M : A$ for $\Gamma \vdash M : A$ as $\forall \Theta \in \mathcal{X}\{\Gamma\}. \Theta M \in \mathcal{C}\{A\}$.

THEOREM D.2 (FUNDAMENTAL LEMMA). *If $\Gamma \vdash M : A$, then $\Gamma \vDash M : A$.*

We devote lemmas to inductive cases of the [Fundamental Lemma](#)'s proof.

LEMMA D.3. *If $\tau := \alpha \llbracket \xi \rrbracket \omega \cdot \langle \rangle \in \llbracket \text{store}_{\ell, v} \rrbracket_{\mathcal{M}}$, then $\ell := v : \tau : \langle \rangle$.*

PROOF. W.l.o.g. $\tau \in \llbracket \text{store}_{\ell, v} \rrbracket_{\mathcal{G}}$, because the general case then follows from [Lemma D.1](#).

Thus, the interrupted execution is just a single STORE step. Indeed, the states $\langle \dot{\alpha}, \xi, \mathfrak{o} \rangle$ and $\langle \dot{\omega}, \xi, \mathfrak{c} \rangle$ match those in STORE's conclusion. The conditions of STORE are met thanks to τ being a trace, e.g. the segment of the stored message being unoccupied due to $\xi.c$ being well-formed. \square

LEMMA D.4. *If $\tau := \alpha \llbracket \xi \rrbracket \omega \cdot v \in \llbracket \text{rmw}_{\ell, \varphi, \vec{w}} \rrbracket_{\mathcal{M}}$, then $\text{rmw}_{\varphi}(\ell; \vec{w}) : \tau : v$.*

PROOF. W.l.o.g. $\tau \in \llbracket \text{rmw}_{\ell, \varphi, \vec{w}} \rrbracket_{\mathcal{G}}$, because the general case then follows from [Lemma D.1](#).

Thus, the interrupted execution is a single READONLY step (if $\tau \in \llbracket \text{rmw}_{\ell, \varphi, \vec{w}}^{\text{RO}} \rrbracket_{\mathcal{G}}$) or a single RMW step (if $\tau \in \llbracket \text{rmw}_{\ell, \varphi, \vec{w}}^{\text{RMW}} \rrbracket_{\mathcal{G}}$), in which the initial view points to the loaded message. \square

LEMMA D.5. *If $\xi \in \xi_1 \parallel \xi_2$ and $M_i : \alpha_i \llbracket \xi_i \rrbracket \omega_i \cdot r_i : V_i$, then*

$$M_1 \parallel M_2 : \inf_{\xi, \mathfrak{o}} \{\alpha_1, \alpha_2\} \llbracket \xi \rrbracket \sup_{\xi, \mathfrak{c}} \{\omega_1, \omega_2\} \cdot \langle r_1, r_2 \rangle : \langle V_1, V_2 \rangle$$

PROOF. We obtain the required interrupted execution by interleaving the interrupted executions following the interleaving that generated ξ from ξ_1 and ξ_2 with the following modifications:

- prepending ADV—lifted suitably with PARLEFT/PARRIGHT—to the first transition taken by each side;
- prepending PARINIT to the first transition;
- appending PARFIN to the last transition (since $\sup_{\xi, c} \{\omega_1, \omega_2\} = \omega_1 \sqcup \omega_2$). \square

PROOF OF THE FUNDAMENTAL LEMMA. By induction on the typing derivation $\Gamma \vdash M : A$.

$\frac{(a : A) \in \Gamma}{\Gamma \vDash a : A}$	Let $\Theta \in \text{Sub}_\Gamma$ be such that $\forall (a : A) \in \Gamma. \Theta_a \in \mathcal{V}\{A\}$. To show $\Theta a = \Theta_a \in C\{A\}$, let $\tau \in \llbracket \Theta a \rrbracket_{\mathcal{M}}^c = \text{return}^M \llbracket \Theta a \rrbracket_{\mathcal{M}}^v$. Suffice it we show $\Theta_a : \tau : \Theta_a$. Using Lemma D.1, we restrict to the case of $\tau \in \text{return}^{\mathcal{G}} \llbracket \Theta a \rrbracket_{\mathcal{M}}^v$. So τ is of the form $\kappa \llbracket \mu, \mu \rrbracket \kappa : \llbracket \Theta a \rrbracket_{\mathcal{M}}^v$. The required interrupted execution is obtained by taking no steps in its only transition.
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$\frac{\Gamma, a : A \vDash M : B}{\Gamma \vDash \lambda a : A. M : A \rightarrow B}$	Let $\Theta \in \text{Sub}_\Gamma$ be such that $\forall (a : A) \in \Gamma. \Theta_a \in \mathcal{V}\{A\}$. Denote by K the term $\Theta(\lambda a : A. M) = \lambda a : A. \Theta_{\{a\}} M$. To show that $K \in C\{A\}$, let $\tau \in \llbracket K \rrbracket_{\mathcal{M}}^c = \text{return}^M \llbracket K \rrbracket_{\mathcal{M}}^v$. Like the previous case, we can show $K : \tau : K$ using Lemma D.1. This is sufficient, because $K \in \mathcal{V}\{A \rightarrow B\}$. Indeed, for $V \in \mathcal{V}\{A\}$, denoting by $\Theta[V/a]$ the substitution equal to Θ except at V which it maps to a , by the induction hypothesis we have $(\Theta_{\{a\}} M)[a \mapsto V] = \Theta[V/a]M \in C\{B\}$.
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$\frac{\Gamma \vDash M : A \quad \Gamma \vDash N : A \rightarrow B}{\Gamma \vDash NM : B}$	Let $\Theta \in \text{Sub}_\Gamma$ be such that $\forall (a : A) \in \Gamma. \Theta_a \in \mathcal{V}\{A\}$. To show that $\Theta(NM) = (\Theta N)(\Theta M) \in C\{A\}$ holds, let $\tau \in \llbracket (\Theta N)(\Theta M) \rrbracket_{\mathcal{M}}^c = \llbracket \Theta N \rrbracket_{\mathcal{M}}^c \gg^M \lambda f. \llbracket \Theta M \rrbracket_{\mathcal{M}}^c \gg^M f$. Unfolding:
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$$\exists \tau_1 := \alpha_1 \llbracket \xi_1 \rrbracket \omega_1 \cdot f \in \llbracket \Theta N \rrbracket_{\mathcal{M}}^c, \tau_2 := \alpha_2 \llbracket \xi_2 \rrbracket \omega_2 \cdot r \in \llbracket \Theta M \rrbracket_{\mathcal{M}}^c, \tau_3 := \alpha_3 \llbracket \xi_3 \rrbracket \omega_3 \cdot s \in fr.$$

$$\omega_1 \leq \alpha_2 \wedge \omega_2 \leq \alpha_3 \wedge \tau = \alpha_1 \llbracket \xi_1 \xi_2 \xi_3 \rrbracket \omega_3 \cdot s \in \llbracket (\Theta N)(\Theta M) \rrbracket_{\mathcal{M}}^c$$

By the induction hypotheses, there exists $\lambda a : A. K \in \mathcal{V}\{A \rightarrow B\}$ such that $\Theta N : \tau_1 : \lambda a : A. K$, and there exists $V \in \mathcal{V}\{A\}$ such that $\Theta M : \tau_2 : V$. So $K[a \mapsto V] \in C\{B\}$, and using the Substitution Lemma: $fr = \llbracket \lambda a : A. K \rrbracket_{\mathcal{M}}^v \llbracket V \rrbracket_{\mathcal{M}}^v = \llbracket K[a \mapsto V] \rrbracket_{\mathcal{M}}^c$. Therefore, there exists $W \in \mathcal{V}\{B\}$ such that $K[a \mapsto V] : \tau_3 : W$. We transform to sequence the interrupted executions into one that corresponds to τ as follows: we lift the one corresponding to τ_1 using APPELEFT to the context $[-](\Theta M)$, we lift the one corresponding to τ_2 using APPRIGHT to the context $(\lambda a : A. K)[-]$, and we prepend APP to the one corresponding to τ_3 . By using ADV to compensate for the difference in delimiting views, we get $(\Theta N)(\Theta M) : \tau : W$.

$\frac{\Gamma \vDash M : \text{Loc} \quad \Gamma \vDash N : \text{Val}}{\Gamma \vDash M := N : 1}$	$\frac{\varphi \in \text{RMW}_n \quad \Gamma \vDash M : \text{Loc} \quad \Gamma \vDash N : \text{Val}^n}{\Gamma \vDash \text{rmw}_\varphi(M; N) : \text{Val}}$	Binds unfold like in the case above. The rest is handled
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using Lemma D.3 and Lemma D.4 respectively.

$\frac{\Gamma \vDash M_1 : A_1 \quad \Gamma \vDash M_2 : A_2}{\Gamma \vDash M_1 \parallel M_2 : (A_1 * A_2)}$	Let $\Theta \in \text{Sub}_\Gamma$ be such that $\forall (a : A) \in \Gamma. \Theta_a \in \mathcal{V}\{A\}$. Thanks to Lemma D.1, to show $\Theta(M_1 \parallel M_2) = \Theta M_1 \parallel \Theta M_2 \in C\{A_1 * A_2\}$, it is sufficient to consider $\tau \in \llbracket \Theta M_1 \rrbracket_{\mathcal{M}}^c \parallel \llbracket \Theta M_2 \rrbracket_{\mathcal{M}}^c$. Unfolding the concurrent construct, there exist $\tau_i := \alpha_i \llbracket \xi_i \rrbracket \omega_i \cdot r_i \in \llbracket \Theta M_i \rrbracket_{\mathcal{M}}^c$ and $\xi \in \xi_1 \parallel \xi_2$ such that $\tau := \inf_{\xi, o} \{\alpha_1, \alpha_2\} \llbracket \xi \rrbracket \sup_{\xi, c} \{\omega_1, \omega_2\} \cdot \langle r_1, r_2 \rangle$. By induction hypotheses, there exist $V_i \in \mathcal{V}\{A_i\}$ such that $\Theta M_i : \tau_i : V_i$. So $\langle V_1, V_2 \rangle \in \mathcal{V}\{A_1 * A_2\}$, and by Lemma D.5, $\Theta M_1 \parallel \Theta M_2 : \tau : \langle V_1, V_2 \rangle$.
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The other cases are treated similarly. \square

To prove the Evaluation Lemma we observe that \mathbf{a} -rewrites preserve evaluation:

Table 2. Validated transformations. Needed rules used from \mathbf{a} appear above the symbol \rightarrow .

Generalized Sequencing (let $a = M_1$ in M_2) \parallel (let $b = N_1$ in N_2) \rightarrow match $M_1 \parallel N_1$ with $\langle a, b \rangle$. $M_2 \parallel N_2$ Sequencing $M \parallel N \rightarrow \langle M, N \rangle$	Symmetric-Monoidal Laws, e.g. $M \parallel N \rightarrow$ match $N \parallel M$ with $\langle b, a \rangle$. $\langle a, b \rangle$
Irrelevant Read Introduction $\langle \rangle \rightarrow \ell? ; \langle \rangle$ Irrelevant Read Elimination $\ell? ; \langle \rangle \rightarrow \langle \rangle$	Write-RMW Elimination $\ell := v ; \mathbf{rmw}_\varphi(\ell; \vec{w}) \xrightarrow{\text{Ab}} \ell := \varphi_{\vec{w}}^{\text{id}} v ; v$ $\ell := v ; \ell? \rightarrow \ell := v ; v$ $\ell := v ; \text{CAS}(\ell, v, u) \xrightarrow{\text{Ab}} \ell := u ; v$ $\ell := v ; \text{CAS}(\ell, w, u) \rightarrow \ell := v ; v \quad (v \neq w)$ $\ell := v ; \text{FAA}(\ell, w) \xrightarrow{\text{Ab}} \ell := v + w ; v$ $\ell := v ; \text{XCHG}(\ell, w) \xrightarrow{\text{Ab}} \ell := w ; v$
Write-Write Elimination $\ell := w ; \ell := v \xrightarrow{\text{Ab}} \ell := v$	RMW-Write Elimination (dom $\psi_{\vec{w}} \supseteq$ dom $\varphi_{\vec{u}}$) let $a = \mathbf{rmw}_\varphi(\ell; \vec{u})$ in match $(\psi_{\vec{w}}) a$ with $\{l_{\perp} _ a \mid l_{\top} v. \ell := v ; a\} \xrightarrow{\text{Ab}} \mathbf{rmw}_\psi(\ell; \vec{w})$ let $a = \ell?$ in (if $a = v$ then $\ell := w$ else $\langle \rangle$) ; $a \rightarrow \text{CAS}(\ell, v, w)$ let $a = \ell?$ in $\ell := a + v ; a \rightarrow \text{FAA}(\ell, v)$ let $a = \ell?$ in $\ell := v ; a \rightarrow \text{XCHG}(\ell, v)$
Write-Read Deorder ($\ell \neq \ell'$) $\langle \ell := v, \ell'? \rangle \xrightarrow{\text{Ti}} \ell := v \parallel \ell'?$	RMW Expansion ($\varphi_{\vec{v}} \leq \psi_{\vec{w}}$) $\mathbf{rmw}_\varphi(\ell; \vec{v}) \xrightarrow{\text{Di}} \mathbf{rmw}_\psi(\ell; \vec{w})$ $\ell? \xrightarrow{\text{Di}} \text{CAS}(\ell, v, v)$ $\text{CAS}(\ell, v, v) \xrightarrow{\text{Di}} \text{FAA}(\ell, 0)$
Atomic Store $\ell := v \rightarrow \text{XCHG}(\ell, v) ; \langle \rangle$	RMW-RMW Elimination $\langle \mathbf{rmw}_\varphi(\ell; \vec{v}), \mathbf{rmw}_\psi(\ell; \vec{w}) \rangle \xrightarrow{\text{Ab}} \text{let } a = \mathbf{rmw}_\zeta(\ell; \vec{u}) \text{ in } \langle a, \varphi_{\vec{v}}^{\text{id}} a \rangle$ ($\zeta_{\vec{u}} = \psi_{\vec{w}} \circ^{\text{id}} \varphi_{\vec{v}}$) $\langle \ell?, \ell? \rangle \rightarrow \text{let } a = \ell? \text{ in } \langle a, a \rangle$ $\langle \text{FAA}(\ell, v), \text{FAA}(\ell, w) \rangle \rightarrow \text{let } a = \text{FAA}(\ell, v + w) \text{ in } \langle a, a + v \rangle$ $\langle \ell?, \text{CAS}(\ell, v, w) \rangle \rightarrow \text{let } a = \text{CAS}(\ell, v, w) \text{ in } \langle a, a \rangle$ $\langle \text{XCHG}(\ell, w), \ell? \rangle \rightarrow \text{let } a = \text{XCHG}(\ell, w) \text{ in } \langle a, w \rangle$

LEMMA D.6. For $x \in \mathbf{a}$, if $\pi \xrightarrow{x} \tau$ and $\langle \pi.\dot{\text{i}}\text{vw}, \pi.\text{ch.o} \rangle, M \Downarrow \pi.v1$, then $\langle \tau.\dot{\text{i}}\text{vw}, \tau.\text{ch.o} \rangle, M \Downarrow \tau.v1$.

PROOF. In any case, $\tau.v1 = \pi.v1$. If $x = \text{Ti}$ or $x = \text{Ab}$, then also $\tau.\dot{\text{i}}\text{vw} = \pi.\dot{\text{i}}\text{vw}$ and $\tau.\text{ch.o} = \pi.\text{ch.o}$, so the claim holds trivially. Only $x = \text{Di}$ remains, where $\pi.\dot{\text{i}}\text{vw} = \tau.\dot{\text{i}}\text{vw} [\uparrow \epsilon]$ and $\pi.\text{ch.o} = \tau.\text{ch.o} [\uparrow \epsilon]$ for some message ϵ . We obtain the required execution underlying $\langle \tau.\dot{\text{i}}\text{vw}, \tau.\text{ch.o} \rangle, M \Downarrow \tau.v1$ from the one that underlies $\langle \pi.\dot{\text{i}}\text{vw}, \pi.\text{ch.o} \rangle, M \Downarrow \pi.v1$ by replacing the timestamp $\epsilon.i$ with $\epsilon.t$ everywhere. We elide the straightforward simulation argument that justifies this. \square

PROOF OF THE EVALUATION LEMMA. Denote $\tau := \alpha \langle \underline{\mu}, \rho \rangle \omega . r \in \llbracket M \rrbracket^c$. By **Retroactive Closure**, $\llbracket M \rrbracket^c = \llbracket M \rrbracket_{\mathcal{M}}^c$. So there exists $\pi \in \llbracket M \rrbracket_{\mathcal{M}}^c$ such that $\pi \xrightarrow{\mathbf{a}} \tau$. Proceed by induction on the number of \mathbf{a} -rewrites. If none, $\tau = \pi \in \llbracket M \rrbracket_{\mathcal{M}}^c$, so by the **Fundamental Lemma**, $M : \tau : V$ for some V . Since M is of ground type, so is $V = \llbracket V \rrbracket_{\mathcal{M}}^v = r$, and thus $\langle \dot{\alpha}, \mu \rangle, M \rightsquigarrow_{\text{RA}_{\leq}}^* \langle \dot{\omega}, \rho \rangle, r$, so $\langle \dot{\alpha}, \mu \rangle, M \Downarrow r$.

Otherwise, we have $\pi \xrightarrow{\mathbf{a}} \tau' \xrightarrow{x} \tau$ where $x \in \mathbf{a}$ and $\langle \tau'.\dot{\text{i}}\text{vw}, \tau'.\text{ch.o} \rangle, M \Downarrow \tau'.v1$ by the induction hypothesis. We replace τ' with τ using **Lemma D.6**, as required. \square

E Validating Transformations

Table 2 lists various transformations $M \rightarrow N$ that can be proved this way, organized such that the general pattern appears first, followed by specific instantiations and corollaries.

For handling the RMW modifiers, we use additional notations. For modifiers $\Phi, \Psi \in \text{Val} \rightarrow \text{Val}$:

- The *domain of definition* of Φ is $\text{dom } \Phi := \{v \in \text{Val} \mid \Phi v \neq \perp\}$.

- We say that Ψ is an *expansion* of Φ , denoted by $\Phi \leq \Psi$, if $\Phi v \neq \Psi v$ occurs only when $\Phi v = \perp$ and $\Psi v = v$. Intuitively, this means that Φ and Ψ are the same, except that in some cases in which Φ reads and does not write, Ψ atomically reads and rewrites the read value.
- We denote by Φ^{id} the unique expansion of Φ that is total: $\Phi^{\text{id}}v := \text{if } \Phi v = \perp \text{ then } v \text{ else } \Phi v$. Intuitively, Φ^{id} rewrites the read value whenever Φ reads but does not write.
- We let $(\Psi \circ^{\text{id}} \Phi) v := \text{if } \Phi v = \perp \text{ then } \Psi v \text{ else } \Psi^{\text{id}}(\Phi v)$. Intuitively, $(\Psi \circ^{\text{id}} \Phi)$ composes the modification of Φ followed by the modification of Ψ , only failing if both do.

Moreover, some optimizations involving modifiers assume the language can express corresponding constructs. For example, the Write-RMW Elimination instantiated with $\varphi = \text{faa}$ requires addition (+), and the RMW-Write Elimination instantiated with $\varphi = \text{cas}$ requires branching on value comparison (**if** – = – **then** – **else** –). Under this assumption, for every primitive modifier φ and every tuple \vec{v} of length $\varphi.\text{ar}$, both $\varphi_{\vec{v}}$ and $\varphi_{\vec{v}}^{\text{id}}$ are represented by closed, pure (effect-free) terms, of type $\text{Val} \rightarrow \{\iota_{\perp} \text{ of } \mathbf{1} \mid \iota_{\top} \text{ of } \text{Val}\}$ and $\text{Val} \rightarrow \text{Val}$ respectively. These are used implicitly in Table 2.

In the following we prove selected results from Table 2. We explicitly mention the use of α -rewrites, but often leave uses of τ -rewrites implicit. For convenience, we denote $\alpha[\xi\eta]\omega \cdot s := (\alpha[\xi]\kappa \cdot r) \gg (\sigma[\eta]\omega \cdot s)$, and we say this trace resulted from binding the first with the second.

PROPOSITION E.1. *If $\Gamma \vdash M_1 : A_1; \Gamma \vdash N_1 : B_1; \Gamma, a : A' \vdash M_2 : A_2$; and $\Gamma, b : B' \vdash N_2 : B_2$:*

$$\llbracket (\text{let } a = M_1 \text{ in } M_2) \parallel (\text{let } b = N_1 \text{ in } N_2) \rrbracket^c \supseteq \llbracket \text{match } M_1 \parallel N_1 \text{ with } \langle a, b \rangle. M_2 \parallel N_2 \rrbracket^c$$

(Formally, in the right denotation we use $\Gamma, a : A', b : B' \vdash M_2 : A_2$ and $\Gamma, a : A', b : B' \vdash N_2 : B_2$.)

PROOF. Let $\gamma \in \llbracket \Gamma \rrbracket$ and denote the left and right sets of the required containment by P and Q respectively. Thus we require $P \supseteq Q$.

Let $\varrho \in Q$. By **Deferral of Closure**, ϱ is in the **ca**-closure of:

$$Q' := \llbracket M_1 \rrbracket^c \gamma \parallel \llbracket N_1 \rrbracket^c \gamma \gg^{\mathcal{G}} \lambda \langle \gamma_a, \gamma_b \rangle. \llbracket M_2 \rrbracket^c (\gamma_c)_{(c:C) \in \Gamma, a:A'} \parallel \llbracket N_2 \rrbracket^c (\gamma_c)_{(c:C) \in \Gamma, b:B'}$$

So there exists $\varrho' \in Q'$ that **ca**-rewrites to ϱ . This ϱ' results from binding two traces. On the left, $\inf_{\xi, \sigma} \{\alpha_1, \kappa_1\} [\xi] \omega_1 \sqcup \sigma_1 \cdot \langle r_1, s_1 \rangle$, where:

$$\tau_1 := \alpha_1 [\xi_1] \omega_1 \cdot r_1 \in \llbracket M_1 \rrbracket^c \gamma; \quad \pi_1 := \kappa_1 [\eta_1] \sigma_1 \cdot s_1 \in \llbracket N_1 \rrbracket^c \gamma; \quad \xi \in \xi_1 \parallel \eta_1$$

On the right, $\inf_{\eta, \sigma} \{\alpha_2, \kappa_2\} [\eta] \omega_2 \sqcup \sigma_2 \cdot \langle r_2, s_2 \rangle$, where, setting $\gamma_a := r_1$ and $\gamma_b := s_1$:

$$\tau_2 := \alpha_2 [\xi_2] \omega_2 \cdot r_2 \in \llbracket M_2 \rrbracket^c (\gamma_c)_{(c:C) \in \Gamma, a:A'}; \quad \pi_2 := \kappa_2 [\eta_2] \sigma_2 \cdot s_2 \in \llbracket N_2 \rrbracket^c (\gamma_c)_{(c:C) \in \Gamma, b:B'}; \quad \eta \in \xi_2 \parallel \eta_2$$

The binding implies that $\omega_1 \sqcup \sigma_1 \leq \inf_{\eta, \sigma} \{\alpha_2, \kappa_2\}$. In particular, $\omega_1 \leq \alpha_2$ and $\sigma_1 \leq \kappa_2$. Therefore, $\tau_1 \gg \tau_2 = \alpha_1 [\xi_1 \xi_2] \omega_2 \cdot r_2 \in \llbracket \text{let } a = M_1 \text{ in } M_2 \rrbracket^c$ and $\pi_1 \gg \pi_2 = \kappa_1 [\eta_1 \eta_2] \sigma_2 \cdot s_2 \in \llbracket \text{let } b = N_1 \text{ in } N_2 \rrbracket^c$. Since $\xi\eta \in \xi_1 \xi_2 \parallel \eta_1 \eta_2$ and $(\xi\eta) \cdot \sigma = \xi \cdot \sigma$, we obtain ϱ' by interleaving these. Therefore, $\varrho' \in P$. Since P is **ca**-closed, $\varrho \in P$. \square

In the rest of this section we show results of the form $\llbracket M \rrbracket^c \supseteq \llbracket N \rrbracket_{\mathcal{G}}^c$. Each entails $\llbracket M \rrbracket^c \supseteq \llbracket N \rrbracket^c$ by **Deferral of Closure**, thus justifying $M \rightsquigarrow N$ in Table 2.

PROPOSITION E.2. $\llbracket \langle \rangle \rrbracket^c \supseteq \llbracket \ell? ; \langle \rangle \rrbracket_{\mathcal{G}}^c$.

PROOF. Let $\tau \in \llbracket \ell? ; \langle \rangle \rrbracket_{\mathcal{G}}^c$. Unfolding definitions:

$$\llbracket \ell? ; \langle \rangle \rrbracket_{\mathcal{G}}^c := \llbracket \text{rmw}_{\ell, \lambda_{\perp}} \rrbracket_{\mathcal{G}} \gg^{\mathcal{G}} \lambda_{\perp}. \text{return}^{\mathcal{G}} \langle \rangle = \{ \alpha \langle \mu, \mu \rangle \langle \rho, \rho \rangle \omega \cdot \langle \rangle \in \text{Trace1} \mid \exists v \in \mu_{\ell}. \alpha \mapsto v \}$$

Therefore, we have the form $\tau = \alpha \langle \mu, \mu \rangle \langle \rho, \rho \rangle \omega \cdot \langle \rangle$. From $\alpha \langle \mu, \mu \rangle \alpha \cdot \langle \rangle \in \llbracket \langle \rangle \rrbracket_{\mathcal{G}}^c$, we obtain $\tau \in \llbracket \langle \rangle \rrbracket^c$ by stuttering (St) and forwarding (Fw). \square

PROPOSITION E.3. $\llbracket \ell := v \rrbracket^c \supseteq \llbracket \text{XCHG}(\ell, v) ; \langle \rangle \rrbracket_{\mathcal{G}}^c$.

PROOF. By taking the traces in $\llbracket \ell := v \rrbracket_{\mathcal{G}}^c$ in which the newly added message dovetails with the previous message in memory by choosing the initial timestamp appropriately. \square

PROPOSITION E.4. Assuming $\ell \neq \ell'$, $\llbracket \langle \ell := v, \ell' ? \rangle \rrbracket^c \supseteq \llbracket \ell := v \parallel \ell' ? \rrbracket_{\mathcal{G}}^c$.

PROOF. The elements of $\llbracket \text{store}_{\ell, v} \rrbracket_{\mathcal{G}} \parallel \llbracket \text{rmw}_{\ell', \lambda_{\perp}} \rrbracket_{\mathcal{G}}$ are formed by interleaving a store

$$\kappa \left[\langle \mu, \mu \uplus \{ \ell : v @ (q, t) \langle \kappa[\ell \mapsto t] \rangle \} \rangle \kappa[\ell \mapsto t] \right] \cdot \langle \rangle \in \llbracket \text{store}_{\ell, v} \rrbracket_{\mathcal{G}}$$

with a load $\sigma \left[\langle \rho, \rho \rangle \right] \sigma \cdot \cdot w \in \llbracket \text{rmw}_{\ell', \lambda_{\perp}} \rrbracket_{\mathcal{G}}$. Depending on the order, this results in one of:

$$\inf_{\mu} \{ \kappa, \sigma \} \left[\langle \mu, \mu \uplus \{ \ell : v @ (q, t) \langle \kappa[\ell \mapsto t] \rangle \} \rangle \langle \rho, \rho \rangle \right] \kappa[\ell \mapsto t] \sqcup \sigma \cdot \cdot \langle \langle \rangle, w \rangle \quad (\text{WR})$$

$$\inf_{\rho} \{ \kappa, \sigma \} \left[\langle \rho, \rho \rangle \langle \mu, \mu \uplus \{ \ell : v @ (q, t) \langle \kappa[\ell \mapsto t] \rangle \} \rangle \right] \kappa[\ell \mapsto t] \sqcup \sigma \cdot \cdot \langle \langle \rangle, w \rangle \quad (\text{RW})$$

We prove separately that these interleavings are in $\llbracket \langle \ell := v, \ell' ? \rangle \rrbracket^c$.

- (WR): Denoting $\theta := (\rho \setminus \{ \ell : v @ (q, t) \langle \kappa[\ell \mapsto t] \rangle \}) \uplus \{ \ell : v @ (q, t) \langle \alpha[\ell \mapsto t] \rangle \}$ where $\alpha := \inf_{\mu} \{ \kappa, \sigma \}$:

$$\alpha \left[\langle \mu, \mu \uplus \{ \ell : v @ (q, t) \langle \alpha[\ell \mapsto t] \rangle \} \rangle \right] \alpha[\ell \mapsto t] \cdot \langle \rangle \in \llbracket \text{store}_{\ell, v} \rrbracket_{\mathcal{G}} \\ \alpha[\ell \mapsto t] \sqcup \sigma \left[\langle \theta, \theta \rangle \right] \alpha[\ell \mapsto t] \sqcup \sigma \cdot \cdot w \in \llbracket \text{rmw}_{\ell', \lambda_{\perp}} \rrbracket_{\mathcal{G}}$$

By forwarding (Fw) after binding we obtain:

$$\alpha \left[\langle \mu, \mu \uplus \{ \ell : v @ (q, t) \langle \alpha[\ell \mapsto t] \rangle \} \rangle \langle \theta, \theta \rangle \right] \kappa[\ell \mapsto t] \sqcup \sigma \cdot \cdot \langle \langle \rangle, w \rangle \in \llbracket \langle \ell := v, \ell' ? \rangle \rrbracket^c$$

All that remains is to tighten (Ti) $\ell : v @ (q, t) \langle \alpha[\ell \mapsto t] \rangle$ to $\ell : v @ (q, t) \langle \kappa[\ell \mapsto t] \rangle$.

- (RW): Using the result for (WR), with $\theta := \mu \uplus \{ \ell : v @ (q, t) \langle \kappa[\ell \mapsto t] \rangle \}$:

$$\inf_{\mu} \{ \kappa, \sigma \} \left[\langle \mu, \theta \rangle \langle \theta, \theta \rangle \right] \kappa[\ell \mapsto t] \sqcup \sigma \cdot \cdot \langle \langle \rangle, w \rangle \in \llbracket \langle \ell := v, \ell' ? \rangle \rrbracket^c$$

We can rewind (Rw) $\inf_{\mu} \{ \kappa, \sigma \}$ to $\inf_{\rho} \{ \kappa, \sigma \}$, since $\rho \subseteq \mu$. By mumbling (Mu) and stuttering (St), we are done. \square

PROPOSITION E.5. Assuming $\varphi_{\vec{v}} \leq \psi_{\vec{w}}$, $\llbracket \text{rmw}_{\varphi}(\ell; \vec{v}) \rrbracket^c \supseteq \llbracket \text{rmw}_{\psi}(\ell; \vec{w}) \rrbracket_{\mathcal{G}}^c$.

PROOF. Let $\tau \in \llbracket \text{rmw}_{\psi}(\ell; \vec{w}) \rrbracket_{\mathcal{G}}^c$, resulting from loading a value u from a message v . If $\varphi_{\vec{v}} u = \psi_{\vec{w}} u$, then obviously $\tau \in \llbracket \text{rmw}_{\varphi}(\ell; \vec{v}) \rrbracket_{\mathcal{G}}^c$. Otherwise, by assumption $\varphi_{\vec{v}} u = \perp$ and $\psi_{\vec{w}} u = u$. So we have $\tau = \kappa \left[\langle \mu, \mu \uplus \{ \epsilon \} \rangle \right] \kappa[\ell \mapsto t] \cdot \cdot u$, with $\epsilon := \ell : u @ (\kappa_{\ell}, t) \langle \kappa[\ell \mapsto t] \rangle$, where $v \in \mu_{\ell}$ and $v.t = \kappa_{\ell}$. In the left denotation, by loading $v \uparrow \epsilon$ we have $\kappa \uparrow \epsilon \left[\langle \mu \uparrow \epsilon, \mu \uparrow \epsilon \rangle \right] \kappa \uparrow \epsilon \cdot \cdot u = \left(\kappa \left[\langle \mu, \mu \rangle \right] \kappa[\ell \mapsto t] \cdot \cdot u \right) \uparrow \epsilon$. By diluting (Di) we obtain τ . \square

PROPOSITION E.6. Assuming $\forall v' \in \text{Val}. \zeta_{\vec{u}} v' = (\psi_{\vec{w}v'} \circ^{\text{id}} \varphi_{\vec{v}}) v'$,

$$\llbracket \text{let } a = \text{rmw}_{\varphi}(\ell; \vec{v}) \text{ in } \langle a, \text{rmw}_{\psi}(\ell; \vec{w}a) \rangle \rrbracket^c \supseteq \llbracket \text{let } a = \text{rmw}_{\zeta}(\ell; \vec{u}) \text{ in } \langle a, \varphi_{\vec{v}}^{\text{id}} a \rangle \rrbracket_{\mathcal{G}}^c$$

PROOF. Let $\pi \in \llbracket \text{let } a = \text{rmw}_{\zeta}(\ell; \vec{u}) \text{ in } \langle a, \varphi_{\vec{v}}^{\text{id}} a \rangle \rrbracket_{\mathcal{G}}^c$. So a $\tau' := \alpha \left[\langle \mu, \rho \rangle \right] \omega \cdot \cdot v' \in \llbracket \text{rmw}_{\psi}(\ell; \vec{w}) \rrbracket_{\mathcal{G}}^c$ exists due to loading $v \in \mu_{\ell}$ with $v.v1 = v'$, such that $\tau := \alpha \left[\langle \mu, \rho \rangle \right] \omega \cdot \cdot \left\langle v', \varphi_{\vec{v}}^{\text{id}} v' \right\rangle \xrightarrow{\text{St}} \xrightarrow{\text{Fw}} \pi$.

RO. If $\tau' \in \llbracket \text{rmw}_{\psi}^{\text{RO}}(\ell; \vec{w}) \rrbracket_{\mathcal{G}}$, then we have $\tau = \kappa \left[\langle \mu, \mu \rangle \right] \kappa \cdot \cdot \left\langle v', \varphi_{\vec{v}}^{\text{id}} v' \right\rangle$ where $\zeta_{\vec{u}} v' = \perp$ and $v.t = \kappa_{\ell}$.

By assumption, $\varphi_{\vec{v}} v' = \perp$, so $\varphi_{\vec{v}}^{\text{id}} v' = v'$; and $\psi_{\vec{w}v'} v' = \perp$, so by loading v in both RMWs we can obtain τ in the left denotation.

RMW. If $\tau' \in \llbracket \text{rmw}_{\ell, \zeta_{\vec{u}}}^{\text{RMW}} \rrbracket_{\mathcal{G}}$, then we have $\tau = \kappa \langle \mu, \mu \uplus \{ \ell : u' @ (\kappa_{\ell}, t) \langle \kappa[\ell \mapsto t] \rangle \} \rangle \kappa[\ell \mapsto t] \therefore \langle v', \varphi_{\vec{v}}^{\text{id}} v' \rangle$ where $\zeta_{\vec{u}} v' = u'$ and $v.t = \kappa_{\ell}$. The $\varphi_{\vec{v}} v' = \perp$ case is similar to before, where again τ is found in the left denotation by loading v in both RMWs, with the difference that here $\psi_{\vec{w}} v' = u'$, to the second RMW also writes the message $\ell : u' @ (\kappa_{\ell}, t) \langle \kappa[\ell \mapsto t] \rangle$. The $\varphi_{\vec{v}} v' = w'$ case remains, in which $\varphi_{\vec{v}}^{\text{id}} v' = w'$. In the sub-case that $\psi_{\vec{v}} w' = \perp$ we have $w' = u'$, and we find τ in the left denotation by loading v and writing $\ell : u' @ (\kappa_{\ell}, t) \langle \kappa[\ell \mapsto t] \rangle$ in the first RMW, which the second RMW loads. In the sub-case where $\psi_{\vec{v}} w' = u'$, the first RMW writes $\ell : w' @ (\kappa_{\ell}, \frac{\kappa_{\ell} + t}{2}) \langle \kappa[\ell \mapsto \frac{\kappa_{\ell} + t}{2}] \rangle$ instead. For the second RMW we take a trace with initial view $\kappa[\ell \mapsto \frac{\kappa_{\ell} + t}{2}]$, enabling its loading of this new message and writing $\ell : u' @ (\frac{\kappa_{\ell} + t}{2}, t) \langle \kappa[\ell \mapsto t] \rangle$. To find τ in the left denotation we have the latter message absorb (Ab) the former message.

Either way, τ is in $\llbracket \text{let } a = \text{rmw}_{\varphi}(\ell; \vec{v}) \text{ in } \langle a, \text{rmw}_{\psi}(\ell; \vec{w}a) \rangle \rrbracket^c$, and therefore so is π . \square

COROLLARY E.7. Assuming $\zeta_{\vec{u}} = \psi_{\vec{w}} \circ^{\text{id}} \varphi_{\vec{v}}$,

$$\llbracket \langle \text{rmw}_{\varphi}(\ell; \vec{v}), \text{rmw}_{\psi}(\ell; \vec{w}) \rangle \rrbracket^c \supseteq \llbracket \text{let } a = \text{rmw}_{\zeta}(\ell; \vec{u}) \text{ in } \langle a, \varphi_{\vec{v}}^{\text{id}} a \rangle \rrbracket_{\mathcal{G}}^c$$

PROOF. Using a special case of [Proposition E.6](#), where ψ is independent of its final parameter. \square

$$\llbracket \ell := v ; \text{rmw}_{\varphi}(\ell; \vec{w}) \rrbracket^c \supseteq \llbracket \ell := \varphi_{\vec{w}}^{\text{id}} v ; v \rrbracket_{\mathcal{G}}^c.$$

PROOF. Same as the RMW case in the proof of [Proposition E.6](#), except the initial timestamp does not have to equal the timestamp of the loaded message. \square

$$\llbracket \ell := w ; \ell := v \rrbracket^c \supseteq \llbracket \ell := v \rrbracket_{\mathcal{G}}^c.$$

PROOF. Replace the second assignment on the left using [Proposition E.3](#), and follow with [Proposition E.8](#). \square

PROPOSITION E.10. Assuming $\text{dom } \psi_{\vec{w}} \supseteq \text{dom } \varphi_{\vec{u}}$,

$$\llbracket \text{let } a = \text{rmw}_{\varphi}(\ell; \vec{u}) \text{ in match } \psi_{\vec{w}} a \text{ with } \{ \iota_{\perp} _ . a \mid \iota_{\top} v. \ell := v ; a \} \rrbracket^c \supseteq \llbracket \text{rmw}_{\psi}(\ell; \vec{w}) \rrbracket_{\mathcal{G}}^c$$

$$\text{PROOF. Let } \tau \in \llbracket \text{rmw}_{\psi}(\ell; \vec{w}) \rrbracket_{\mathcal{G}}^c = \llbracket \text{rmw}_{\ell, \psi_{\vec{w}}} \rrbracket_{\mathcal{G}} = \llbracket \text{rmw}_{\ell, \psi_{\vec{w}}}^{\text{RO}} \rrbracket_{\mathcal{G}} \cup \llbracket \text{rmw}_{\ell, \psi_{\vec{w}}}^{\text{RMW}} \rrbracket_{\mathcal{G}}.$$

RO. If $\tau \in \llbracket \text{rmw}_{\ell, \psi_{\vec{w}}}^{\text{RO}} \rrbracket_{\mathcal{G}}$, then we have $\tau = \kappa \langle \mu, \mu \rangle \kappa \therefore v.v1$ where $\psi_{\vec{w}}(v.v1) = \perp$, $v \in \mu_{\ell}$, and $v.t = \kappa_{\ell}$. Structurally, we have

$$\llbracket \text{match } (\psi_{\vec{w}}) v.v1 \text{ with } \{ \iota_{\perp} _ . v.v1 \mid \iota_{\top} v. \ell := v ; v.v1 \} \rrbracket^c = \text{return } v.v1$$

By assumption, $\varphi_{\vec{u}}(v.v1) = \perp$. Loading the same message v , we have $\tau \in \llbracket \text{rmw}_{\varphi}(\ell; \vec{u}) \rrbracket^c$. We obtain the desired trace from binding it with $\kappa \langle \mu, \mu \rangle \kappa \therefore v.v1 \in \text{return } v.v1$.

RMW. If $\tau \in \llbracket \text{rmw}_{\ell, \psi_{\vec{w}}}^{\text{RMW}} \rrbracket_{\mathcal{G}}$, then we have $\tau = \kappa \langle \mu, \mu \uplus \{ \ell : v @ (v.t, t) \langle \kappa[\ell \mapsto t] \rangle \} \rangle \kappa[\ell \mapsto t] \therefore v.v1$ where $\psi_{\vec{w}}(v.v1) = v$, $v \in \mu_{\ell}$, and $v.t = \kappa_{\ell}$. Structurally, we have

$$\llbracket \text{match } \psi_{\vec{w}}(v.v1) \text{ with } \{ \iota_{\perp} _ . v.v1 \mid \iota_{\top} v. \ell := v ; v.v1 \} \rrbracket^c = \llbracket \ell := v ; v.v1 \rrbracket^c$$

Loading the same message v , we proceed depending on $\varphi_{\vec{u}}(v.v1)$.

$\varphi_{\vec{u}}(v.v1) = \perp$. We can bind $\kappa \langle \mu, \mu \rangle \kappa \therefore v.v1 \in \llbracket \text{rmw}_{\varphi}(\ell; \vec{u}) \rrbracket^c$, with $\tau \in \llbracket \ell := v ; v.v1 \rrbracket^c$.

Table 3. Components filling roles in the definition of rewrite rules, using the notations of Table 1.

rule	source		target	
	'er	'ee	'er	'ee
loosen		ϵ		v
expel	$\epsilon_1^{v.i}$		ϵ	v
condense	v	ϵ	$v [\uparrow\epsilon]$	
stutter				$\langle \mu, \mu \rangle$
mumble	$\langle \mu, \rho \rangle$	$\langle \rho, \theta \rangle$	$\langle \mu, \theta \rangle$	
tighten		v		ϵ
absorb	ϵ	v	$\epsilon_1^{v.i}$	
dilute	$v [\uparrow\epsilon]$		v	e

$\varphi_{\bar{u}}(v.v1) \neq \perp$. Then we have $\kappa \langle \mu, \rho \rangle \kappa[\ell \mapsto \frac{\kappa_{\ell+t}}{2}] \therefore v.v1 \in \llbracket \mathbf{rmw}_{\varphi}(\ell; \bar{u}) \rrbracket^c$, where $\rho := \mu \uplus \{ \ell: \varphi_{\bar{u}}(v.v1) @ (v.t, \frac{\kappa_{\ell+t}}{2}) \llbracket \kappa[\ell \mapsto \frac{\kappa_{\ell+t}}{2}] \rrbracket \}$. We can bind it with

$$\kappa[\ell \mapsto \frac{\kappa_{\ell+t}}{2}] \llbracket \langle \rho, \rho \uplus \{ \ell: v @ (\frac{\kappa_{\ell+t}}{2}, t) \llbracket \kappa[\ell \mapsto t] \rrbracket \} \rrbracket \kappa[\ell \mapsto t] \therefore v.v1 \in \llbracket \ell := v ; v.v1 \rrbracket^c$$

where at the end we absorb (Ab) the first message into the second. \square

F Proof of Rewrite Commutativity

In this section we prove **Rewrite Commutativity**. We make a few observations to help us navigate the elaborate case-split that makes up the proof.

Active roles in rewrite rules. Intuitively, the \mathfrak{g} - and \mathfrak{a} -rules have an object message that is acted upon, and sometimes a subject message that partakes in the action. For example, in the absorb rule there is a message, which we call the absorb'ee, that is being "absorbed" into another, which we call the absorb'er. We think of the absorb'er as a message that changed, rather than two different messages. In stutter and mumble the active components are transitions rather than messages.

Table 3 lists which components of the source and target of each rewrite rule fill the subject and object roles. We use these roles to distinguish scenarios within each commutativity case in the proof of **Rewrite Commutativity**. The roles for forward and rewind are omitted because there is no need to analyze different scenarios within the cases involving them in the proof.

Conditions for rewrite validity. As we introduced the rewrite rules in §6, we noted conditions that imply that the target of a rewrite is a trace, assuming the source is. We summarize these below:

LEMMA F.1. For $x \in \mathfrak{gc}$, assume τ is a trace and $\tau \xrightarrow{x} \pi$. Then, using the notations of Table 1:

- If $x = \text{Mu}$, then $\pi \in \text{Trace}$.
- If $x = \text{Ls}$, then $\pi \in \text{Trace}$ iff $\text{Ls}^{\vee}(v, \eta)$: either η is empty, or $v \hookrightarrow (\eta \bar{\cup} \{v\}) \cdot o$.
- If $x = \text{Ex}$, then $\pi \in \text{Trace}$ iff $\text{Ex}^{\vee}(v, \eta)$: either η is empty, or $v \hookrightarrow (\eta \bar{\cup} \{v\}) \cdot o$.
- If $x = \text{Cn}$, then $\pi \in \text{Trace}$ iff $\text{Cn}^{\vee}(\epsilon, \xi)$: either ξ is empty, $\epsilon.i \notin \xi \cdot \text{c.t}$, or $\epsilon \cdot \text{seg} \cap \xi \cdot \text{c.seg} = \emptyset$.
- If $x = \text{St}$, then $\pi \in \text{Trace}$ iff $\text{St}^{\vee}(\alpha, \mu)$: $\alpha \succ \mu \in \text{Mem}$.
- If $x = \text{Fw}$, then $\pi \in \text{Trace}$ iff $\text{Fw}^{\vee}(\omega, \xi)$: $\omega \hookrightarrow \xi \cdot c$.
- If $x = \text{Rw}$, then $\pi \in \text{Trace}$ iff $\text{Rw}^{\vee}(\alpha, \xi)$: $\alpha \hookrightarrow \xi \cdot o$.

In each case in the proof, the rewrite sequence after commuting includes a new pre-trace. We must show that this is a trace for the sequence to be valid, because **Rewrite Commutativity** regards the restriction of the rewrite rules to traces. Lemma F.1 is the workhorse that powers this task.

Table 4. Diagrams for different scenarios of each case of $x \sqsubseteq y$.

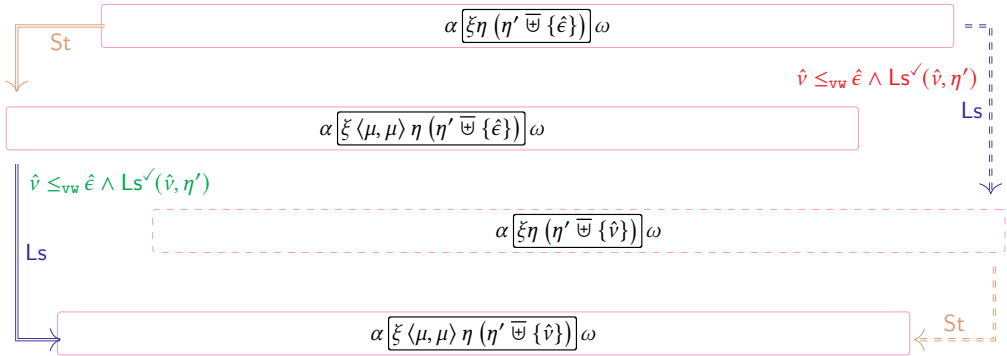
$y \setminus x$	St	Mu	Fw	Rw	Ti	Ab	Di
Ls	1, 2	7, 8	13	14	19, 20	26, 27	33, 34, 35
Ex	3, 4	9, 10	15	16	21, 22	28, 29	36, 37, 38
Cn	5, 6	11, 12	17	18	23, 24, 25	30, 31, 32	39, 40, 41, 42
St					43, 44	45, 46	47, 48
Mu					49, 50, 51, 52	53, 54, 55, 56	57, 58, 59, 60
Fw					61	63	65
Rw					62	64	66

PROOF OF REWRITE COMMUTATIVITY. Diagrams attached below depict how the rewrite rules commute in different scenarios. We summarize the reasoning involved below. Use [Table 4](#) to navigate through the cases.

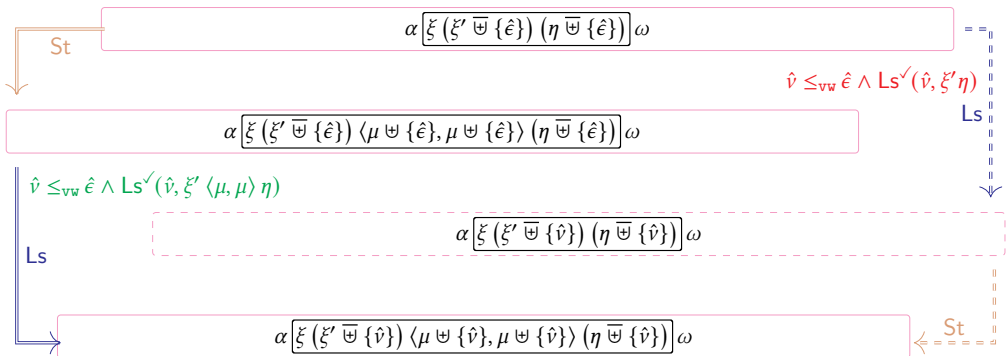
- For cases of $St \sqsubseteq y$ where $y \in \mathfrak{g}$ (1, 2, 3, 4, 5, 6), the required condition is about the same chronicle as the assumed condition, except for possibly a removed transition. This means that its opening memory is an extension of the original (result of adding messages), and the closing memory is a reduction of the original (result of removing messages). The condition of pointing downwards into a memory is stable under extensions, and the condition of non-intersection is stable under reductions. Cases of $Mu \sqsubseteq y$ where $y \in \mathfrak{g}$ (7, 8, 9, 10, 11, 12) are simpler because the opening and closing memory remain the same.
- The cases of $Fw \sqsubseteq y$ and $Rw \sqsubseteq y$ where $y \in \mathfrak{g}$ (13, 14, 15, 16, 17, 18) are trivial because the required condition remains the same.
- For cases of $Ti \sqsubseteq y$ where $y \in \mathfrak{g}$, the required condition in the cases of $y \in \{Ls, Ex\}$ (19, 20, 21, 22) holds because pointing downwards into a memory is stable under “loosening” a message within the memory ($v \leq \epsilon$). The remaining $y = Cn$ case (23, 24, 25) holds because the difference between the required condition and the original keeps the occupied timestamps the same, and the \sqsubseteq relation is stable under “loosening” the first argument.
- For cases of $Ab \sqsubseteq y$ where $y \in \mathfrak{g}$, the required condition in the cases of $y \in \{Ls, Ex\}$ (26, 27, 28, 29) holds because pointing downwards into a memory is stable under changing a message’s initial timestamp and adding a message within the memory. The remaining $y = Cn$ case (30, 31, 32) holds because the difference between the required condition and the original keeps the occupied timestamps the same, and the \sqsubseteq relation is stable under changing the initial timestamp of the first argument.
- Cases of $Di \sqsubseteq y$ where $y \in \mathfrak{g}$ hold thanks to [Lemma 6.5](#) when $y \in \{Ls, Ex\}$ (33, 34, 35, 36, 37, 38). The remaining $y = Cn$ case (39, 40, 41, 42) is the most complicated. First, we note that $(- [\uparrow \epsilon]) [\uparrow \hat{\epsilon} [\uparrow \epsilon]] = (- [\uparrow \hat{\epsilon}]) [\uparrow \epsilon [\uparrow \hat{\epsilon}]]$, which means that the pre-trace to be “diluted” is of the correct shape. The rewrite itself is valid because \sqsubseteq is stable under changing the timestamp of the second argument. In particular, it is stable under pulling both arguments along the same message which does not intersect the arguments’ segments. Finally, the condition for the pre-trace to be a trace is satisfied because the message is being pulled along a message that was either removed or known to appear later in the chronicle (as a local message); either way, the segment is free. There are also the cases where they overlap this way or that, in which we use the trivial dovetailing geometry of \sqsubseteq .
- For cases of $x \sqsubseteq St$ where $x \in \mathfrak{a}$, the required condition in the cases of $x \in \{Ti, Ab\}$ (43, 44, 45, 46) holds because there remains a message at each timestamp where there was a message

originally, and the initial view remains the same. The remaining $y = \text{Di}$ case (47, 48) holds because pointing-to is stable under pulling along a message; and if the initial view pointed to the dilute'ee then after pulling it, it will point to the dilute'er pulled along the dilute'ee.

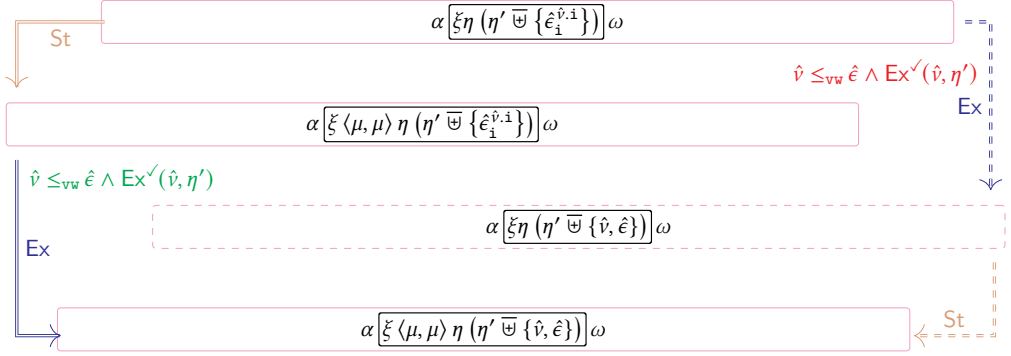
- The cases of $x \rightleftharpoons \text{Mu}$ where $x \in \mathfrak{a}$ (49, 50, 51, 52, 53, 54, 55, 56, 57, 58, 59, 60) do not require special considerations regarding conditions.
- For cases of $x \rightleftharpoons y$ where $x \in \mathfrak{a}$ and $y \in \{\text{Fw}, \text{Rw}\}$, the required condition in the cases of $x \in \{\text{Ti}, \text{Ab}\}$ (61, 62, 63, 64) holds because pointing downwards into a memory is stable under “loosening” a message within the memory ($v \leq \epsilon$). The case of $x \in \text{Di}$ (65, 66) hold thanks to Lemma 6.5, and the fact that pointing downward into a memory is stable under pulling along the same message; and if the delimiting view pointed to the dilute'ee then after pulling it, it will point to the dilute'er pulled along the dilute'ee. \square



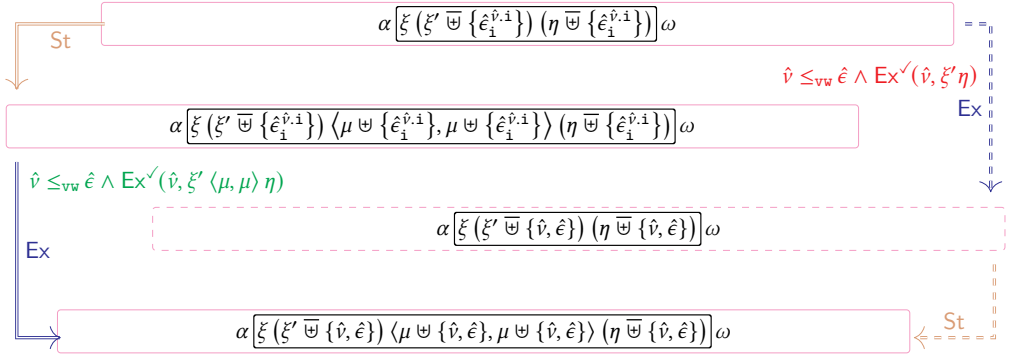
1. The $\text{St} \rightleftharpoons \text{Ls}$ case when the loosen'ee does not appear across the stutter'ee.



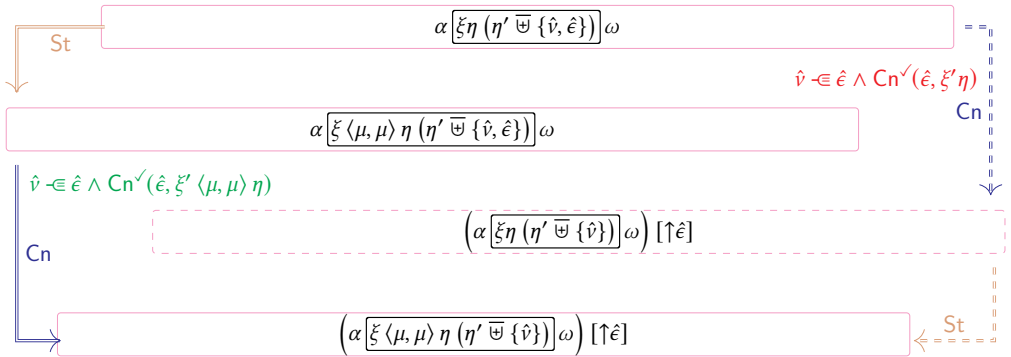
2. The $\text{St} \rightleftharpoons \text{Ls}$ case when the loosen'ee appears across the stutter'ee.



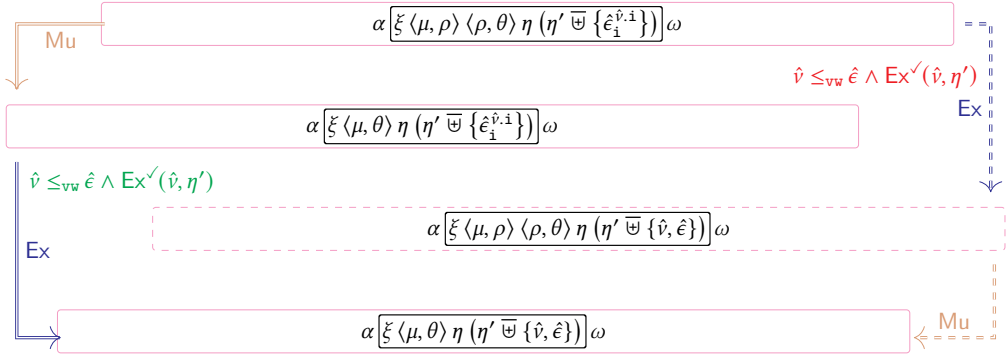
3. The St ⇔ Ex case when the expel'ee does not appear across the stutter'ee.



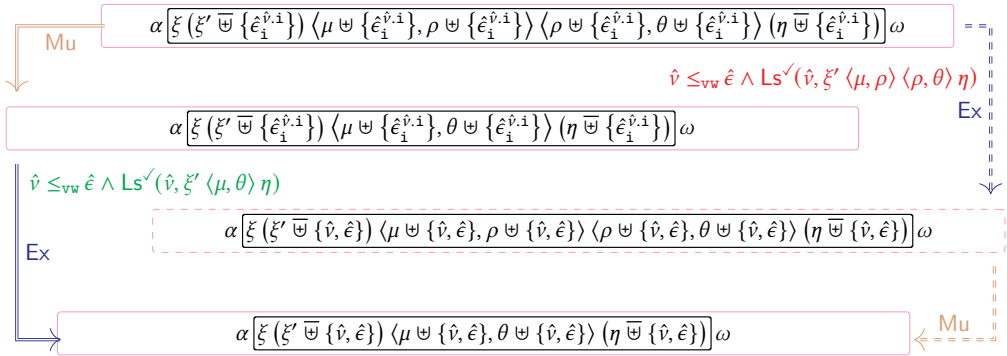
4. The St ⇔ Ex case when the expel'ee appears across the stutter'ee.



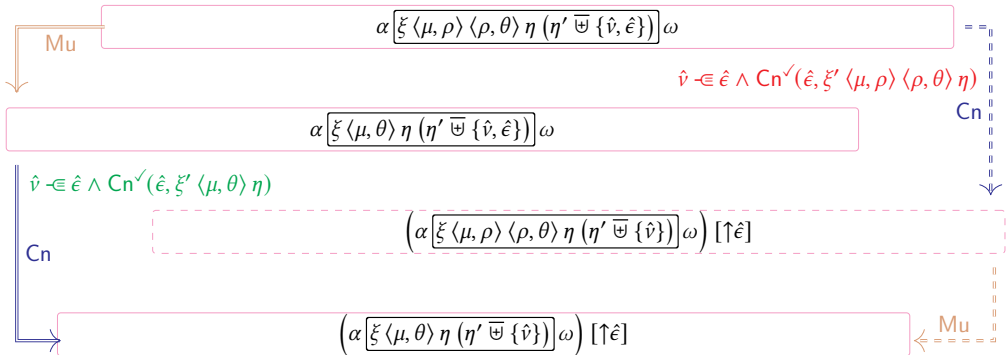
5. The St ⇔ Cn case when the condense'ee does not appear across the stutter'ee.



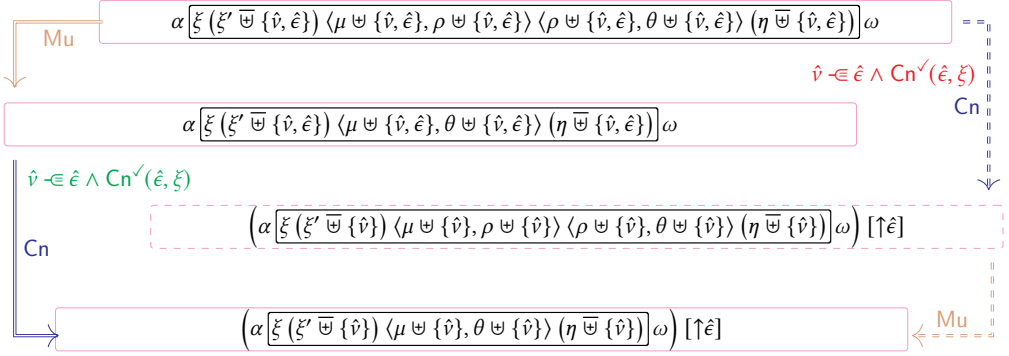
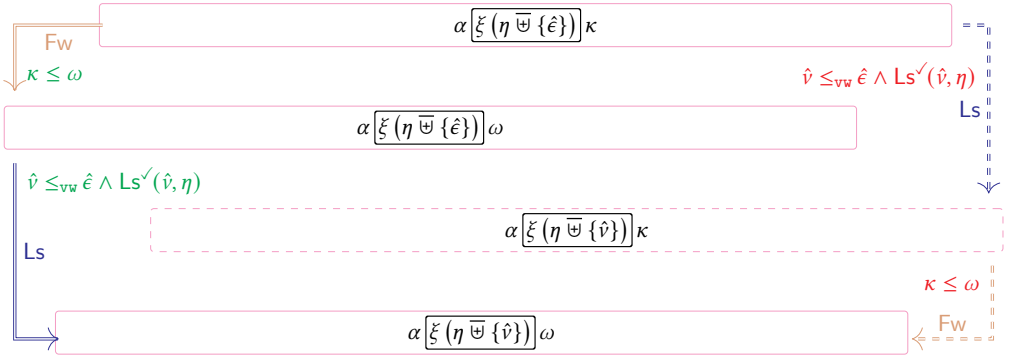
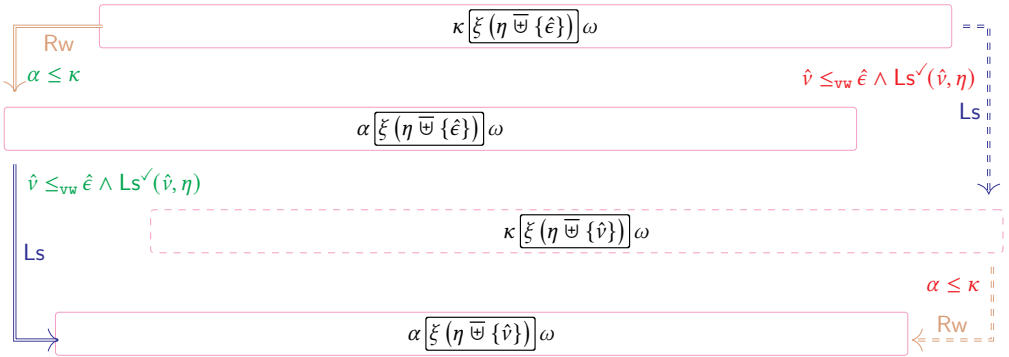
9. The $\text{Mu} \rightleftharpoons \text{Ex}$ case when the expel'ee does not appear across the mumble'er.

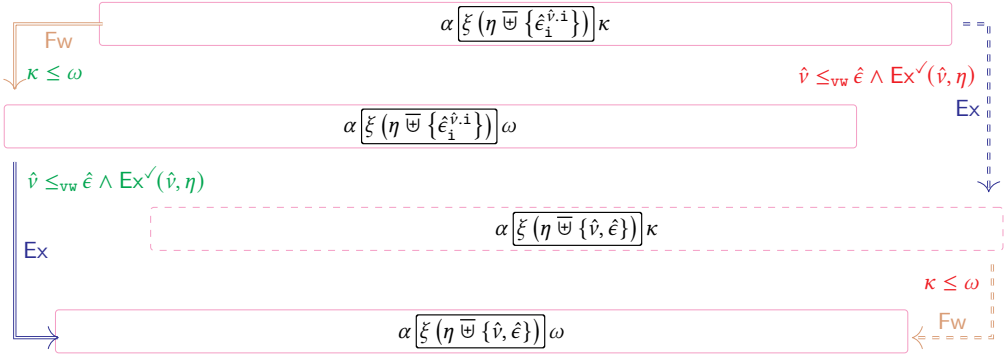
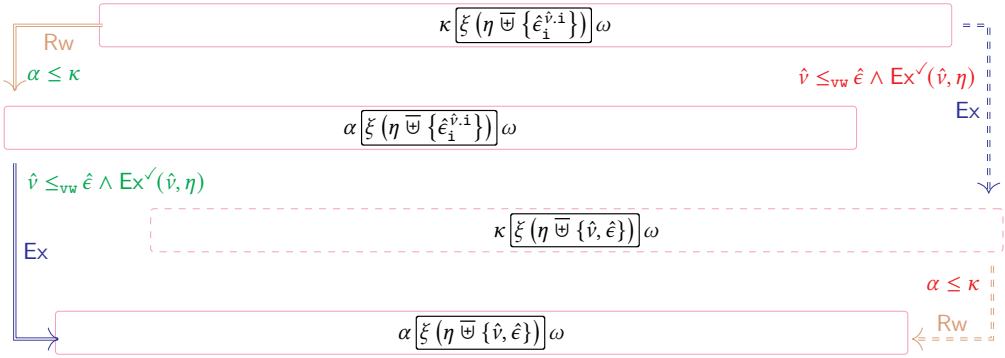
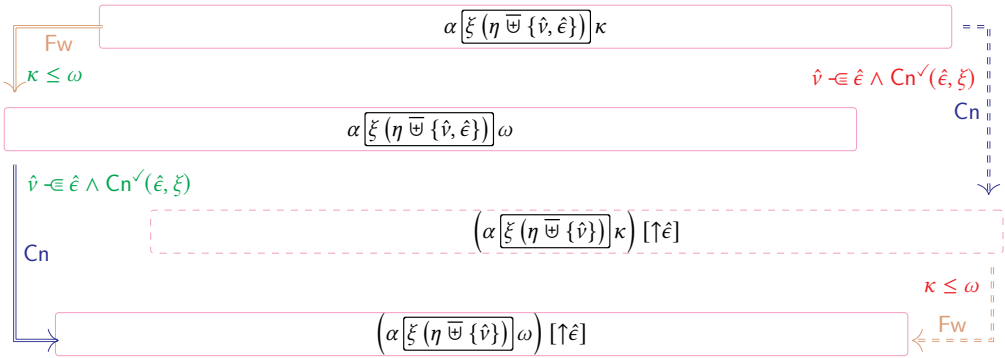


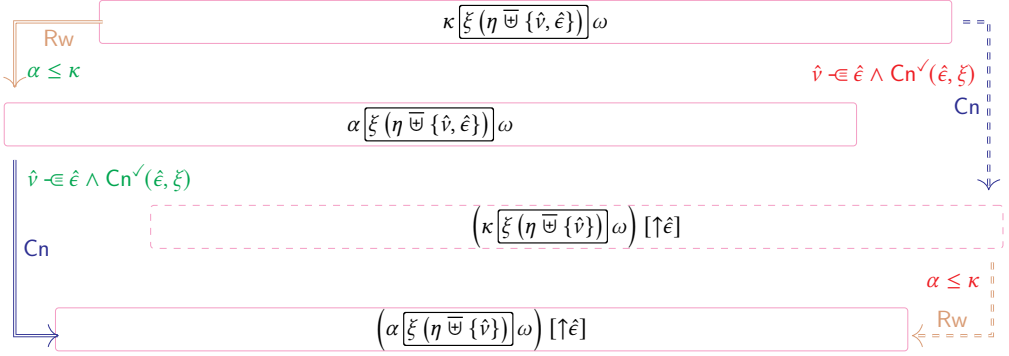
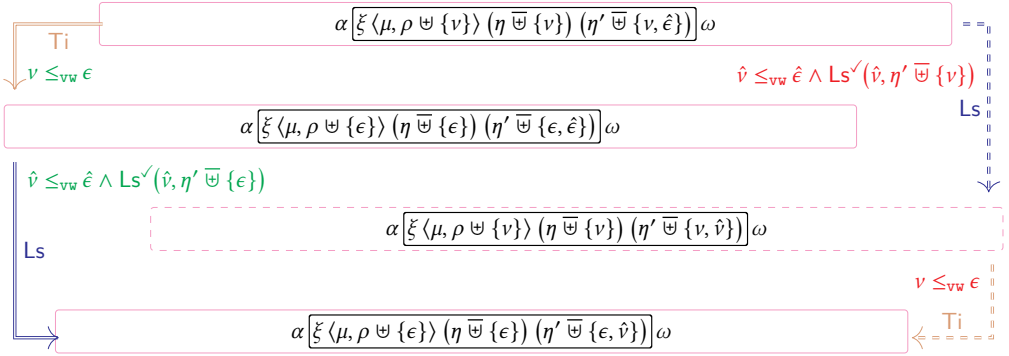
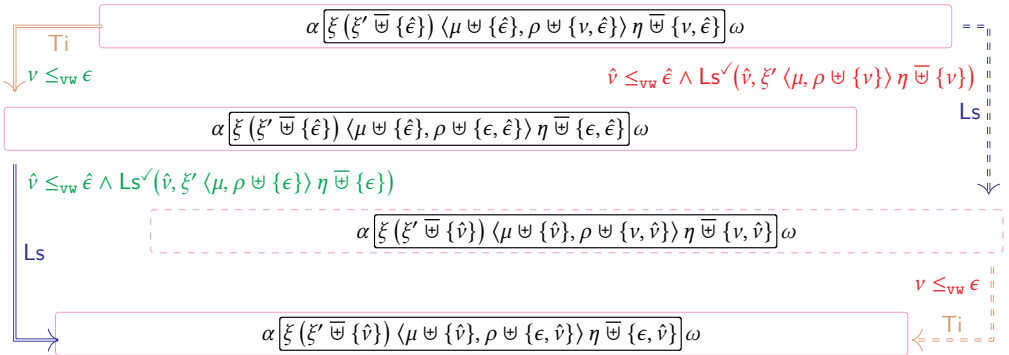
10. The $\text{Mu} \rightleftharpoons \text{Ex}$ case when the expel'ee appears across the mumble'er.

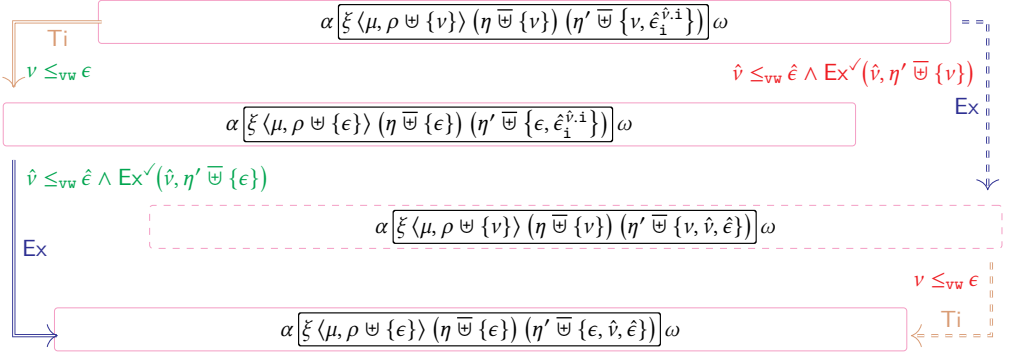


11. The $\text{Mu} \rightleftharpoons \text{Cn}$ case when the condense'ee does not appear across the mumble'er.

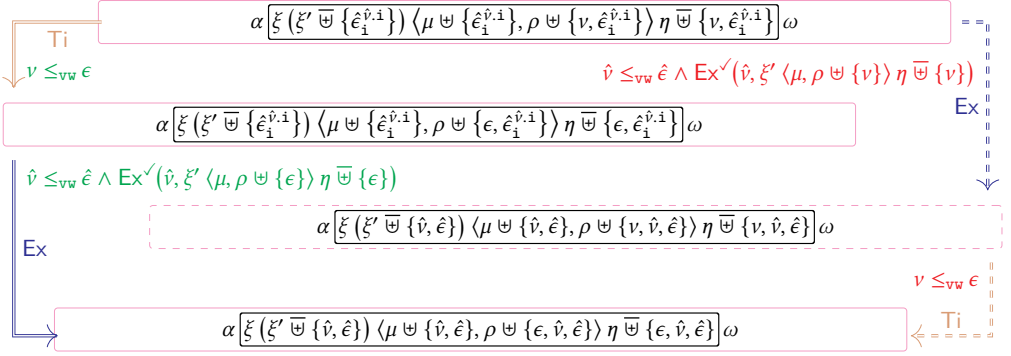
12. The $\text{Mu} \cong \text{Cn}$ case when the condense'ee appears across the mumble'er.13. The $\text{Fw} \cong \text{Ls}$ case.14. The $\text{Rw} \cong \text{Ls}$ case.

15. The Fw \Leftrightarrow Ex case.16. The Rw \Leftrightarrow Ex case.17. The Fw \Leftrightarrow Cn case.

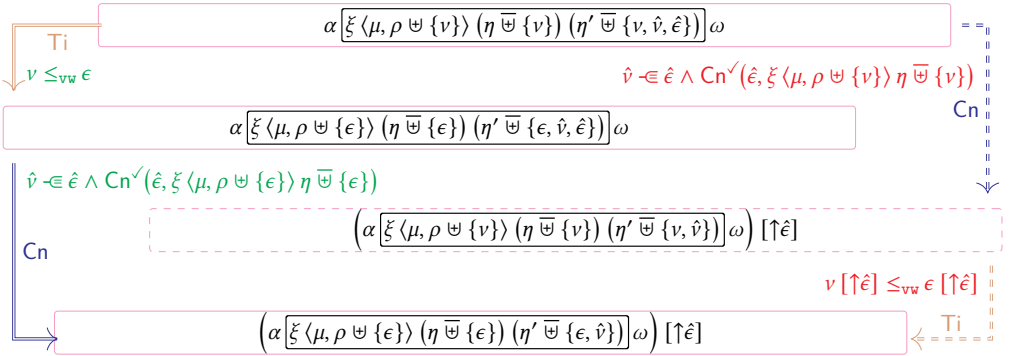
18. The $Rw \Leftrightarrow Cn$ case.19. The $Ti \Leftrightarrow Ls$ case when the loosen'ee appears first after the tighten'ee.20. The $Ti \Leftrightarrow Ls$ case when the loosen'ee appears first before the tighten'ee.



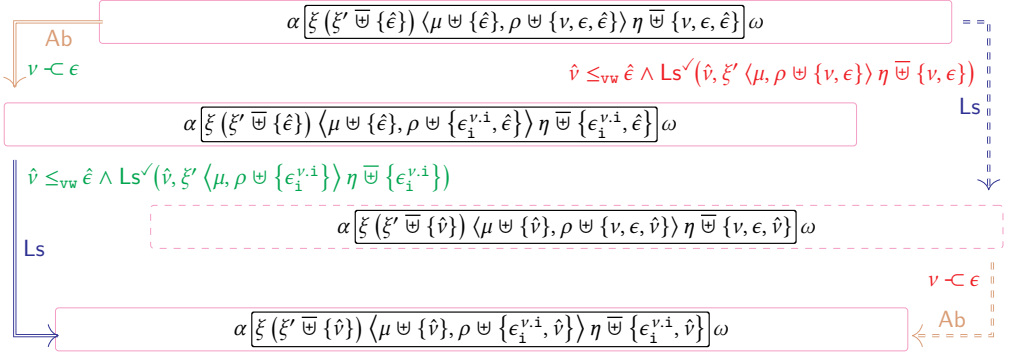
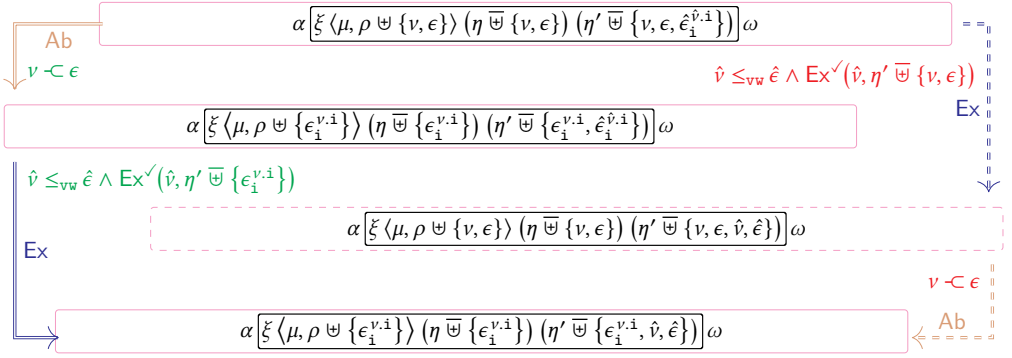
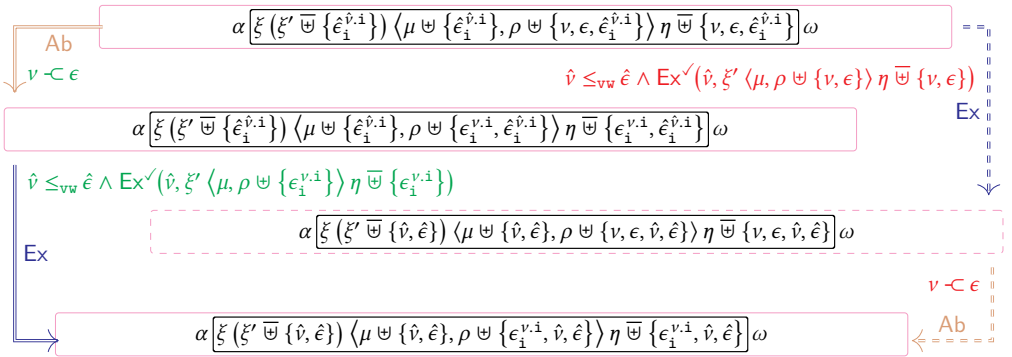
21. The Ti ⇔ Ex case when the expel'ee appears first after the tighten'ee.

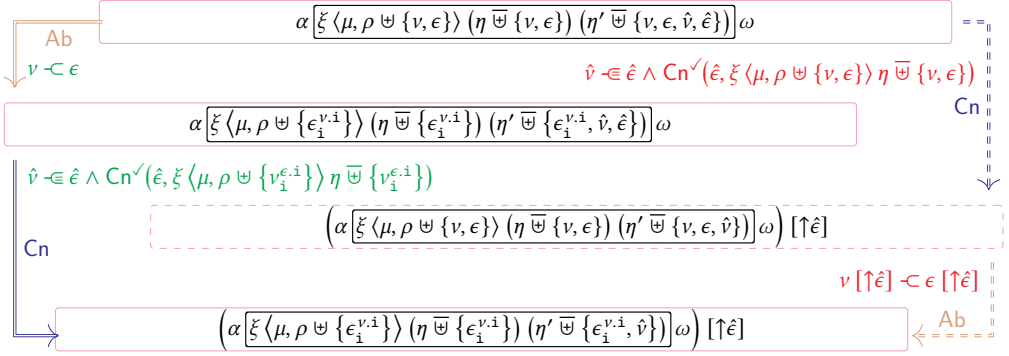


22. The Ti ⇔ Ex case when the expel'ee appears first before the tighten'ee.

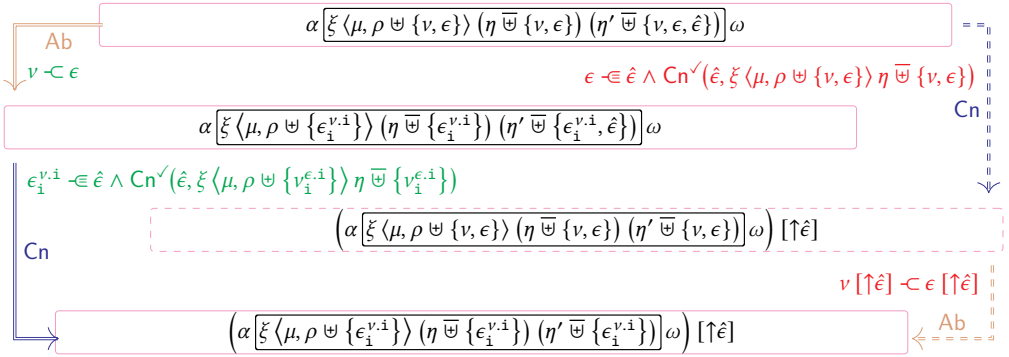


23. The Ti ⇔ Cn case when the condense'ee appears first after the tighten'ee, and the tighten'ee is not the condense'er.

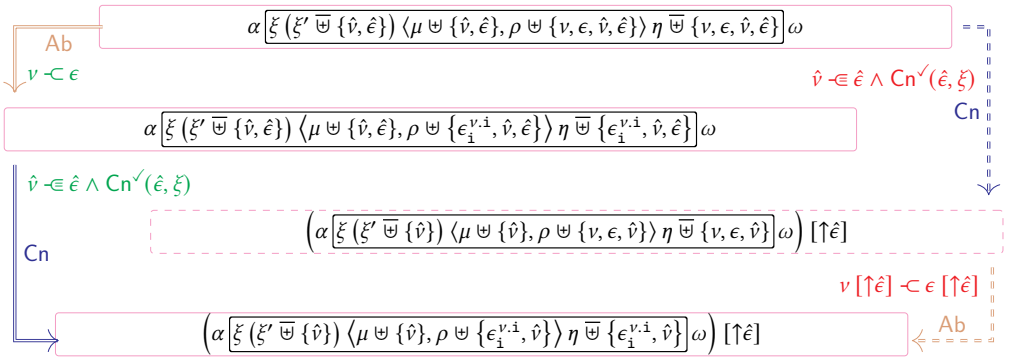
27. The Ab \Leftarrow Ls case when the loosen'ee appears first before the absorb'ee.28. The Ab \Leftarrow Ex case when the expel'ee appears first after the absorb'ee.29. The Ab \Leftarrow Ex case when the expel'ee appears first before the absorb'ee.



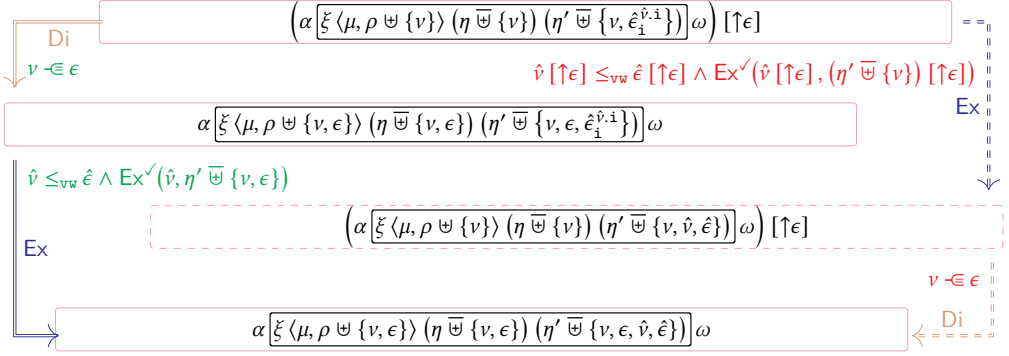
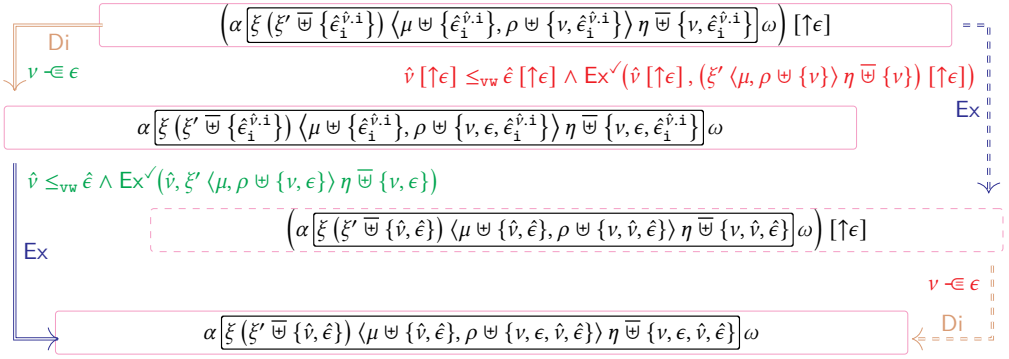
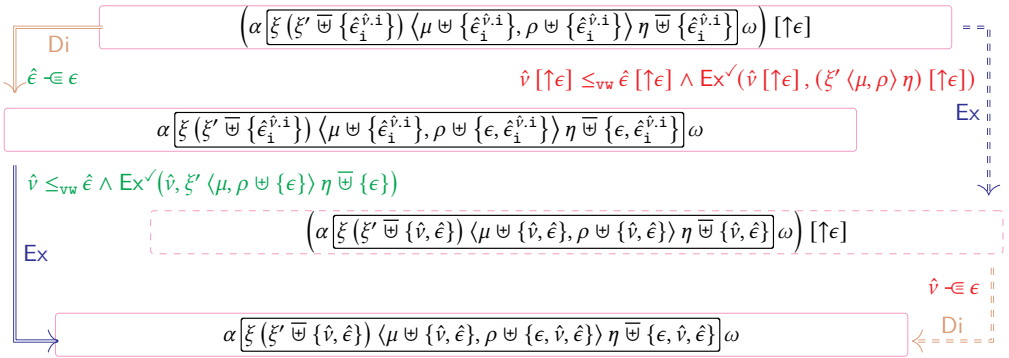
30. The Ab \simeq Cn case when the condense'ee appears first after the absorb'er, and the absorb'er is not the condense'er.

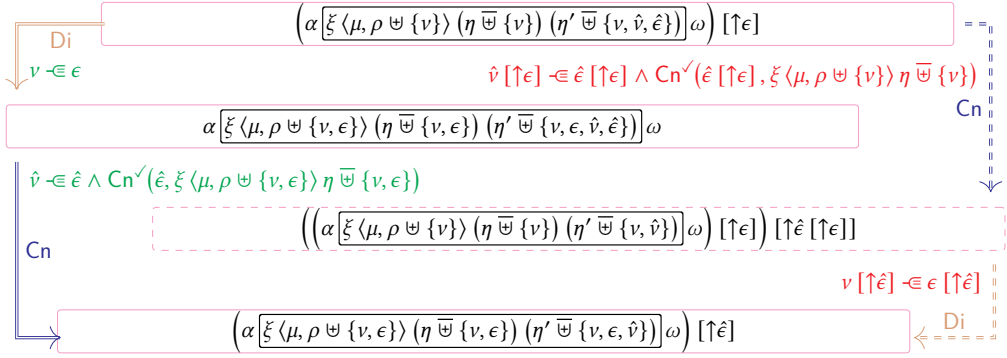


31. The Ab \simeq Cn case when the condense'ee appears first after the absorb'er, and the absorb'er is the condense'er.

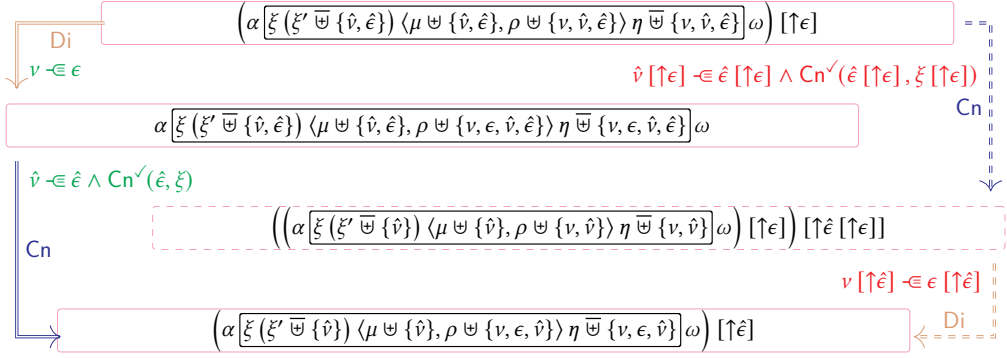


32. The Ab \simeq Cn case when the condense'ee appears first before the absorb'er.

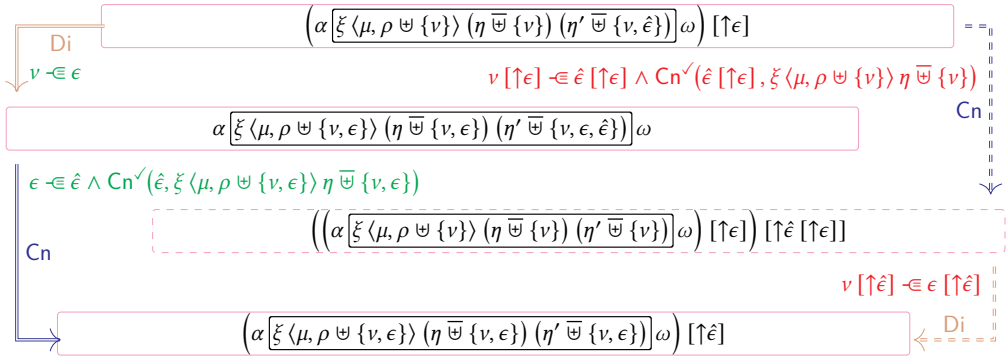
36. The Di \Leftrightarrow Ex case when the expel'er appears first after the dilute'ee.37. The Di \Leftrightarrow Ex case when the expel'er appears first before the dilute'ee, and the dilute'er is not the expel'er.38. The Di \Leftrightarrow Ex case when the expel'er appears first before the dilute'ee, and the dilute'er is the expel'er.



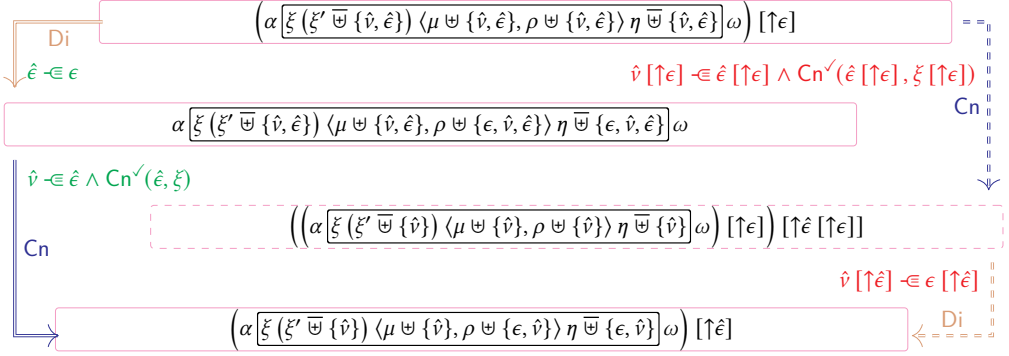
39. The $Di \Leftrightarrow Cn$ case when the condense'ee appears first after the dilute'ee, and the dilute'ee is not the condense'er.



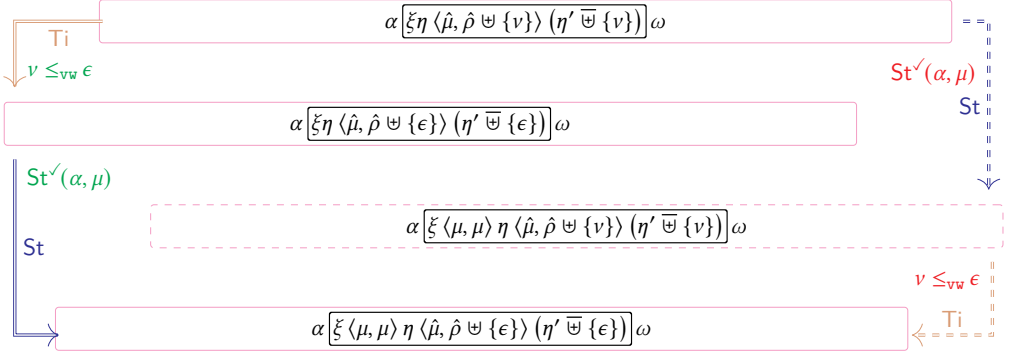
40. The $Di \Leftrightarrow Cn$ case when the condense'ee appears first before the dilute'ee, and the dilute'er is not the condense'ee.



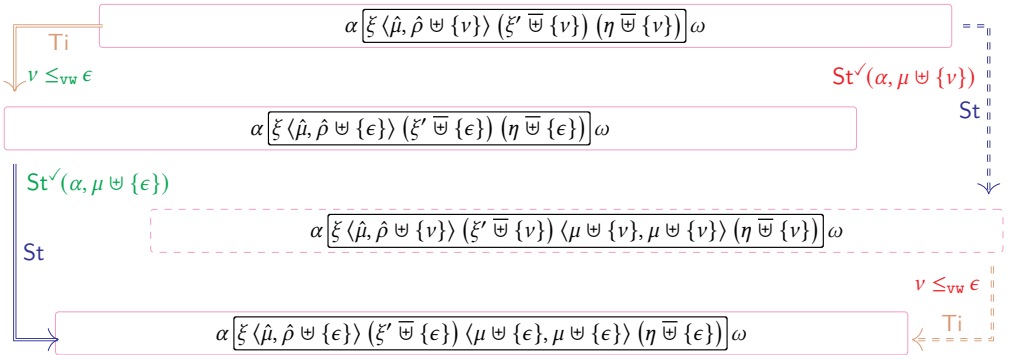
41. The $Di \Leftrightarrow Cn$ case when the condense'ee appears first after the dilute'ee, and the dilute'ee is the condense'er.



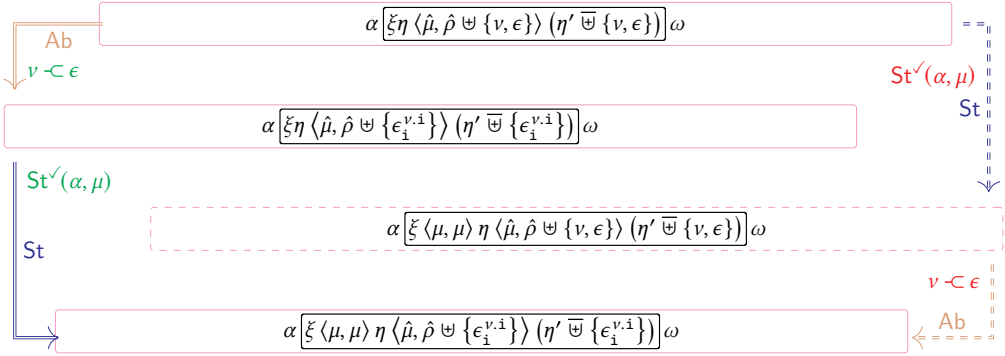
42. The $\text{Di} \preceq \text{Cn}$ case when the condense'ee appears first before the dilute'ee, and the dilute'er is the condense'ee.



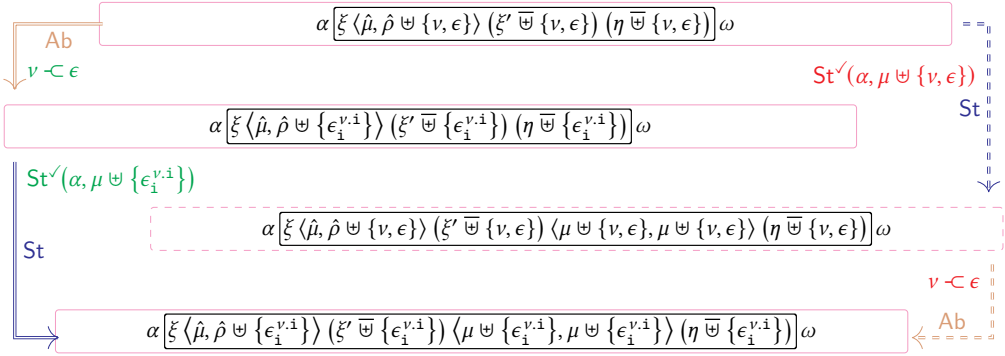
43. The $\text{Ti} \preceq \text{St}$ case when the tighten'ee does not appear across the stutter'ee.



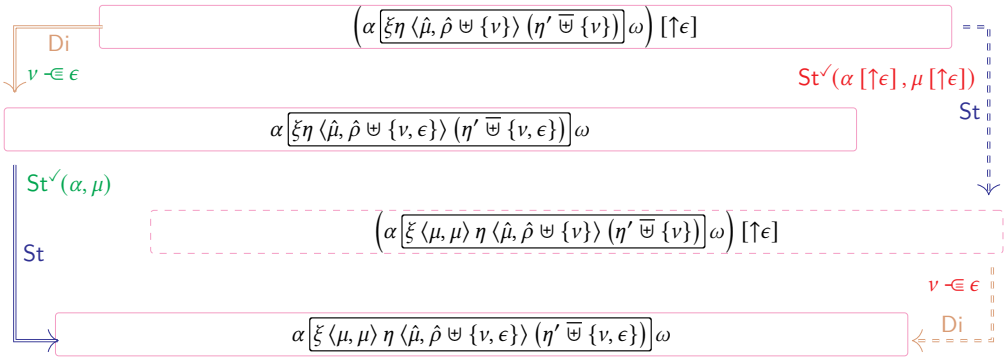
44. The $\text{Ti} \preceq \text{St}$ case when the tighten'ee appears across the stutter'ee.



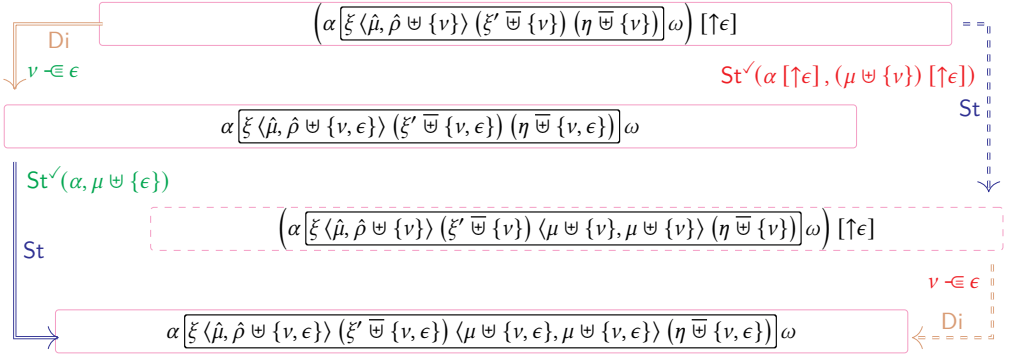
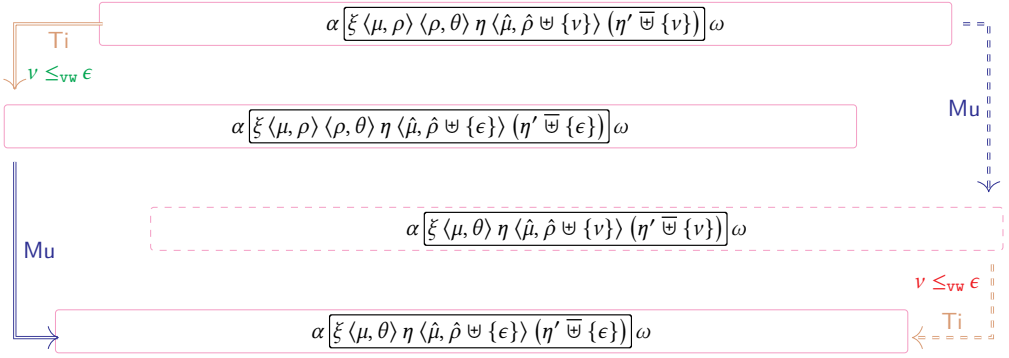
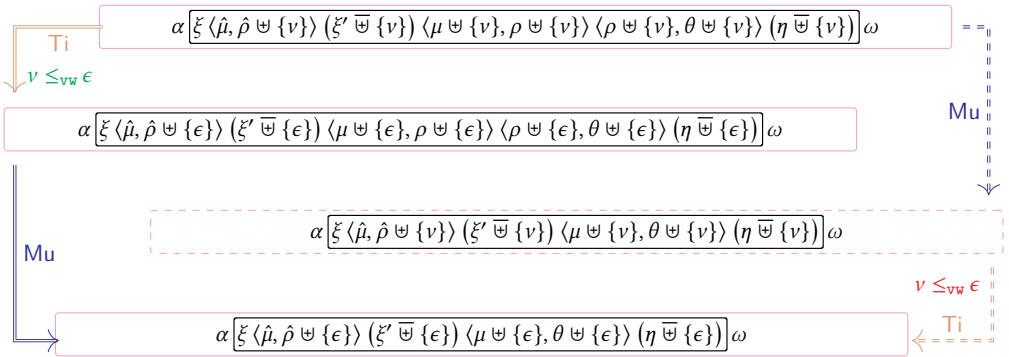
45. The Ab ⇔ St case when the absorb'ee does not appear across the stutter'ee.

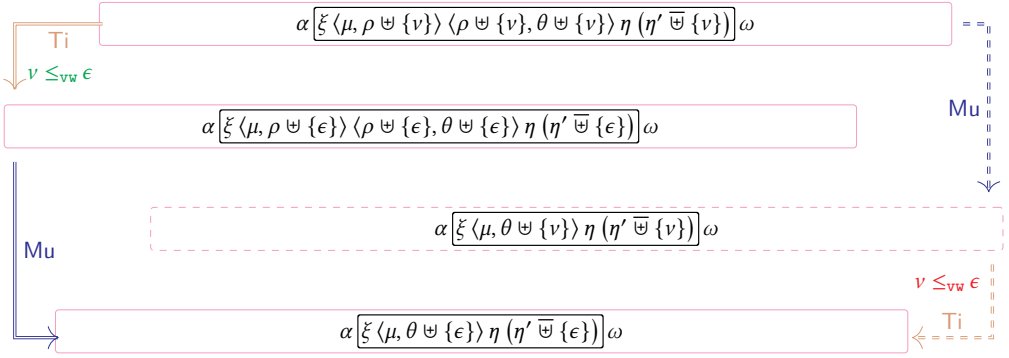
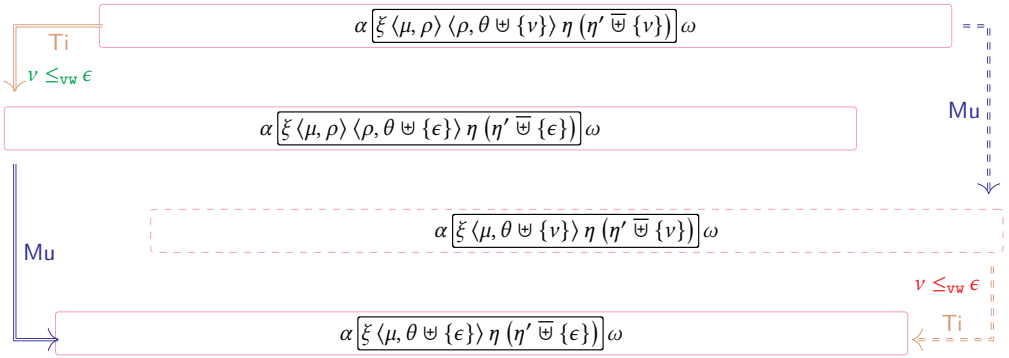
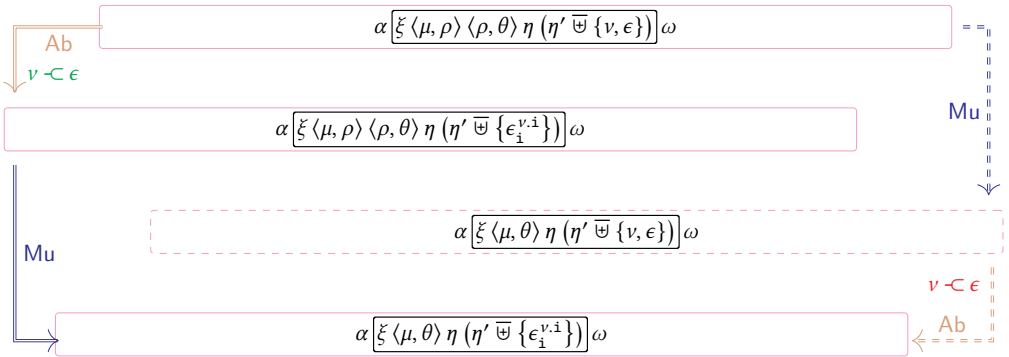


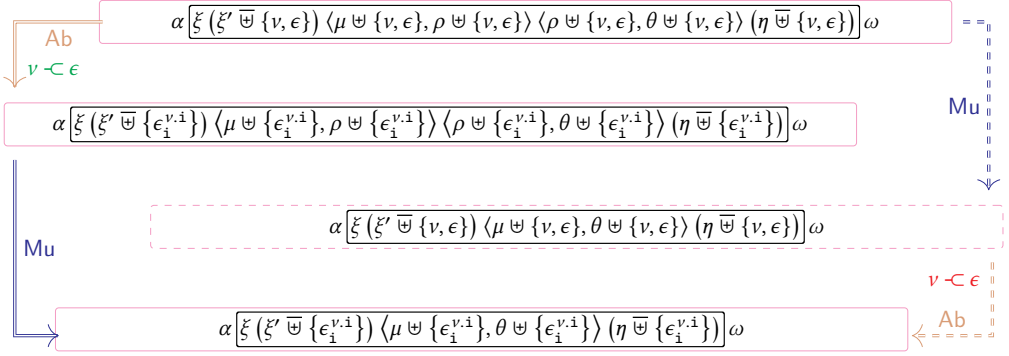
46. The Ab ⇔ St case when the absorb'ee appears across the stutter'ee.



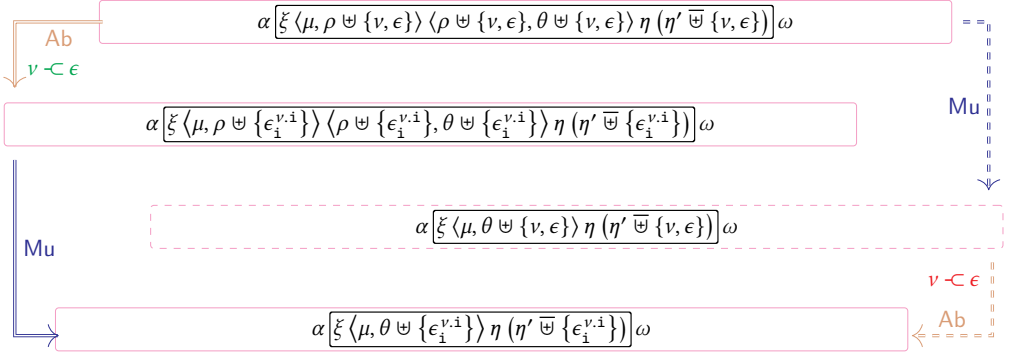
47. The Di ⇔ St case when the dilute'ee does not appear across the stutter'ee.

48. The Di \Leftrightarrow St case when the dilute'ee appears across the stutter'ee.49. The Ti \Leftrightarrow Mu case when the tighten'ee appears in neither the mumble'er nor the mumble'ee.50. The Ti \Leftrightarrow Mu case when the tighten'ee appears in both the mumble'er and the mumble'ee.

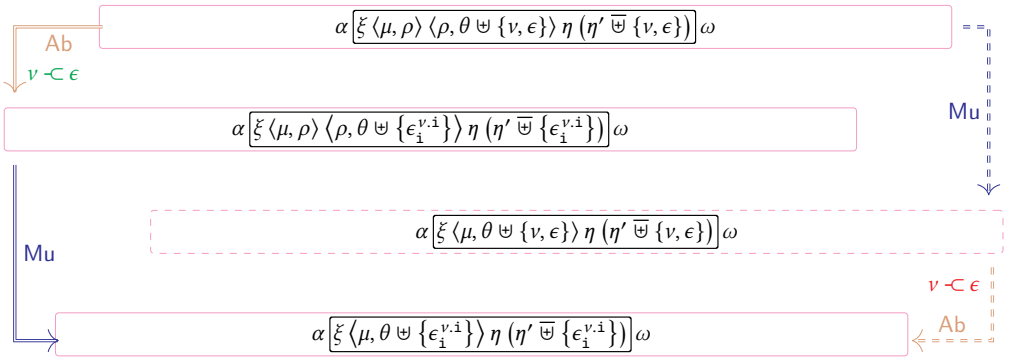
51. The $Ti \Leftrightarrow Mu$ case when the tighten'ee appears first in the mumble'er.52. The $Ti \Leftrightarrow Mu$ case when the tighten'ee appears first in the mumble'ee.53. The $Ab \Leftrightarrow Mu$ case when the absorb'ee appears in neither the mumble'er nor the mumble'ee.



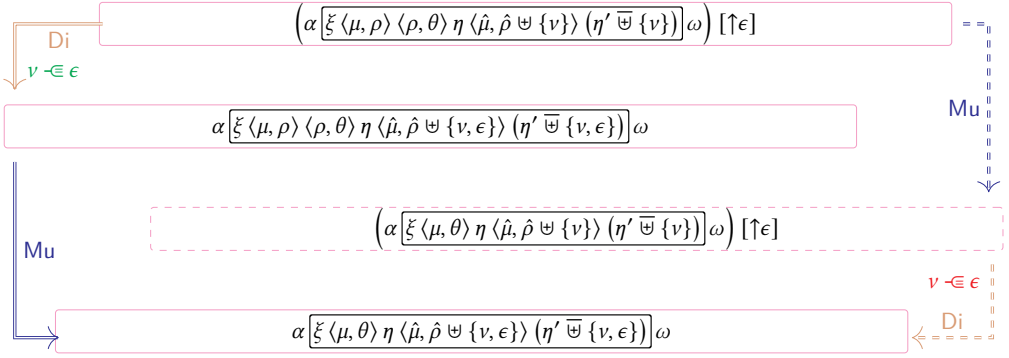
54. The Ab ≃ Mu case when the absorb'ee appears in both the mumble'er and the mumble'ee.



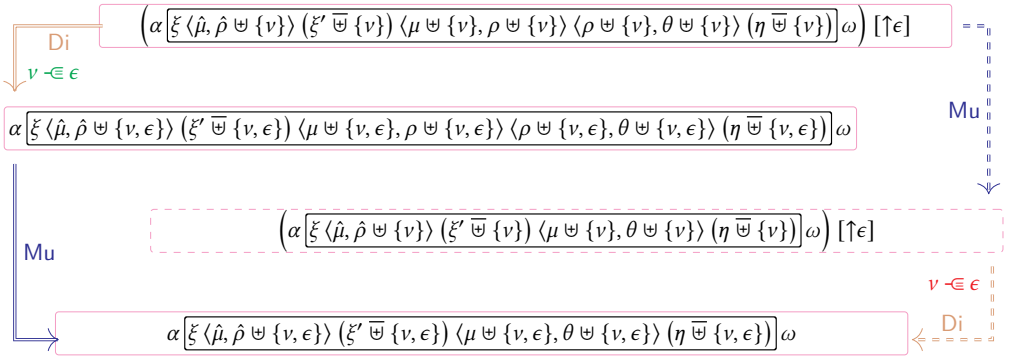
55. The Ab ≃ Mu case when the absorb'ee appears first in the mumble'er.



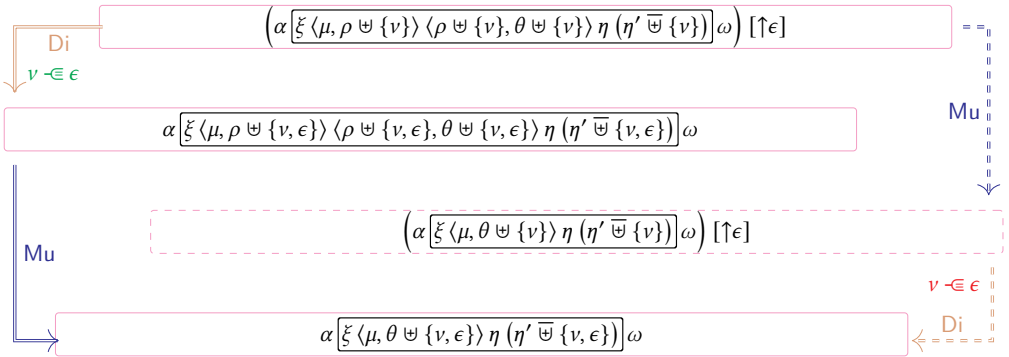
56. The Ab ≃ Mu case when the absorb'ee appears first in the mumble'ee.



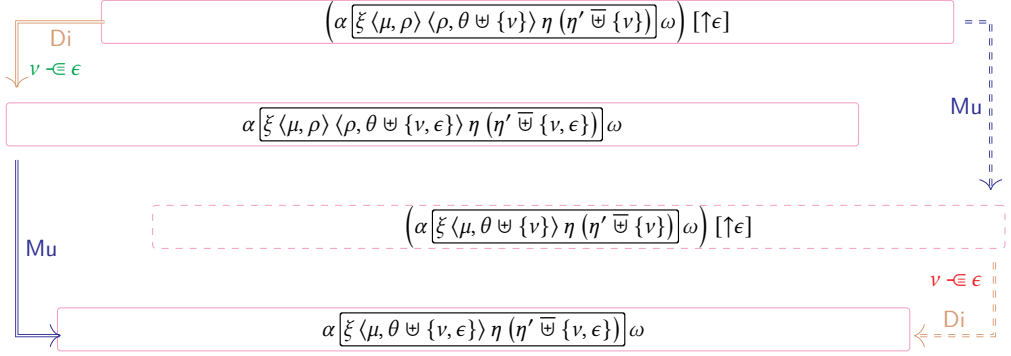
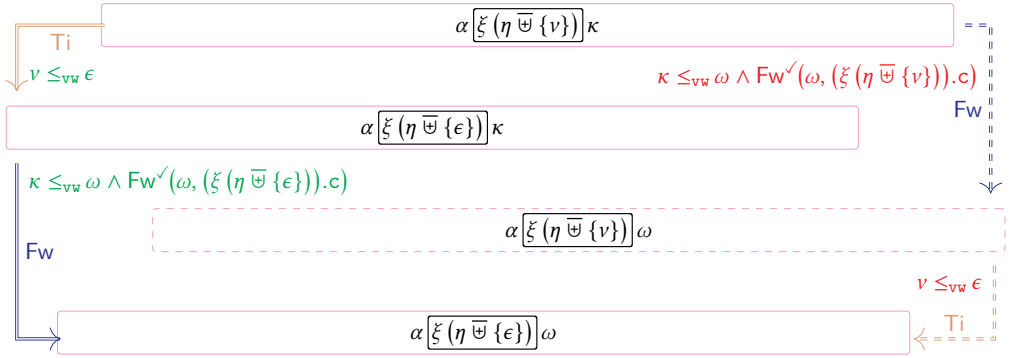
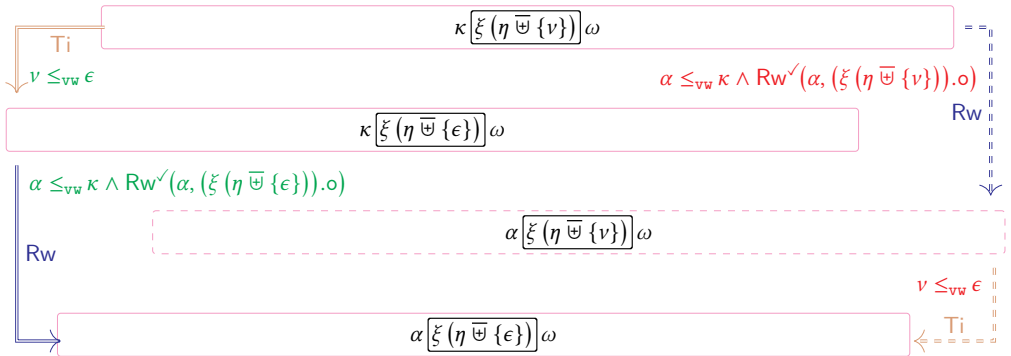
57. The $\text{Di} \rightleftharpoons \text{Mu}$ case when the dilute'ee appears in neither the mumble'er nor the mumble'ee.

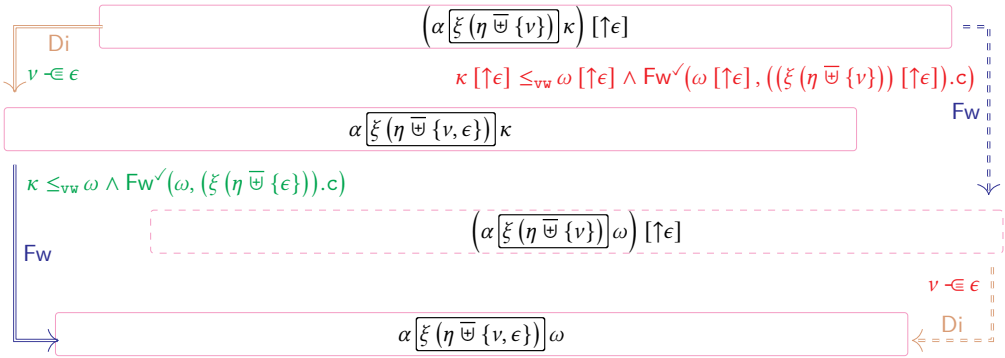
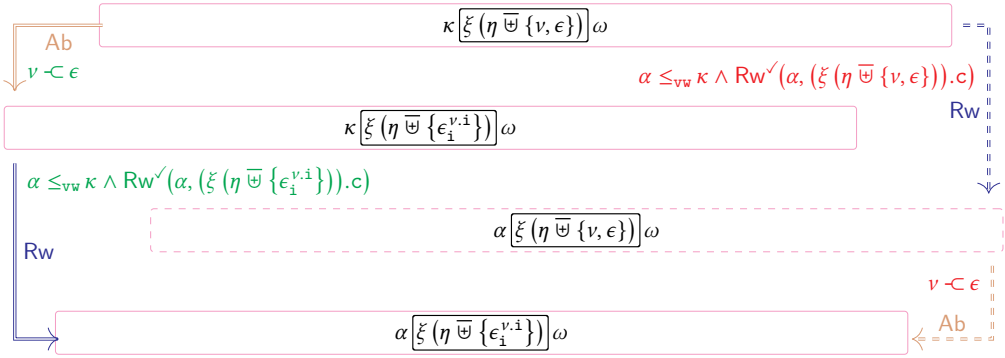
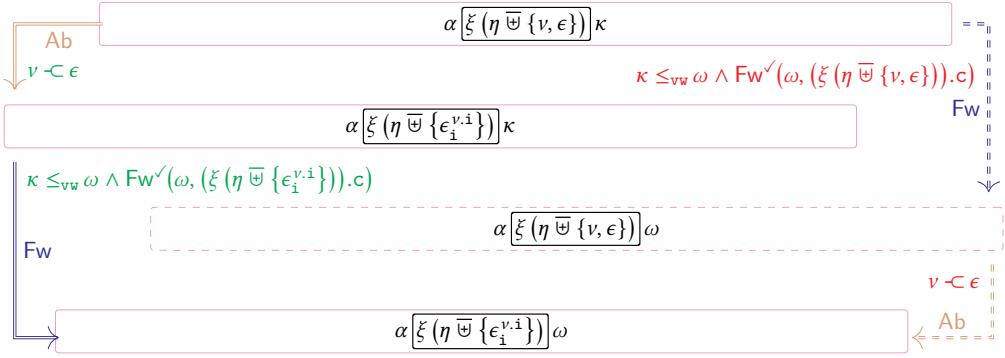


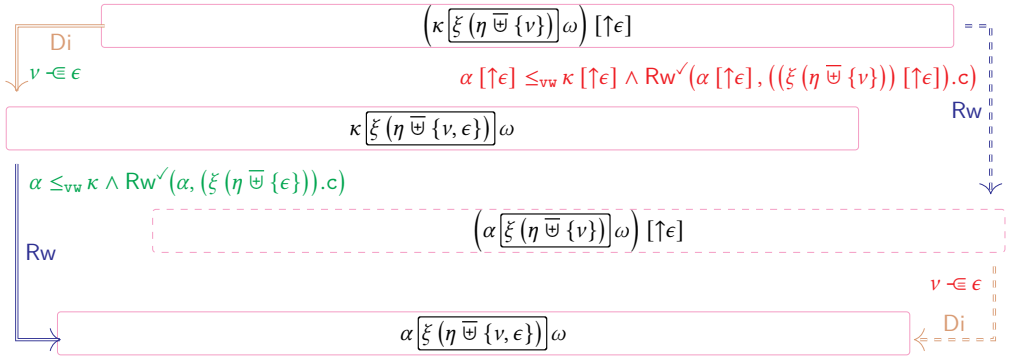
58. The $\text{Di} \rightleftharpoons \text{Mu}$ case when the dilute'ee appears in both the mumble'er and the mumble'ee.



59. The $\text{Di} \rightleftharpoons \text{Mu}$ case when the dilute'ee appears first in the mumble'er.

60. The Di \Leftrightarrow Mu case when the dilute'ee appears first in the mumble'ee.61. The Ti \Leftrightarrow Fw case.62. The Ti \Leftrightarrow Rw case.





66. The Di \Leftrightarrow Rw case.